TOSHIBA 1SV232

TOSHIBA VARIABLE CAPACITANCE DIODE SILICON EPITAXIAL PLANAR TYPE

1 S V 2 3 2

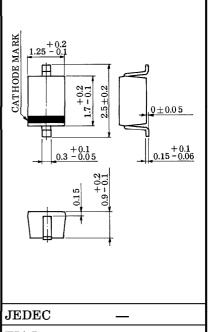
CATV TUNING.

Unit in mm

- High Capacitance Ratio : $C_{2V}/C_{25V} = 10.5$ (Typ.)
- Excellent C-V Characteristics, and Small Tracking Error.
- Useful for Small Size Tuner.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Reverse Voltage	v_{R}	30	V
Peak Reverse Voltage	v_{RM}	$(R_L = 10 \mathrm{k}\Omega)$	V
Junction Temperature	$T_{ m j}$	125	°C
Storage Temperature Range	$\mathrm{T_{stg}}$	-55~125	$^{\circ}\mathrm{C}$



JEDEC	_	
EIAJ	_	
TOSHIBA	1-1E1A	

Weight: 0.004g

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Reverse Voltage	v_{R}	$I_R = 1 \mu A$	30	_	_	V
Reverse Current	$I_{ m R}$	$V_R = 28V$		_	10	nA
Capacitance	C_{2V}	$V_R=2V$, f=1MHz	28	30.3	32	pF
Capacitance	c_{25V}	V_R =25V, f=1MHz	2.75	2.90	3.10	pF
Capacitance Ratio	C_{2V}/C_{25V}		10	10.5	_	_
Series Resistance	$r_{ m S}$	V_R =5V, f=470MHz	_	0.55	0.70	Ω

Note 1: Available in matched group for capacitance to 2.0%.

$$rac{ ext{C (Max.)} - ext{C (Min.)}}{ ext{C (Min.)}} \leq 0.02 \ (ext{V}_{ ext{R}} = 2 \sim 25 ext{V})$$

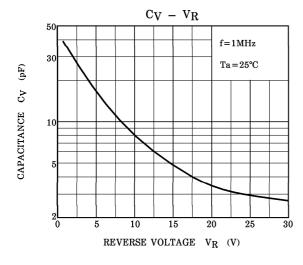
Marking

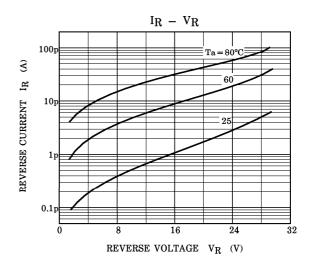


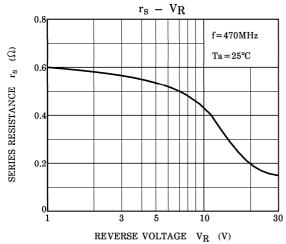
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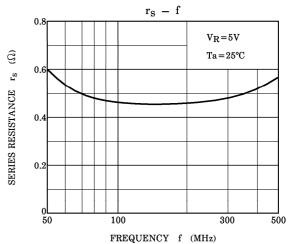
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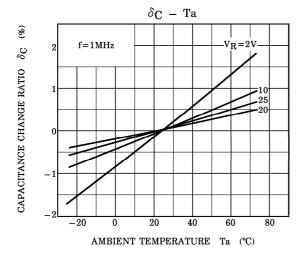
 The information contained herein is subject to change without notice.











NOTE :
$$\delta_{\text{C}}$$
 (%) = $\frac{\text{C (Ta)} - \text{C (25)}}{\text{C (25)}} \times 100$

SPICE PARAMETER

SPICE MODEL : BERKLEY SPICE.2G.6 DIODE MODEL

DATA FORMAT : MODEL FORMAT

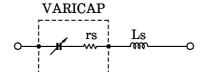
SPICE SYMBOL : $I_S(A)$, $R_S(\Omega)$, N(-), CJ0(F), $V_J(V)$, M(-), $B_V(V)$, $I_{BV}(A)$

FREQUENCY RANGE : $f = 0.1 \sim 3 \text{ GHz}$ REVERSE VOLTAGE RANGE : $V_R = 2 \sim 25 \text{ V}$

PARAMETER

Ls

= 1.00E - 09



- (Note 1): These parameters from IS to M mean die characteristic.

 Actually device has lead inductance so Ls is necessary for simulation.

 And please use default value except above parameters.
- (Note 2): Rs shows the value at the condition of $V_R = 5\,V$ and $f = 470\,MHz$. If another value is needed, please refer to Rs – V_R curve in this data sheets.