TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74ACT86P,TC74ACT86F,TC74ACT86FN

Quad Exclusive OR Gate

The TC74ACT86 is an advanced high speed CMOS QUAD EXCLUSIVE OR GATE fabricated with silicon gate and double-layer metal wiring C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

This device may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels. The internal circuit is includes on output buffer, which provide high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

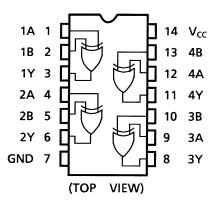
Features

- High speed: $t_{pd} = 5.0 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $ICC = 4 \mu A \text{ (max)}$ at $Ta = 25^{\circ}C$
- Compatible with TTL outputs: VIL = 0.8 V (max)

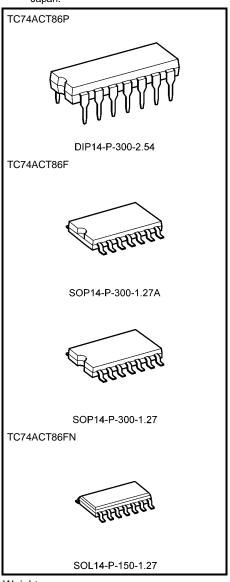
 $V_{IH} = 2.0 \text{ V (min)}$

- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 24$ mA (min) Capability of driving 50 Ω transmission lines.
 - Balanced propagation delays: $t_pLH \simeq t_pHL$
- · Pin and function compatible with 74F86

Pin Assignment



Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.) SOP14-P-300-1.27 : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.)

IEC Logic Symbol

	/41		_
1A	(1)	= 1	(3) 1Y
1B	(4)		
2A	(5)		(6) 2Y
2B	(9)		
3A	(10)		(8)3Y
3B	(12)		
4A	(13)		(11) 4Y
4B ·	<u> </u>		

Truth Table

А	В	Υ
L	L	L
L	Η	Н
Н	L	Н
Н	Н	L

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±100	mA
Power dissipation	P _D	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65~150	°C

Note1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note2: 500 mW in the range of $Ta = -40\sim65^{\circ}C$. From Ta = 65 to $85^{\circ}C$ a derating factor of -10 mW/°C should be applied up to 300 mW.

Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~V _{CC}	V
Output voltage	Vout	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dV	0~10	ns/V

Note: The recommended operating conditions are required to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.

2



Electrical Characteristics

DC Characteristics

					Ta = 25°C		Ta = -40~85°C				
Characteristics	Symbol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
High-level input voltage	V _{IH}	_			4.5~ 5.5	2.0	ı	ı	2.0	ı	V
Low-level input voltage	V _{IL}		_		4.5~ 5.5	l		0.8	_	0.8	V
	Voн	V _{IN}	$I_{OH} = -50 \mu A$		4.5	4.4	4.5	_	4.4		
High-level output voltage		= V _{IH} or V _{IL}	$I_{OH} = -24 \text{ mA}$		4.5	3.94	_	_	3.80	_	V
			$I_{OH} = -75 \text{ mA}$	(Note)	5.5			_	3.85	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 50 \mu A$		4.5		0.0	0.1	_	0.1	
			$I_{OL} = 24 \text{ mA}$		4.5	_	_	0.36	_	0.44	V
			$I_{OL} = 75 \text{ mA}$	(Note)	5.5			_	_	1.65	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND			5.5			±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND			5.5	_	_	4.0	_	40.0	μΑ
	Ic	Per input: $V_{IN} = 3.4 \text{ V}$ Other input: V_{CC} or GND			5.5	_	_	1.35	_	1.5	mA

Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

AC Characteristics ($C_L = 50 \text{ pF}, R_L = 500 \Omega, \text{ input: } t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
Characteristics			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t _{pLH}	_	5.0 ± 0.5	_	5.7	10.5	1.0	12.0	ns
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation capacitance	C _{PD} (Note)	_		_	23	_	_	_	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

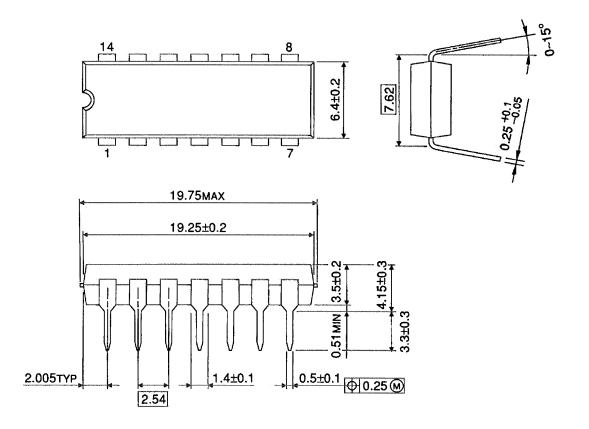
Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$ (per gate)



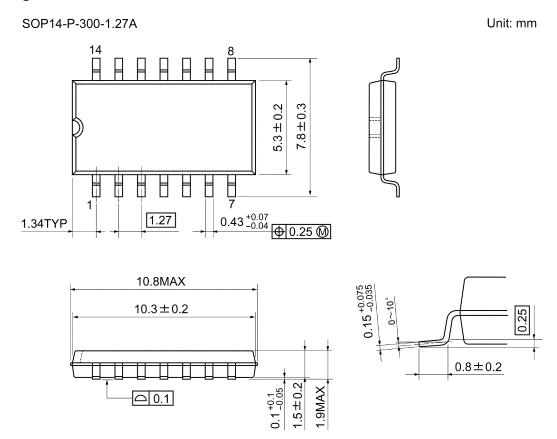
Package Dimensions

DIP14-P-300-2.54 Unit: mm



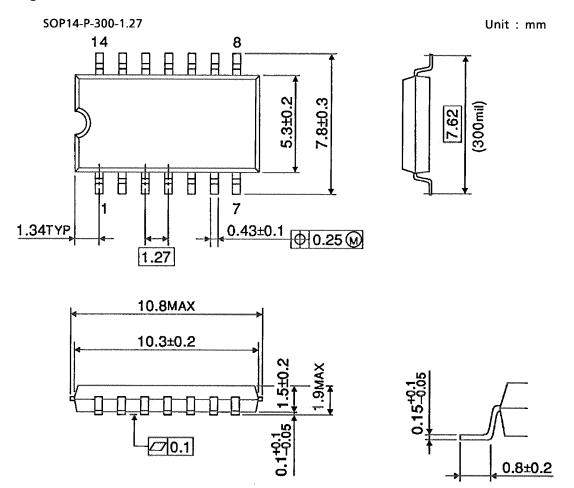
Weight: 0.96 g (typ.)

Package Dimensions



Weight: 0.18 g (typ.)

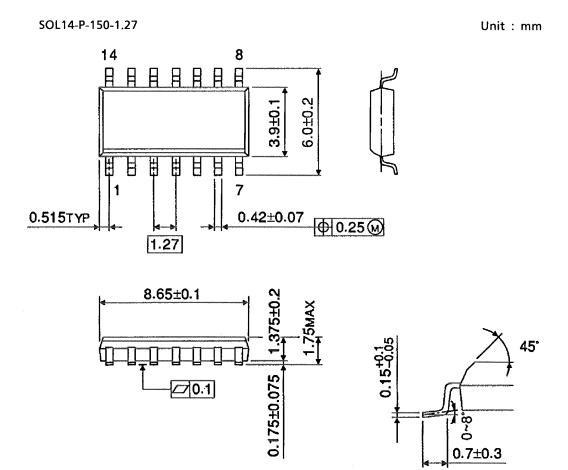
Package Dimensions



Weight: 0.18 g (typ.)



Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

Note: Lead (Pb)-Free Packages

DIP14-P-300-2.54 SOP14-P-300-1.27A SOL14-P-150-1.27

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