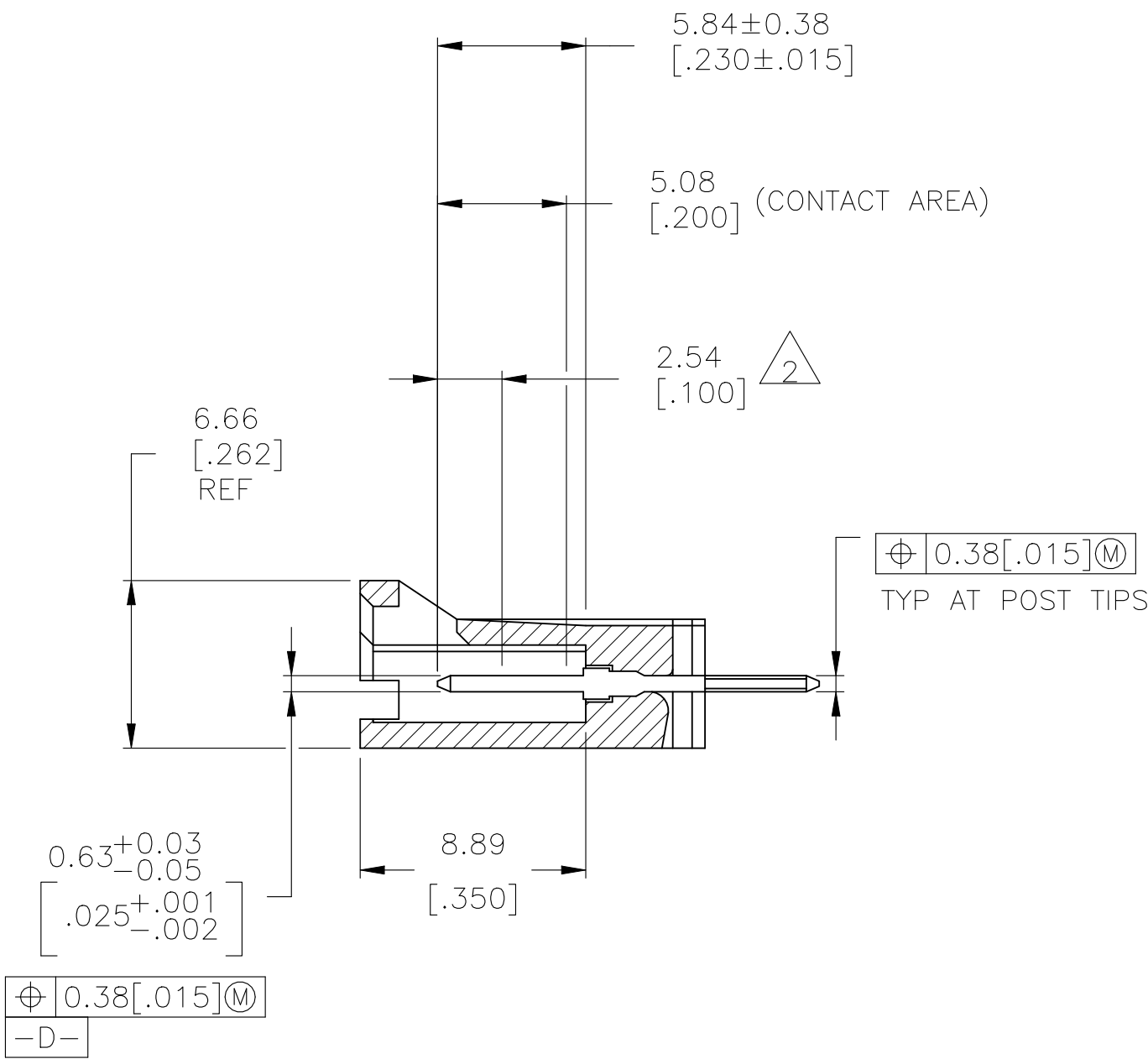
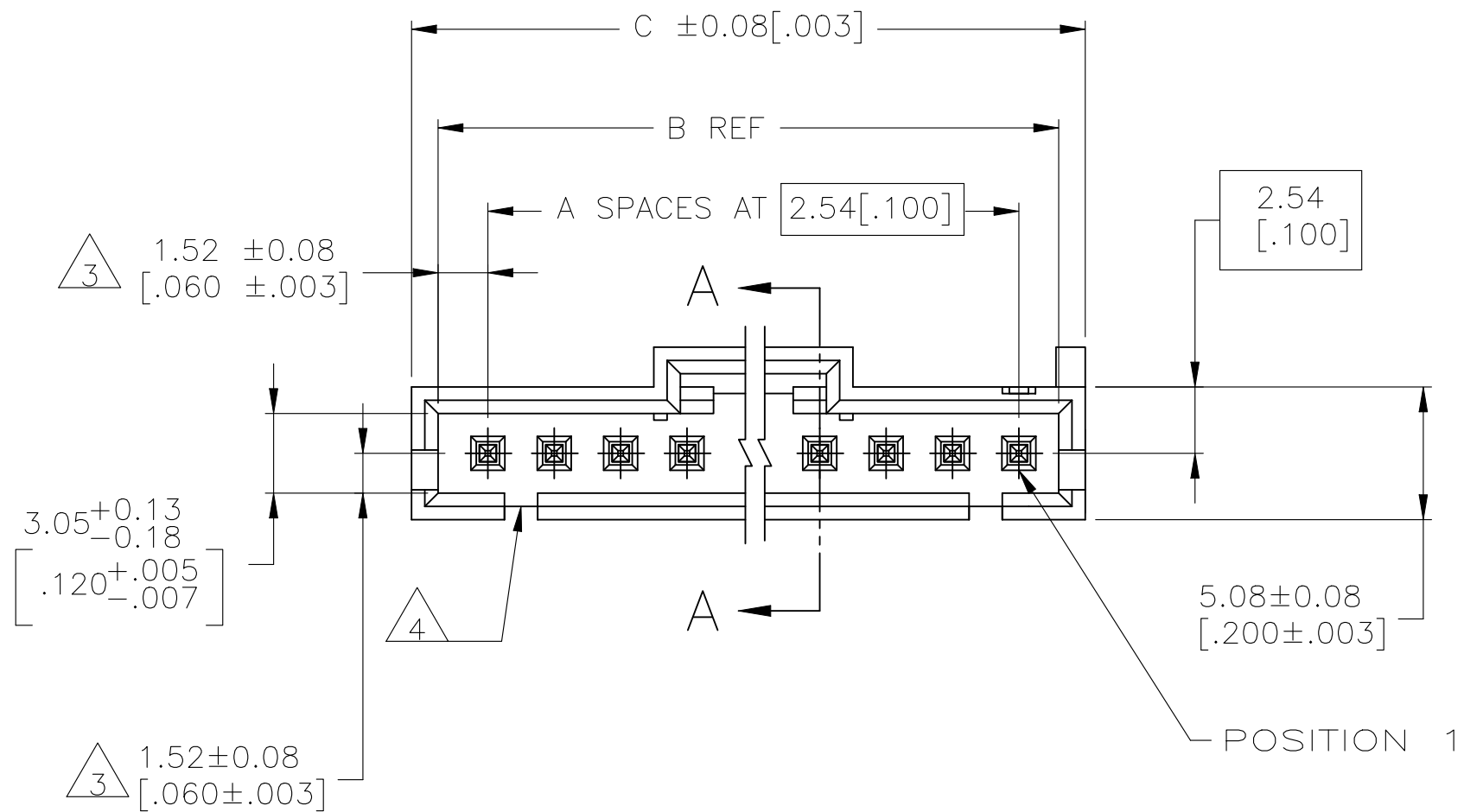
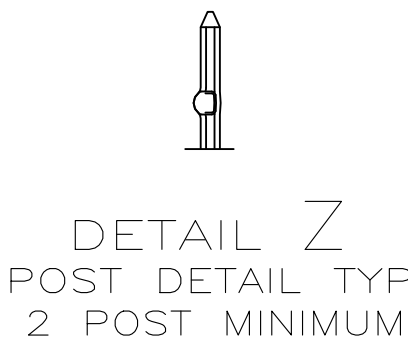
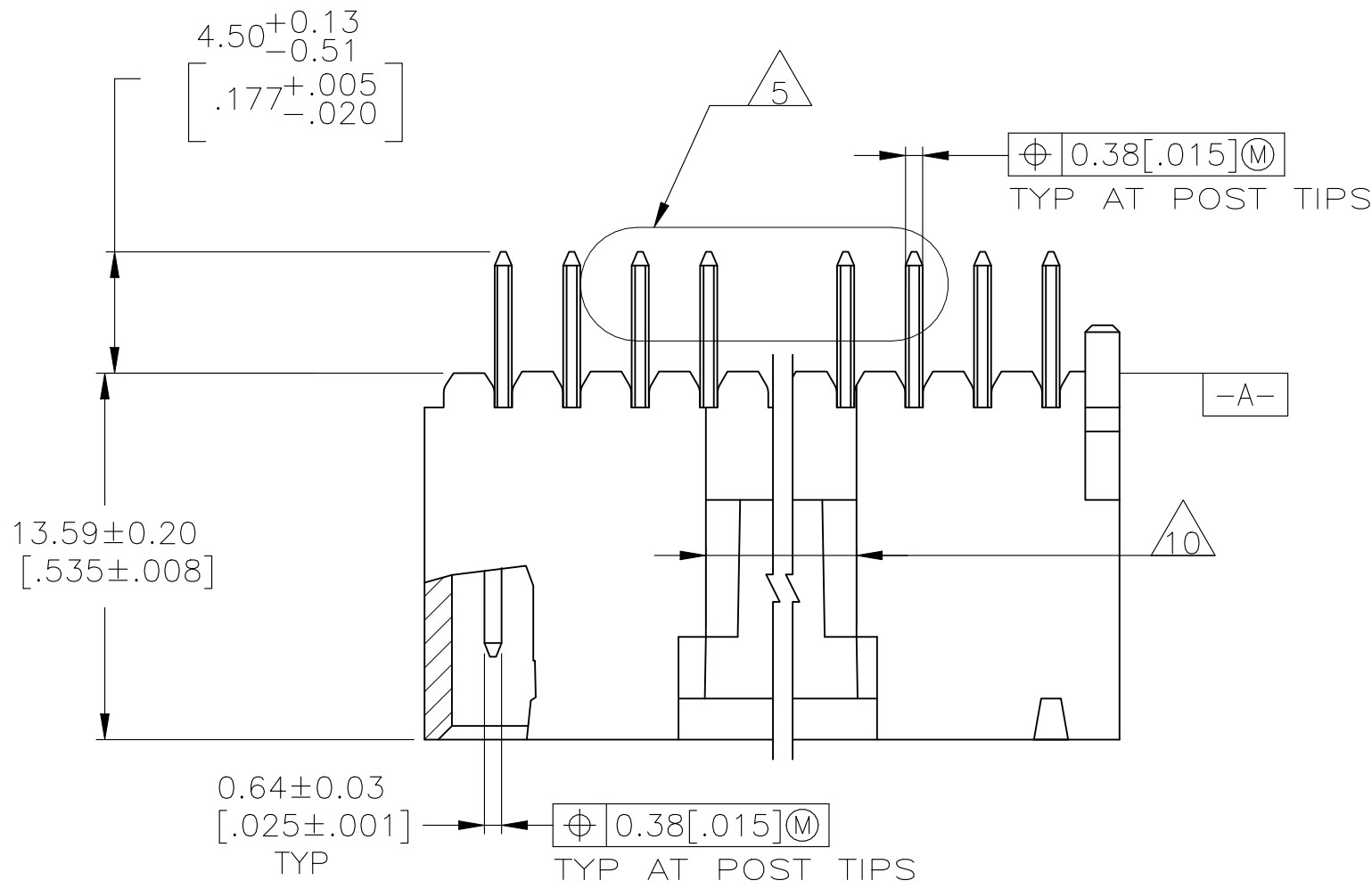
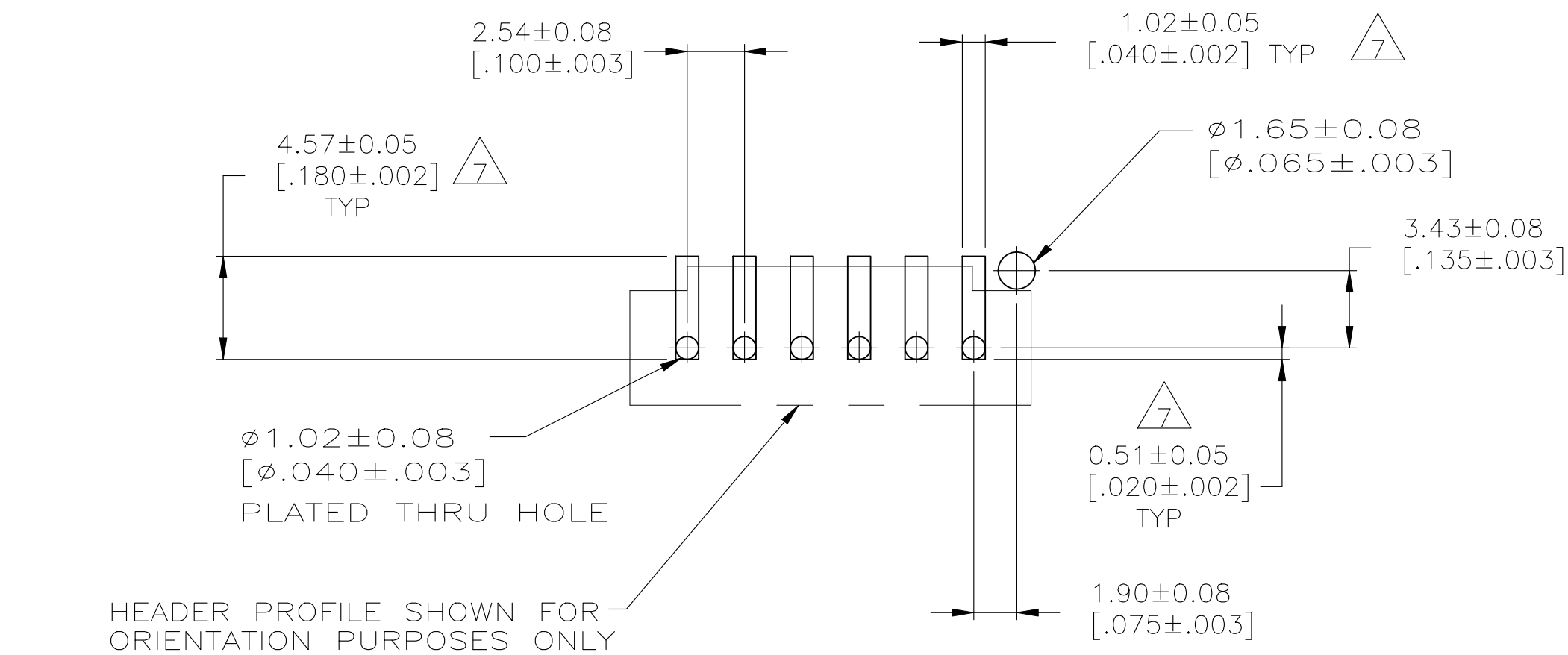


REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	A	ECO-21-001114	27JAN2021	RK	JO



- 1 FINISH: 0.00038[.000015] GOLD IN THE CONTACT AREA, 0.00254[.000100] MATTE TIN ON REMAINDER OF CONTACT, ALL OVER 0.00127[.000050] NICKEL.
- 2 POINT OF MEASUREMENT FOR PLATING THICKNESS.
- 3 THE NOTED DIMENSIONS APPLY FROM THE BASIC DIMENSION CENTER LINE (NOT THE POST CENTER LINE) TO THE SURFACE INDICATED.
- 4 ONE POLARIZATION SLOT FOR 2 AND 3 POSITION ASSEMBLIES ONLY.
- 5 SELECT POST TAILS FORMED TO PROVIDE CONNECTOR HOLD DOWN UNTIL SOLDERED, CONFIGURATION ACCEPTS 0.69[.027]–2.03[.080] THICK PRINTED CIRCUIT BOARD. (SEE DETAIL Z).
- 6 PARTS ARE PACKAGED IN GANG OF TUBES.
- 7 DIMENSIONS NOTED ARE FOR SOLDER STENCIL LAYOUT FOR USE WITH 1.58 ± 0.20[0.062 ± 0.008] THICK PRINTED CIRCUIT BOARDS.
- 8 MATERAIL- HOUSING: LCP, COLOR-BLACK. POSTS: BRASS.
- 9 FINISH: .00076 [.000030] GOLD IN THE CONTACT AREA, .00254[.000100] MATTE TIN ON REMAINDER OF CONTACT, ALL OVER .00127[.000050] NICKEL.
- 10 0.25 [0.010] RECESS PERMISSIBLE IN THIS AREA FOR MOLD SHUT OFF



RECOMMENDED PC BOARD MOUNTING DIMENSIONS
RECOMMENDED STENCIL THICKNESS = 0.25[0.010]

2, 3, 5, 6	$\Delta 9$	20.19 [0.795]	18.29 [0.720]	6	7	2267767-2
2, 3, 5, 6	$\Delta 1$	20.19 [0.795]	18.29 [0.720]	6	7	2267767-1
POST NO. OMITTED	PLATING	C	B	A	NO. OF POSN	PART NO.

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN SATYA.M 10JUL2020	TE Connectivity			
DIMENSIONS: mm [INCHES]		CHK OLSON JOSEPH 10JUL2020	NAME			
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD OLSON JOSEPH 10JUL2020	HDR ASY,AMPMODU MTE,VERT,SINGLE ROW .100 CL,.025 SQ PST,PLZD,LATCH, HD,.177 LEAD			
0 PLC ± -		PRODUCT SPEC	APPLICATION SPEC			
1 PLC ± -		—	—			
2 PLC ± 0.13[.005]		—	—			
3 PLC ± -		—	—			
4 PLC ± -		—	—			
ANGLES ± -		—	—			
FINISH SEE TABLE		WEIGHT —	A100779C=2267767			
MATERIAL $\Delta 8$		CUSTOMER DRAWING		SCALE 4:1	SHEET 1 OF 1	REV A