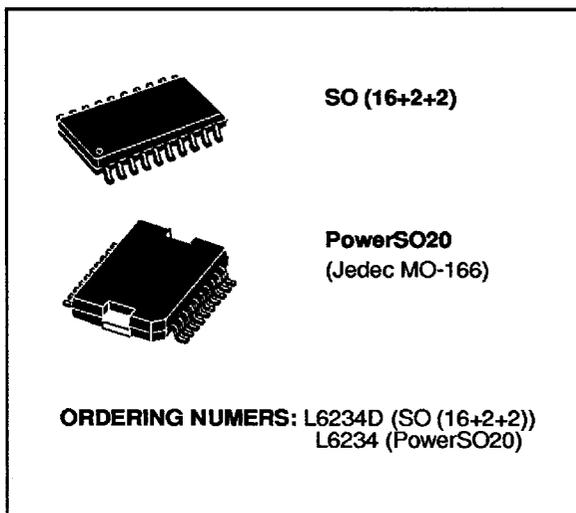


THREE PHASE MOTOR DRIVER

ADVANCE DATA

- SUPPLY VOLTAGE FROM 7 TO 58V
- 5A PEAK CURRENT
- DC MAX CURRENT $T_{case} = 70^{\circ}C$
 - ◆ 1.5A (SO(16+2+2))
 - ◆ 4A (PowerSO20)
- $R_{DS\ ON}$ 0.3 Ω TYP VALUE AT 25°C
- CROSS CONDUCTION PROTECTION
- TTL COMPATIBLE DRIVER
- OPERATING FREQUENCY TO 50KHz
- THERMAL SHUTDOWN
- INTRINSIC FAST FREE WHEELING DIODES
- INPUT AND ENABLE FUNCTION FOR EVERY HALF BRIDGE
- 10V EXTERNAL REFERENCE AVAILABLE
- UNDERVOLTAGE LOCKOUT



DESCRIPTION

The L6234 is a triple half bridge to drive a brushless motor.

It is realized in Multipower BCD technology which combines isolated DMOS power transistors with CMOS and Bipolar circuits on the same chip.

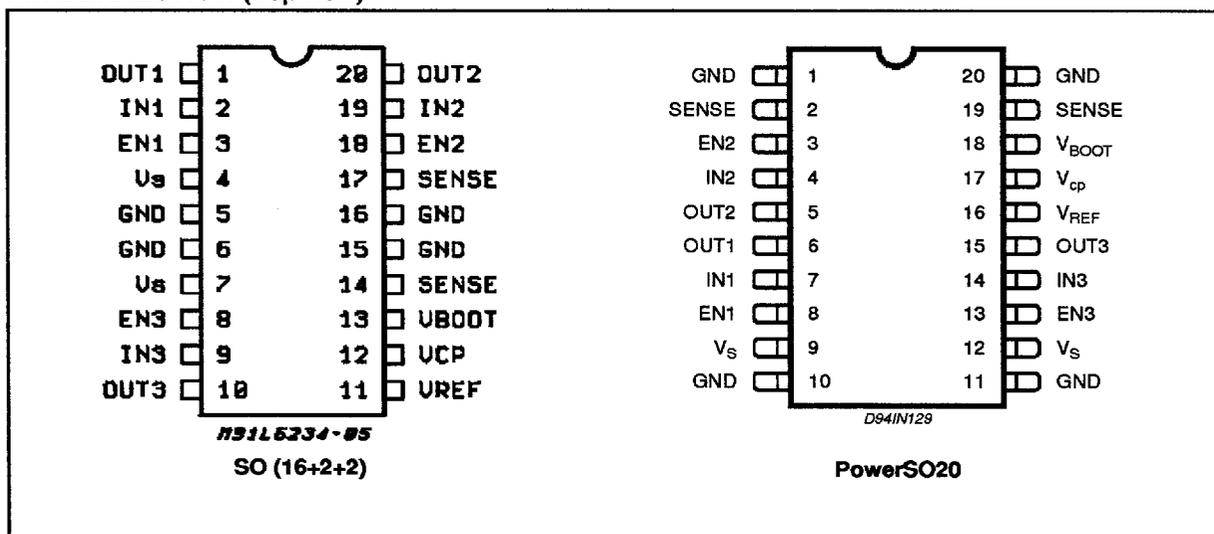
By using mixed technology it has been possible to optimize the logic circuitry and the power stage to achieve the best possible performances.

The output DMOS transistors can sustain a very

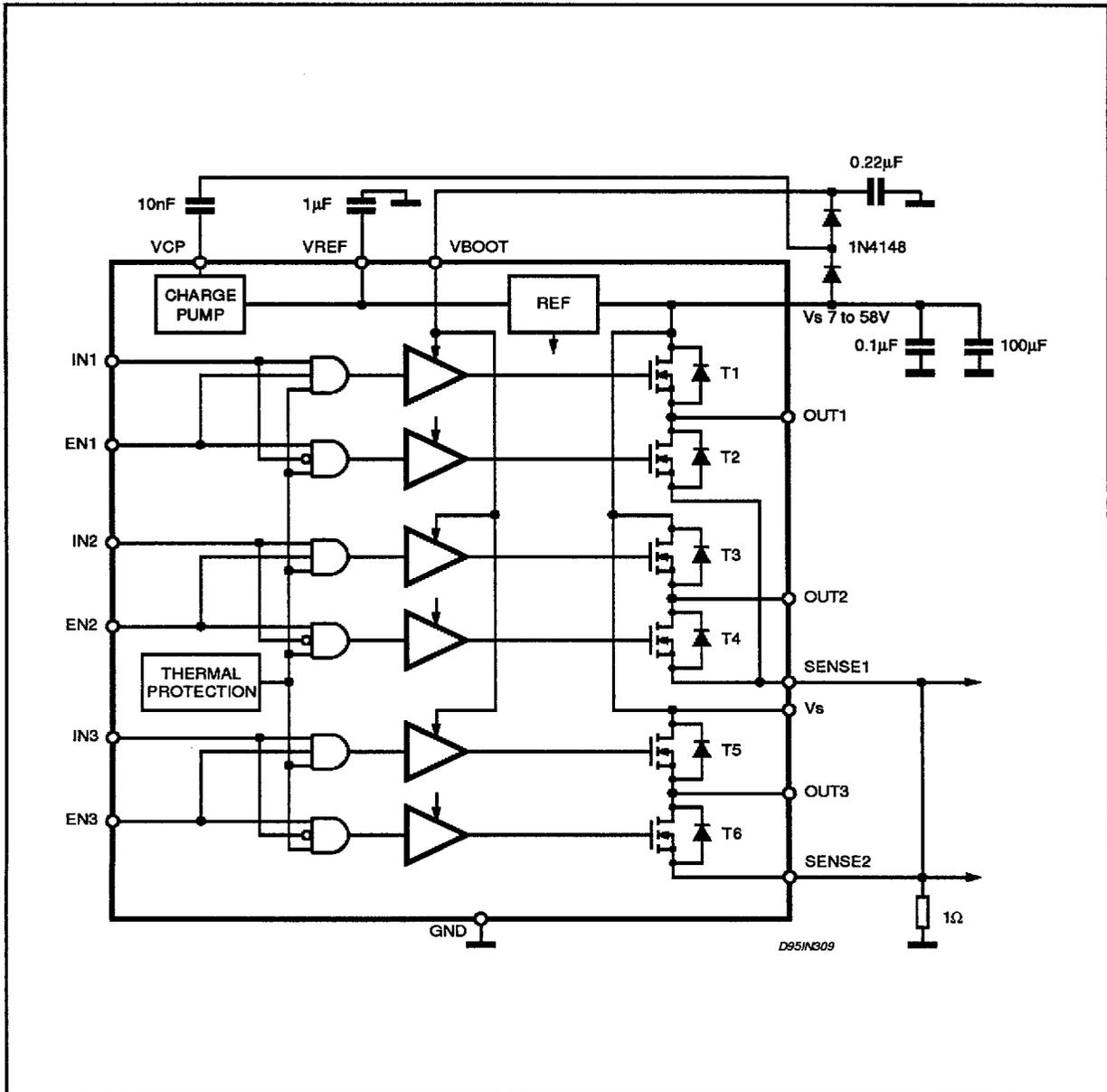
high current due to the fact that the DMOS structure is not affected by the second breakdown effect, the maximum RMS maximum current is practically limited by the dissipation capabilities of the package. All the logic inputs are TTL, CMOS and μP compatible. Each channel is controlled by two separate logic input.

L6234 is available in 20 pin SO package (16+2+2; L6234D) and in PowerSO20.

PIN CONNECTION (Top view)



BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _P	Power Supply Voltage	58	V
V _{IN, VEN}	Input Enable Voltage	- 0.3 to 7	V
I _{peak}	Pulsed Output Current (note) L6234D	2.5	A
V _{SENSE}	Sensing Voltage	-1 to 4	V
V _b	Bootstrap Peak Voltage	68	V
V _{OD}	Differential Output Voltage (between any of the 3 OUT pins)	60	V
P _{tot}	Total Power Dissipation L6234D T _{pins} = 90°C	3.3	W
P _{tot}	Total Power Dissipation L6234D T _{amb} = 70°C	0.9 (*)	W
T _{stg, T_j}	Storage and Junction Temperature Range	-40 to 150	°C

Note 1: Pulse width limited only by junction temperature and the transient thermal impedance

(*) Mounted on board with minimized copper area

THERMAL DATA

Symbol	Description	SO	PowerSO20	Unit
R _{th j-pins}	Thermal Resistance Junction-pins	18	-	°C/W
R _{th j-amb}	Thermal Resistance Junction-ambient	(*)90	(**)13	°C/W

(*) Mounted on board with minimized copper area

(**) Mounted on aluminium substrate

PIN FUNCTIONS

DIP/SO	PowerSO-20	Name	Function
1 20 10	6 5 15	OUT 1 OUT 2 OUT 3	Output of the channels 1/2/3.
2 19 9	7 4 14	IN 1 IN 2 IN 3	Logic input of channels 1/2/3. A logic HIGH level (when the corresponding EN pin is HIGH) switches ON the upper DMOS Power Transistor, while a logic LOW switches ON the corresponding low side DMOS Power.
3 18 8	8 3 13	EN 1 EN 2 EN 3	Enable of the channels 1/2/3. A logic LOW level on this pin switches off both power DMOS of the related channel.
4,7	9, 12	V _s	Power Supply Voltage.
14,17	2,19	SENSE	A resistance R _{sense} connected to this pin provides feedback for motor current control (the two pins must be connected together).
11	16	V _{ref}	Internal Voltage Reference. A capacitor connected from this pin to GND increases the stability of the Power DMOS drive circuit.
12	17	V _{cp}	Bootstrap Oscillator. Oscillator output for the external charge pump.
13	18	V _{BOOT}	Overvoltage input to drive the upper DMOS
5,6 15,16	1,10 11,20	GND	Common Ground Terminal. In Powerdip and SO packages these pins are used to dissipate the heat forward the PCB.

ELECTRICAL CHARACTERISTICS ($V_s = 42V$; $T_j = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V_s	Supply Voltage		7		58	V
V_{ref}	Reference Voltage			10		V
I_s	Quiescent Supply Current			4		mA
f_c	Commutation Frequency				50	KHz
T_s	Thermal Shutdown		150			$^\circ C$
T_D	Dead Time Protection			300		ns
V_h	Under Voltage			6.5		V
V_l	Under Voltage			6		V

OUTPUT DMOS TRANSISTOR

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
I_{DSS}	Leakage Current				1	mA
$R_{DS(ON)}$	ON Resistance			0.3		Ω
V_{sense}	Sensing Voltage		-1		4	V

SOURCE DRAIN DIODE

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V_{SD}	Forward ON Voltage	$I_{SD} = 1.2A$; EN = LOW		0.9		V
T_{RR}	Reverse Recovery Time	$I_F = 1.2A$		300		ns
T_{pr}	Forward Recovery Time			200		ns

LOGIC LEVELS

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V_{INL}, V_{ENL}	Input LOW Voltage		-0.3		0.8	V
V_{INH}, V_{ENH}	Input HIGH Voltage		2		7	V
I_{INL}, I_{ENL}	Input LOW Current	$V_{IN}, V_{EN} = L$			-10	μA
I_{INH}, I_{ENH}	Input HIGH Current	$V_{IN}, V_{EN} = H$		30		μA

CIRCUIT DESCRIPTION

L6234 is a triple half bridge designed to drive brushless DC motors.

Each half bridge has 2 power DMOS transistors with $R_{dsON} = 0.3\Omega$. The 3 half bridges can be controlled independently by means of the 3 inputs IN1, IN2, IN3 and the 3 inputs EN1, EN2, and

EN3. An external connection to the 3 common low side DMOS sources is provided to connect a sensing resistor for constant current chopping application.

The driving stage and the logic stage are designed to work from 8V to 52V.

Figure 1: PowerSO-20 Transient Thermal Resistance

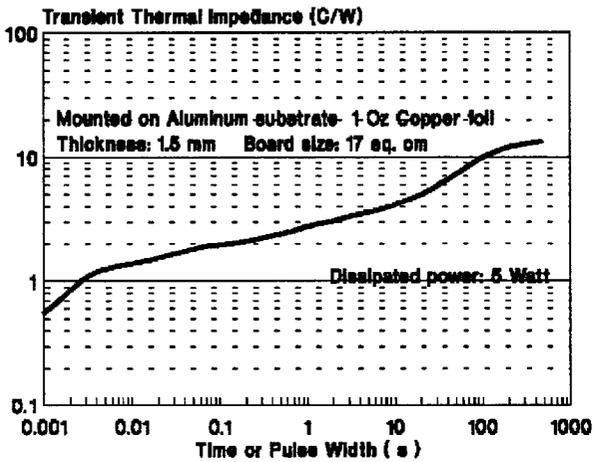


Figure 2: PowerSO-20 Thermal Resistance (Mounted on Aluminium substrate)

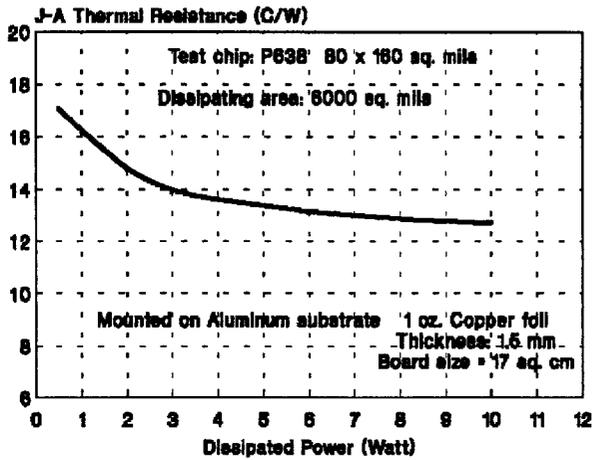


Figure 3: PowerSO-20 Thermal Resistance (Mounted on FR4 monolayer substrate)

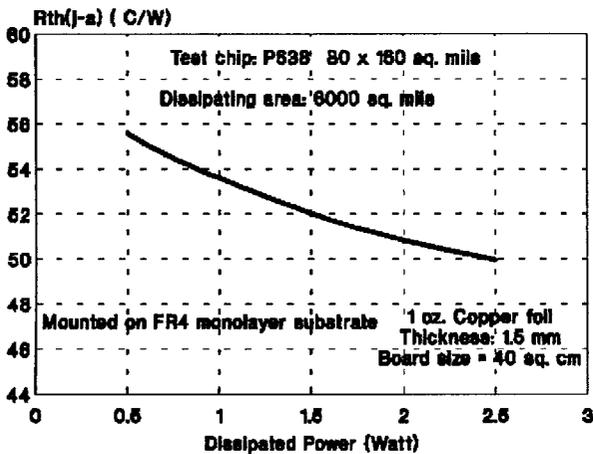


Figure 4: PowerSO-20: with external heatsink

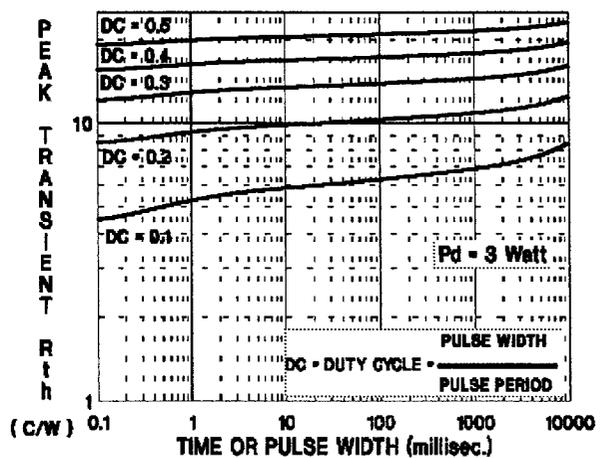
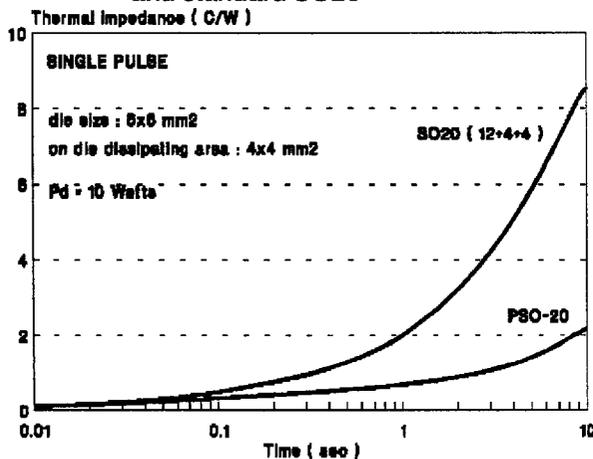


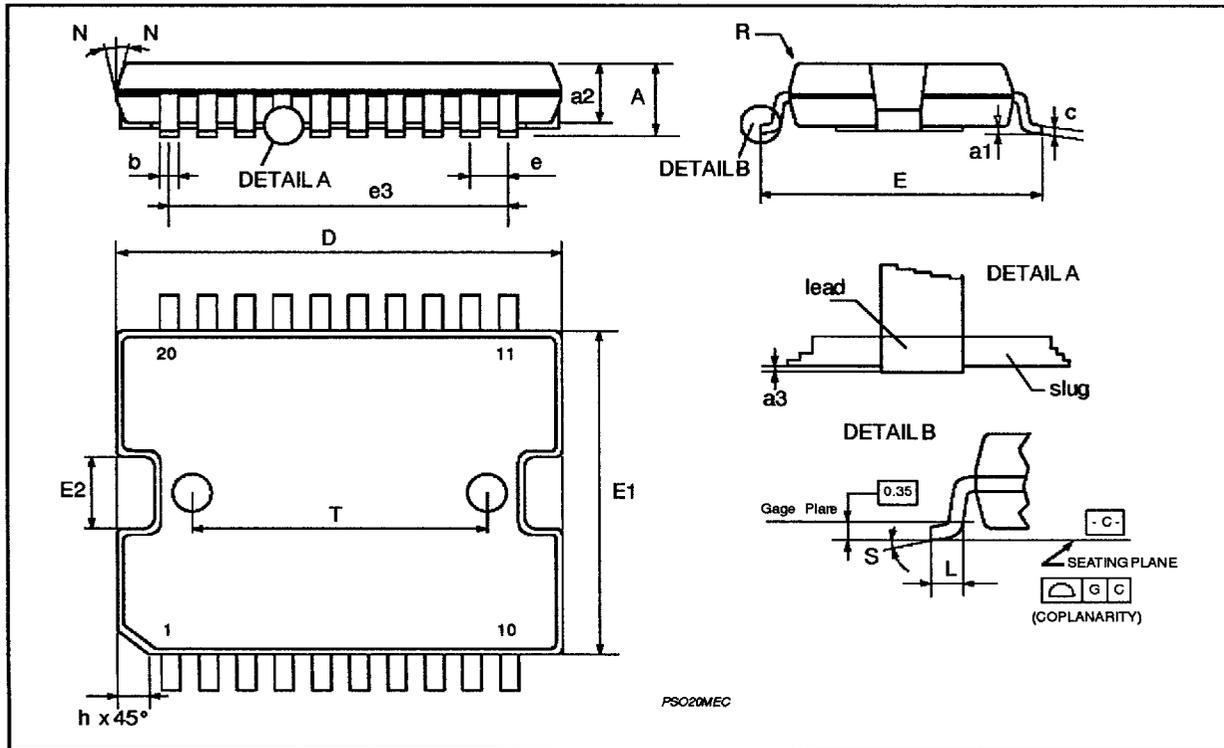
Figure 5: Thermal Impedance of PowerSO-20 and standard SO20



PowerSO-20 PACKAGE MECHANICAL DATA

DIM.	mm			Inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			3.60			0.1417
a1	0.10		0.30	0.0039		0.0118
a2			3.30			0.1299
a3	0		0.10	0		0.0039
b	0.40		0.53	0.0157		0.0209
c	0.23		0.32	0.009		0.0126
D (1)	15.80		16.00	0.6220		0.6299
E	13.90		14.50	0.5472		0.570
e		1.27			0.050	
e3		11.43			0.450	
E1 (1)	10.90		11.10	0.4291		0.437
E2			2.90			0.1141
G	0		0.10	0		0.0039
h			1.10			0.0433
L	0.80		1.10	0.0314		0.0433
N	10° (max.)					
S	8° (max.)					
T		10.0			0.3937	

(1) "D and E1" do not include mold flash or protrusions - Mold flash or protrusions shall not exceed 0.15mm (0.006")



SO20 PACKAGE MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			2.65			0.104
a1	0.1		0.3	0.004		0.012
a2			2.45			0.096
b	0.35		0.49	0.014		0.019
b1	0.23		0.32	0.009		0.013
C		0.5			0.020	
c1	45 (typ.)					
D	12.6		13.0	0.496		0.512
E	10		10.65	0.394		0.419
e		1.27			0.050	
e3		11.43			0.450	
F	7.4		7.6	0.291		0.299
L	0.5		1.27	0.020		0.050
M			0.75			0.030
S	8 (max.)					

