

## SQ3345, SQ2245 CMOS Series

- SQ3345: CMOS with Enable/ Disable, SQ2245 without Enable/Disable
- Lower Ringing Noise Option Available to Reduce EMI
- Half Size (8 Pin DIP) Metal Clock Oscillator
- Available in Thru-Hole or Surface Mount Configuration



์650 kHz – 69.999 MHz

## Standard Specifications

Overall Frequency Stability ± 50 PPM, ± 25 PPM, ± 20 PPM over Operating Temperature Range

Operating Temperature Range 0 to +70°C is standard, but can be extended to -40 to +85°C for certain frequencies

Supply Voltage (Vcc) 5.0 volts and 3.3 volts available

Symmetry (Duty Cycle) 40/60 to 60/40% is standard, but 45/55% at 50% of Vcc is also available (see Waveform 1)

Logic Levels Logic "1" 90% of Vcc MIN; Logic "0" 10% of Vcc MAX

Output Load Standard load is 15 pF (typ. 1 ASIC) maximum, see Test Circuit 3 (consult factory for heavier loads)

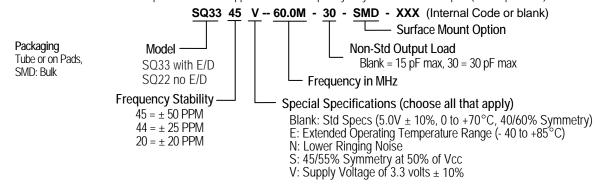
Ringing Noise Depends on frequency and output load. See EMI application note

SQ33: Enable/Disable Option Output enabled when Pin #1 is open or at Logic "1"; Output disabled when Pin #1 is at Logic "0".

(E/D) Frequency Range Max. Supply Current Rise and Fall Time Icc (mA) w/ 15pF load (MHz) Tr & Tf (nS) w/ 15pF load 3.3V 5.0V Typical Maximum 0.650 - 10.0007 10 3.0 4.0 10.001 - 25.999 10 20 2.5 3.5 26.000 - 34.99915 25 2.5 3.5 35.000 - 50.000 20 30 2.5 3.5 50.001 - 69.999 25 35 2.5 3.5

## **Part Numbering Guide**

Portions of the part number that appear after the frequency may not be marked on part (C of C provided)



Consult factory for available frequencies and specs. Not all options available for all frequencies. A special part number may be assigned. Frequency Stability is inclusive of frequency shifts due to calibration, temperature, supply voltage, shock, vibration and load

## Mechanical: inches (mm) not to scale Surface Mount Solder Pads

Due to part size and factory abilities, part marking may vary from lot to lot and may contain our part number or an internal code.

