





Jan 2006



- Pletronics' LV77D Series is a quartz crystal controlled precision square wave generator with an LVDS output.
- The package is designed for high density surface mount designs.
- Low cost mass produced oscillator.
- Tape and Reel or Tube packaging is available.
- 1 to 250 MHz
- 5 x 7 mm LCC Ceramic Package
- Enable/Disable Function on pad 1
- Disable function includes low standby power mode
- · Low Jitter

# Pletronics Inc. certifies this device is in accordance with the RoHS (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.2 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020C

Second Level Interconnect code: e4

#### **Absolute Maximum Ratings:**

Parameter	Unit
V <sub>cc</sub> Supply Voltage	-0.5V to +5.0V
Vi Input Voltage	-0.5V to V <sub>CC</sub> + 0.5V
Vo Output Voltage	-0.5V to V <sub>CC</sub> + 0.5V

#### **Thermal Characteristics**

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



Jan 2006

#### **Part Number:**

LV77	45	D	Ε	V	- 125.0M	-XX		Marking
							Internal code or blank	
							Frequency in MHz	fff.fff M
							Supply Voltage V <sub>cc</sub> V = 3.3V <u>+</u> 10%	V or B
							Enhanced Specification E = Temperature range -40 to 85°C	E
							Series Model	
							Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm	5 4 2
							Series Model	LV7

#### Part Marking:

**LV7**xywwaz Where: x = Frequency stability

fff.fff M ywwaz = Date code

**PLE ss** fff.fff = frequency in MHz

ss = Enhanced specification and voltage

Pletronics may ship the following combinations without notice (this is an enhanced specified device)

44 (25 ppm) stability parts when 45 (50 ppm) was ordered

20 (20 ppm) stability parts when 45 (50 ppm) or 44 (25 ppm) was ordered.

E temperature range parts when extended was not ordered.

Pletronics may ship parts that are not marked for extended temperature range but were tested for extended temperature range, a Certificate of Conformance will accompany these parts.



Jan 2006

#### Electrical Specification for 3.30V ±10% over the specified temperature range

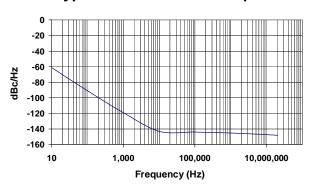
Item	Min	Max	Unit	Condition			
Frequency Range	1	250	MHz				
Frequency Accuracy "45"	-50	+50	ppm		voltages, load changes, aging for 1		
"44"	-25	+25		year, shock, vibration and temperatures			
" <b>20</b> "	-20	+20					
Output Waveform		LVDS					
Output High Level		1.60	Volts	-			
Output Low Level	0.90		Volts	-			
Differential Output (V <sub>OD</sub> )	250	450	mVolts	-	Coolead sireuit D1 = 50 ahma		
Output Offset Voltage (Vos)	1.125	1.375	Volts	<u>&gt;</u> 80 MHz	See load circuit R1 = 50 ohms		
	1.125	1.500	Volts	< 80 MHz			
Differential Output Error (dV <sub>os</sub> )		50	mVolts	-			
Output Symmetry	45	55	%	Referenced to	50% of amplitude or crossing point		
Output T <sub>RISE</sub> and T <sub>FALL</sub>	300	700	pS	> 80 MHz			
	400	900	pS	< 80 MHz	Vth is 20% and 80% of waveform		
Jitter	-	0.15	pS RMS	MS Measured from 12KHz to 20MHz from Fnomina			
	-	2.8		Measured from 10Hz to 1MHz from Fnominal			
Vcc Supply Current	-	66	mA	<u>&gt;</u> 80 MHz	Includes current of properly		
	-	45	mA	< 80 MHz	terminated device		
Enable/Disable Internal Pull-up	50	-	Kohm	To Vcc (equiv	alent resistance)		
V disable	-	0.8	Volts	Referenced to	Ground		
V enable	2.0	-	Volts	Referenced to	Ground		
Output leakage V <sub>OUT</sub> = V <sub>CC</sub>	-10	+10	uA	Pad 1 low, dev	vice disabled		
V <sub>OUT</sub> = 0V	-10	+10	uA				
Enable	-	10	nS	Time for outpu	ut to reach a logic state		
Disable time	•	10	nS	Time for outpu	ut to reach a high Z state		
Start up time	-	5	mS	≥ 80 MHz	Measured from the time		
	-	3	mS	< 80 MHz	Vcc = 3.0V		
Operating Temperature Range	0	+70	°C	Standard Temperature Range			
	-40	+85	°C	Extended Temperature Range "E" Option			
Storage Temperature Range	-55	+125	°C				
Standby Current I <sub>CC</sub>	-	3	uA	<u>&gt;</u> 80 MHz	Dad 1 law daving disabled		
	1	1.5	mA	< 80 MHz	Pad 1 low, device disabled		

Specifications with Pad 1 E/D open circuit

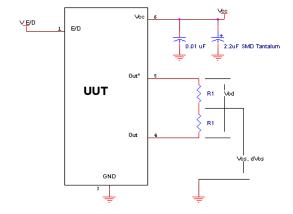


Jan 2006

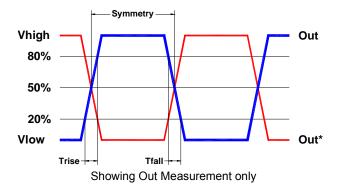
### **Typical Phase-Noise Response**



#### **Load Circuit**



### **Test Waveform**





Jan 2006

#### Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition A
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

#### **ESD Rating**

Model	Minimum Voltage Conditions	
Human Body Model	1500	MIL-STD-883 Method 3115
Charged Device Model	1000	JESD 22-C101

#### **Package Labeling**

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII



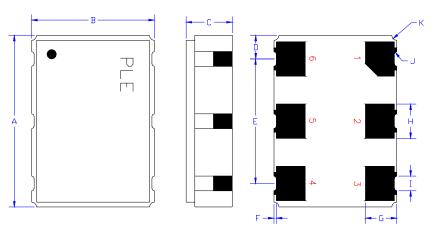
Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

Pb Free
2nd LvL Interconnect
Category=e4
Max Safe Temp=260C for 10s 2X Max



Jan 2006

#### Mechanical:



Contacts:
Gold 11.8 µinches 0.3 µm minimum over
Nickel 50 to 350 µinches 1.27 to 8.89 µm

<sup>1</sup> Typical dimensions

Not to Scale

	Inches	mm
Α	0.276 <u>+</u> 0.006	7.00 <u>+</u> 0.15
В	0.197 <u>+</u> 0.006	5.00 <u>+</u> 0.15
С	0.063 <u>+</u> 0.012	1.87 <u>+</u> 0.30
D <sup>1</sup>	0.038	0.96
E¹	0.200	5.08
F¹	0.004	0.10
G¹	0.050	1.27
Η <sup>1</sup>	0.055	1.40
I <sup>1</sup>	0.024	0.60
J <sup>1</sup>	0.004R	0.10R
K <sup>1</sup>	0.008R	0.20R

Pad	Function	Note			
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is <0.30 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{\rm CC}$ if the oscillator is to be always on.			
2	No connect	There is no internal connection to this pad			
3	Ground (GND)				
4	Output	The outputs must be terminated, 100 ohms between the outputs is the ideal			
5	Output*	termination.			
6	Supply Voltage (V <sub>cc</sub> )	Recommend connecting appropriate power supply bypass capacitors as close as possible.			

# Lead Free

#### Layout and application information

Recommend connecting Pad 1 and Pad 2 together to permit the design to accept Enable/Disable on both input pads

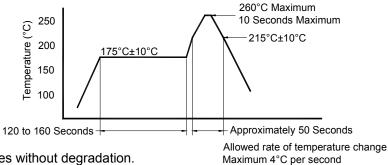
For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



Jan 2006

#### Reflow Cycle (typical for lead free processing)



The part may be reflowed 2 times without degradation.

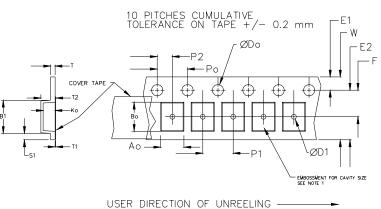
#### Tape and Reel: available for quantities of 250 to 1000 per reel

	Constant Dimensions Table 1							
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max
8mm		1.0			2.0			
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05			
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1
24mm	•	1.5			<u>+</u> 0.1			

	Variable Dimensions Table 2								
Tape         B1         E2 Min         F         P1         T2         W         Ao, Bo & Ko           Size         Max         Max         Ko									
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1		

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm



	B	D
•		

<u></u>\_С

		REEL DIMENSIONS							
Α	inches	7.0	10.0	13.0					
	mm	177.8	254.0	330.2					
В	inches	2.50	4.00	3.75					
	mm	63.5	101.6	95.3	Tape Width				
С	mm	13	13.0 +0.5 / -0.2						
D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0				
	mm			24.4 +2.0 -0.0	24.0				
,	mm			32.4 +2.0 -0.0	32.0				
D	Reel dimensions may vary from the above								

Reel dimensions may vary from the above



Jan 2006

#### **IMPORTANT NOTICE**

Pletronics Incorporated (PLE) reserves the right to make corrections, improvements, modifications and other changes to this product at anytime. PLE reserves the right to discontinue any product or service without notice. Customers are responsible for obtaining the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to PLE's terms and conditions of sale supplied at the time of order acknowledgment.

PLE warrants performance of this product to the specifications applicable at the time of sale in accordance with PLE's limited warranty. Testing and other quality control techniques are used to the extent PLE deems necessary to support this warranty. Except where mandated by specific contractual documents, testing of all parameters of each product is not necessarily performed.

PLE assumes no liability for application assistance or customer product design. Customers are responsible for their products and applications using PLE components. To minimize the risks associated with the customer products and applications, customers should provide adequate design and operating safeguards.

PLE products are not designed, intended, authorized or warranted to be suitable for use in life support applications, devices or systems or other critical applications that may involve potential risks of death, personal injury or severe property or environmental damage. Inclusion of PLE products in such applications is understood to be fully at the risk of the customer. Use of PLE products in such applications requires the written approval of an appropriate PLE officer. Questions concerning potential risk applications should be directed to PLE.

PLE does not warrant or represent that any license, either express or implied, is granted under any PLE patent right, copyright, artwork or other intellectual property right relating to any combination, machine or process which PLE product or services are used. Information published by PLE regarding third-party products or services does not constitute a license from PLE to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from PLE under the patents or other intellectual property of PLE.

Reproduction of information in PLE data sheets or web site is permissible only if the reproduction is without alteration and is accompanied by associated warranties, conditions, limitations and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. PLE is not responsible or liable for such altered documents.

Resale of PLE products or services with statements different from or beyond the parameters stated by PLE for that product or service voids all express and implied warranties for the associated PLE product or service and is an unfair or deceptive business practice. PLE is not responsible for any such statements.

#### **Contacting Pletronics Inc.**

Pletronics Inc. Tel: 425-776-1880 19013 36<sup>th</sup> Ave. W, Suite H Fax: 425-776-2760

Lynnwood, WA 98036-5761 USA E-mail: ple-sales@pletronics.com

URL: www.pletronics.com

Copyright © 2005, 2006, Pletronics Inc.