

# DATA SHEET

## PRECISION CHIP RESISTORS

RC02/12/22/32  
1%  
sizes 1206, 0805, 0603 and 0402



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## RC02/12/22/32 1%

### FEATURES

- Low assembly costs
- High component and equipment reliability
- Excellent performance at high frequency, especially the RC32.
- TC 50 in thick film technology
- Complete precision SMD family.

### APPLICATIONS

- All general purpose applications.

### DESCRIPTION

The resistors are constructed on a high grade ceramic body (aluminium oxide). Internal metal electrodes are added at each end and connected by a resistive paste which is applied to the top surface of the substrate. The composition of the paste is

adjusted to give the approximate resistance required and the value is trimmed to within tolerance, by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat and printed with the resistance value (no printing on RC22H and RC32). Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a lead/tin alloy.

### QUICK REFERENCE DATA

DESCRIPTION	VALUE			
	RC02H	RC12H	RC22H	RC32
Size code	1206 (3216)	0805 (2012)	0603 (1608)	0402 (1005)
Resistance range	1 $\Omega$ to 10 M $\Omega$	1 $\Omega$ to 10 M $\Omega$	1 $\Omega$ to 10 M $\Omega$	1 $\Omega$ to 10 M $\Omega$
Resistance tolerance and E-series	$\pm 1\%$ ; E24/E96 series			
Temperature coefficient; note 1				
1 $\Omega \leq R \leq 10 \Omega$	$\leq 250 \pm 250$	$\leq 250 \pm 250$	$\leq 250 \pm 250$	$\leq 250 \pm 250$
10 $\Omega < R \leq 10 \text{ M}\Omega$	$\leq \pm 100$	$\leq \pm 100$	$\leq \pm 100$	$\leq \pm 200$
Maximum dissipation at $T_{\text{amb}} = 70 \text{ }^\circ\text{C}$	0.25 W	0.125 W	0.1 W	0.063 W
Maximum permissible voltage	200 V (DC or RMS)	150 V (DC or RMS)	50 V (DC or RMS)	50 V (DC or RMS)
Climatic category (IEC 60068)	55/155/56			55/125/56
Basic specification	IEC 60115-8			

### Note

1. All TC values should be multiplied by  $10^{-6}/\text{K}$ .

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### ORDERING INFORMATION

**Table 1** Ordering code indicating resistor type and packing

TYPE	ORDERING CODE 2322 ... ..			
	PAPER TAPE ON REEL			
	5 000 units	10 000 units	20 000 units	50 000 units
RC02H	724 6....	724 7....	724 8....	–
RC12H	734 6....	734 7....	734 8....	–
RC22H	704 6....	704 7....	704 8....	–
RC32	–	706 7....	–	706 8....
<b>Jumper 0 Ω</b>				
RC12H; note 1	734 92006	734 92007	–	–
RC22H; note 2	704 92006	704 92007	–	–

### Notes

1. The jumper has a maximum resistance  $R_{\max} = 50 \text{ m}\Omega$  and a rated current  $I_R = 2 \text{ A}$ .
2. The jumper has a maximum resistance  $R_{\max} = 50 \text{ m}\Omega$  and a rated current  $I_R = 1 \text{ A}$ .

### Ordering code (12NC)

- The resistors have a 12-digit ordering code starting with 2322.
- The subsequent 4 digits indicate the resistor type and packing; see Table 1.
- The remaining 4 digits indicate the resistance value:
  - The first 3 digits indicate the resistance value.
  - The last digit indicates the resistance decade in accordance with Table 2.

**Table 2** Last digit of 12NC

RESISTANCE DECADE	LAST DIGIT
1 to 9.1 Ω	8
10 to 91.0 Ω	9
100 to 910 Ω	1
1 to 9.1 k Ω	2
10 to 91.0 k Ω	3
100 to 910 k Ω	4
1 to 9.1 M Ω	5
10 M Ω	6

### ORDERING EXAMPLE

The ordering code of an RC02H resistor, value 4750 Ω, supplied on paper tape of 5000 units per reel is: 2322 724 64752.

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### FUNCTIONAL DESCRIPTION

#### Product characterization

Standard values of nominal resistance are taken from the E24/E96 series for resistors with a tolerance of  $\pm 1\%$ . The values of the E24/E96 series are in accordance with "IEC publication 60063".

#### Limiting values

TYPE	LIMITING VOLTAGE <sup>(1)</sup> (V)	LIMITING POWER (W)
RC02	200	0.25
RC12	150	0.125
RC22	50	0.1
RC32	50	0.063

#### Note

1. This is the maximum voltage that may be continuously applied to the resistor element, see "IEC publication 60115-8".

### DERATING

The power that the resistor can dissipate depends on the operating temperature; see Fig.1.

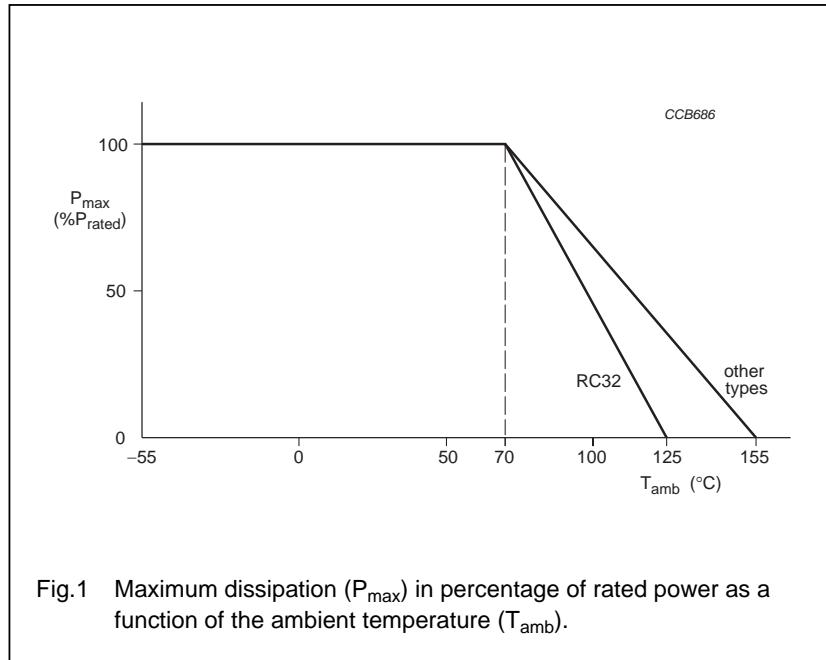
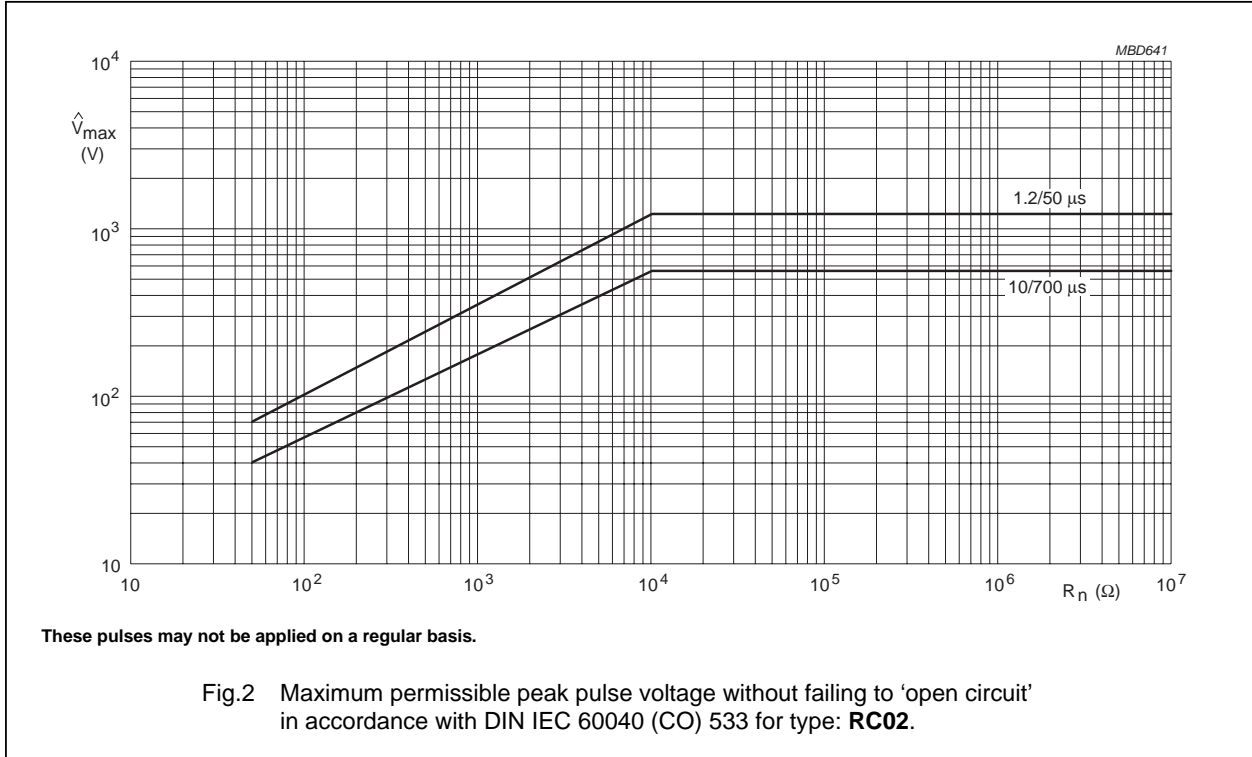


Fig.1 Maximum dissipation ( $P_{max}$ ) in percentage of rated power as a function of the ambient temperature ( $T_{amb}$ ).

**Precision chip resistors  
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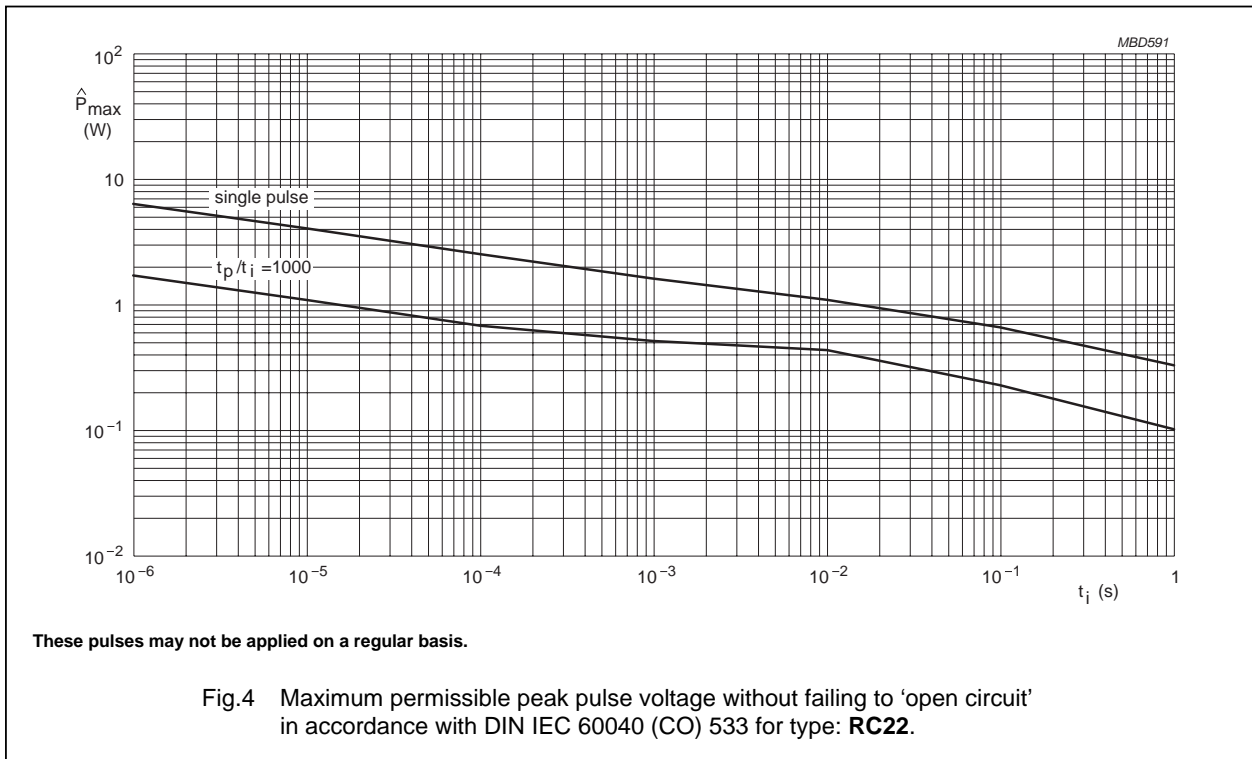
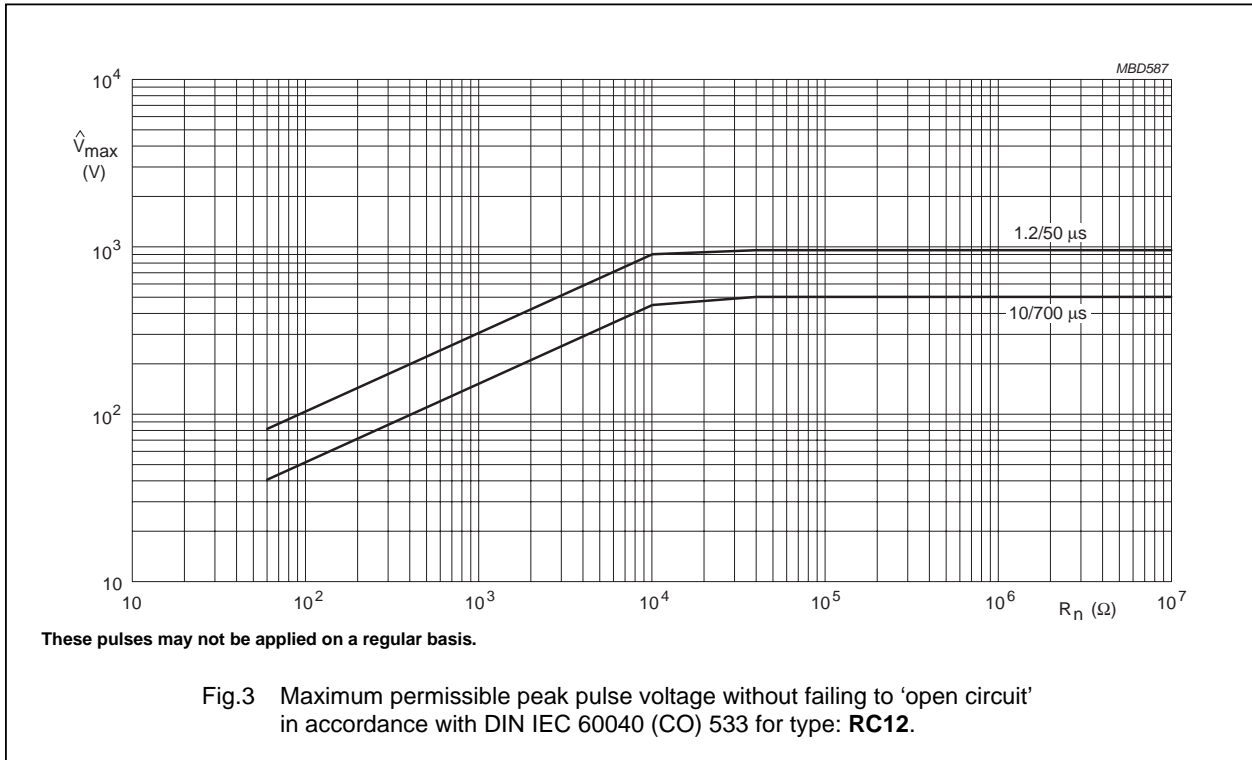
**RC02/12/22/32  
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PULSE LOADING CAPABILITIES



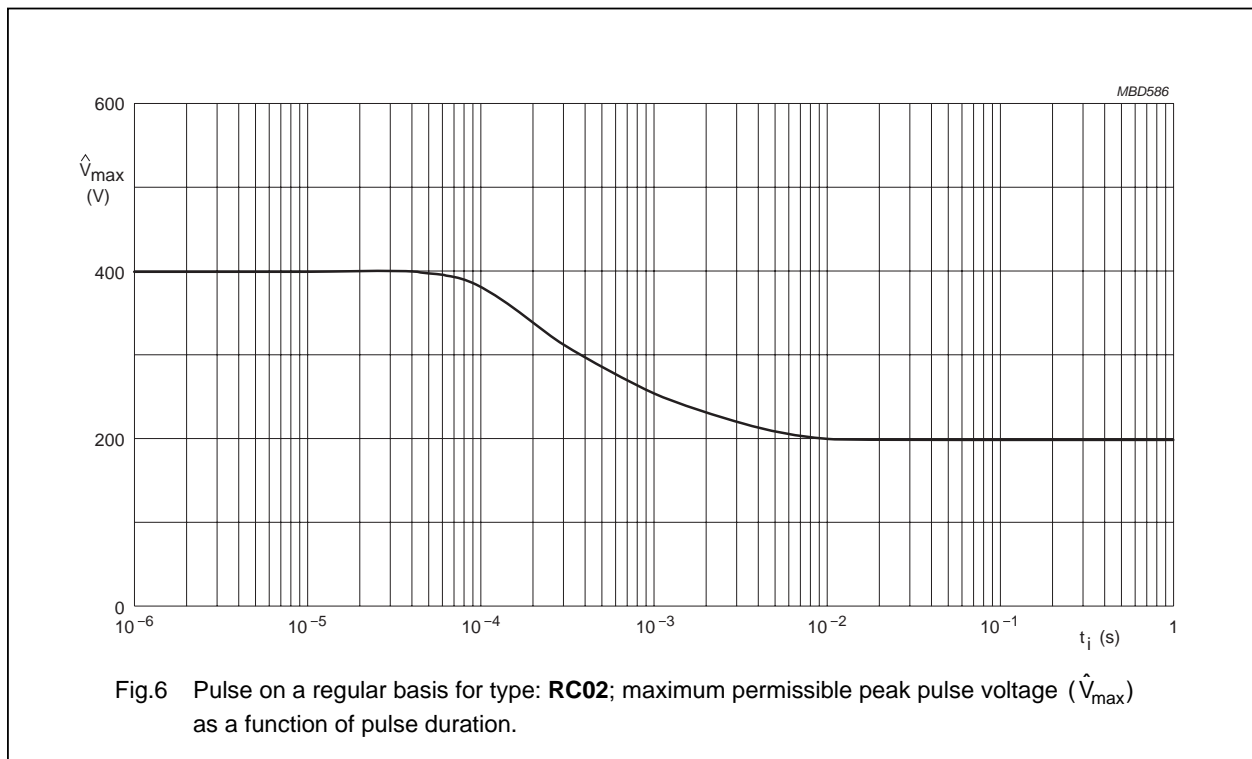
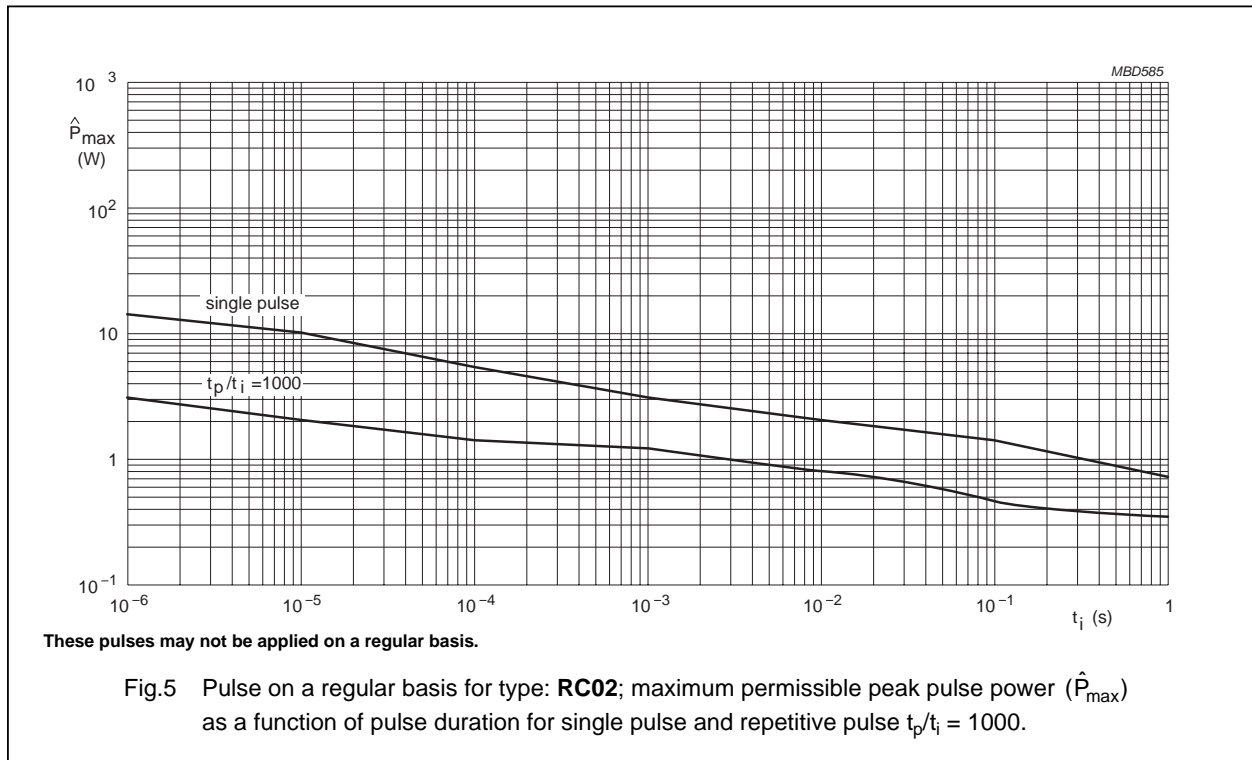
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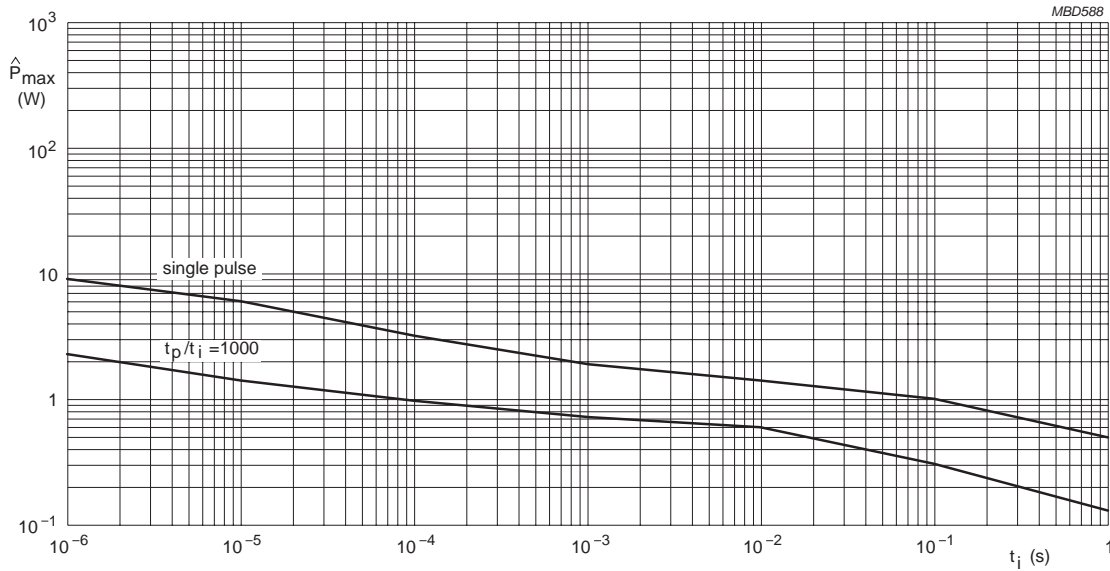


Fig.7 Pulse on a regular basis for type: **RC12**; maximum permissible peak pulse power ( $\hat{P}_{max}$ ) as a function of pulse duration for single pulse and repetitive pulse  $t_p/t_i = 1000$ .

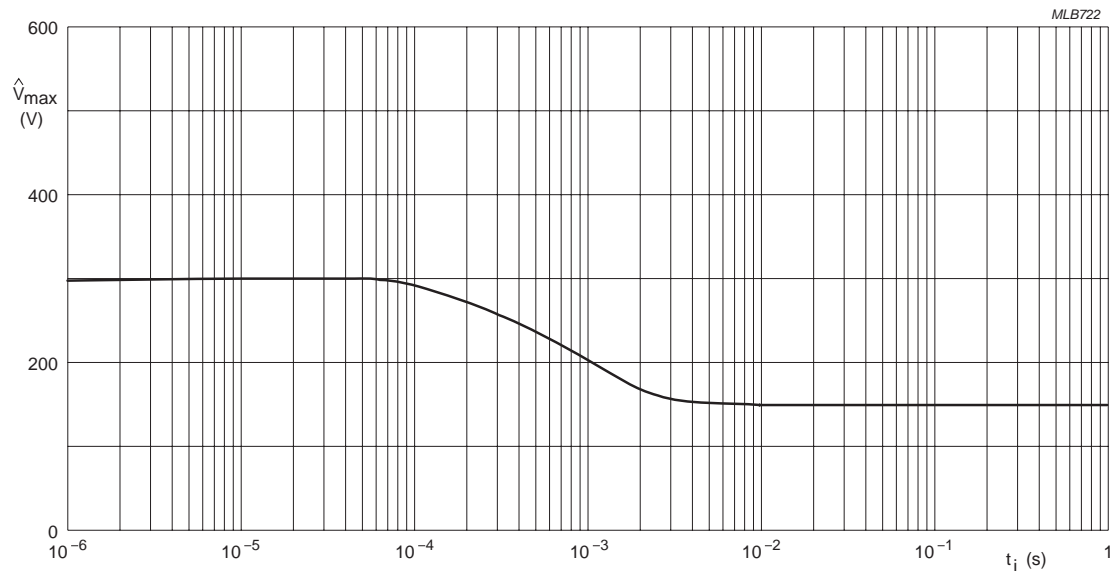
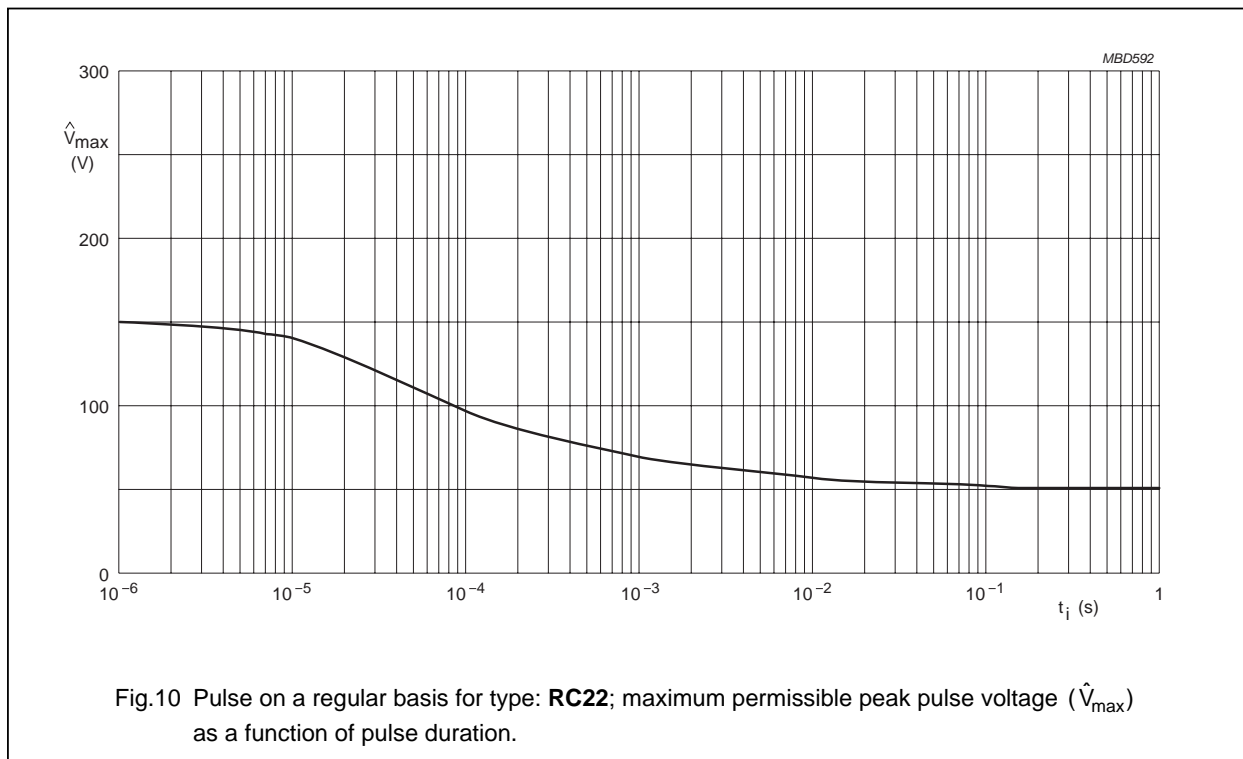
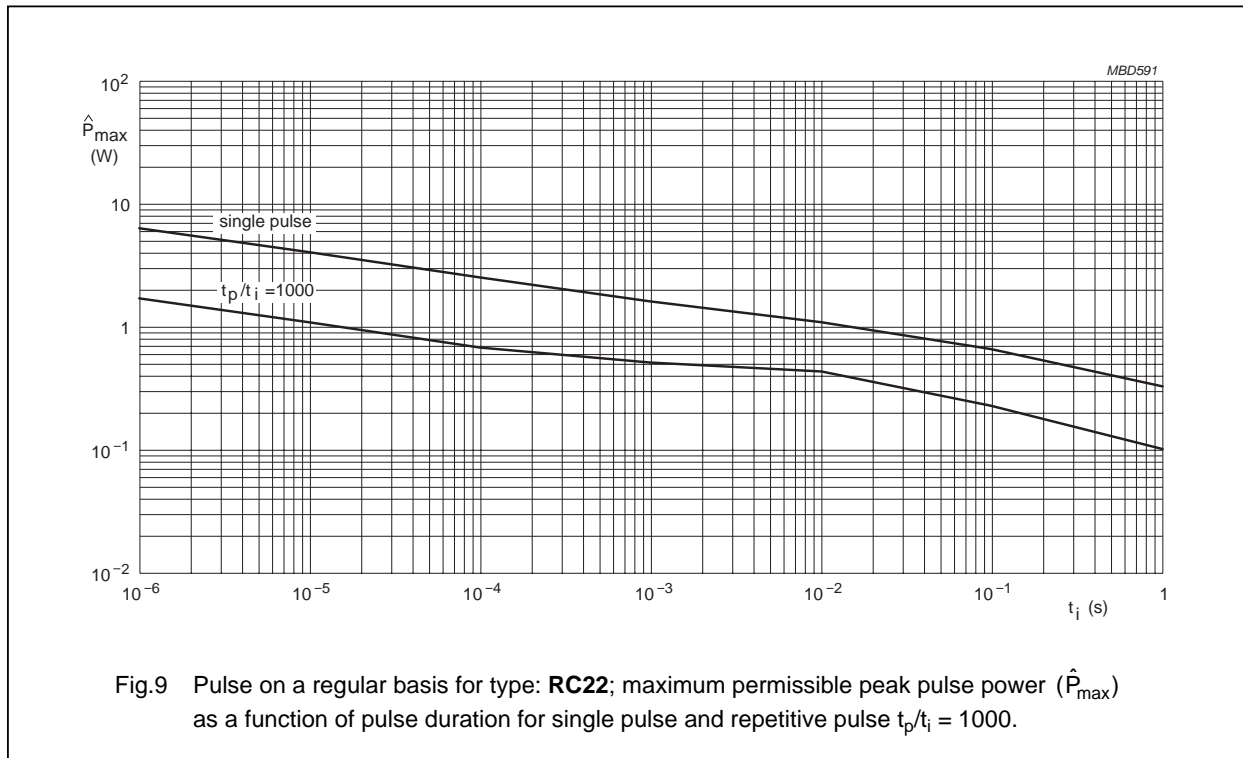


Fig.8 Pulse on a regular basis for type: **RC12**; maximum permissible peak pulse voltage ( $\hat{V}_{max}$ ) as a function of pulse duration.



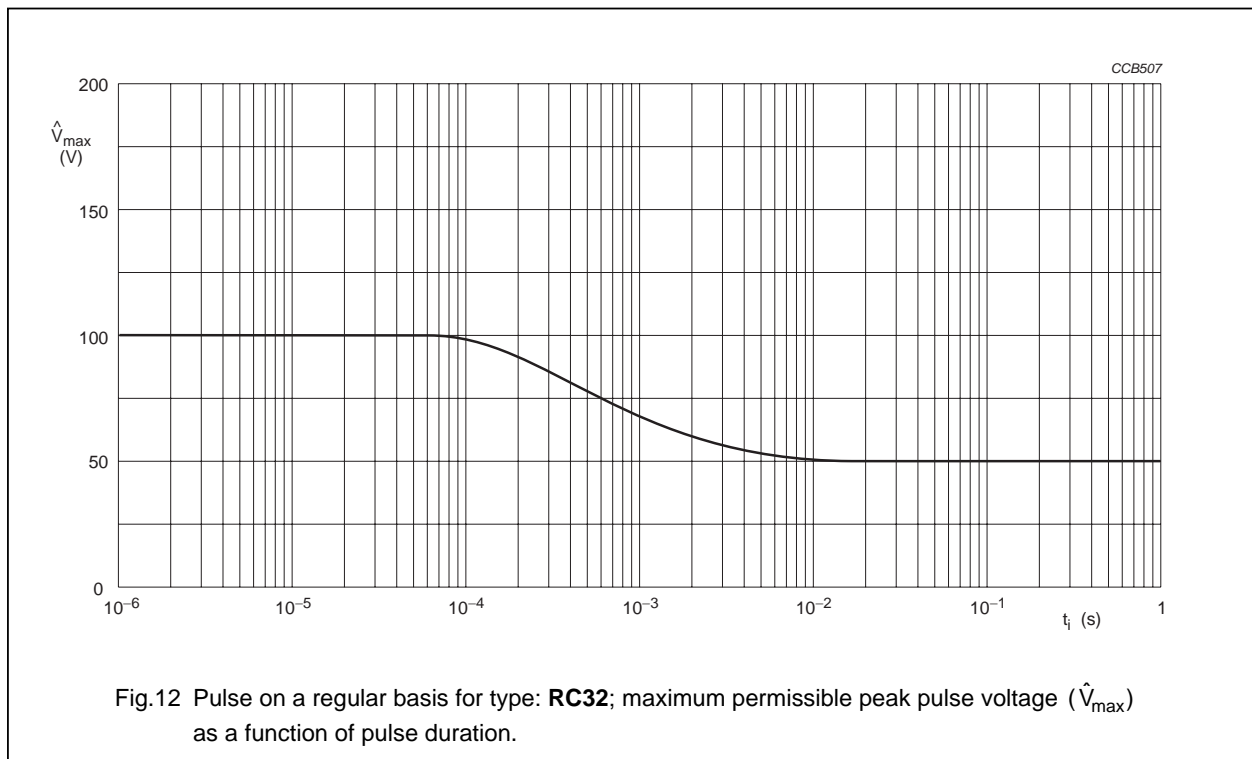
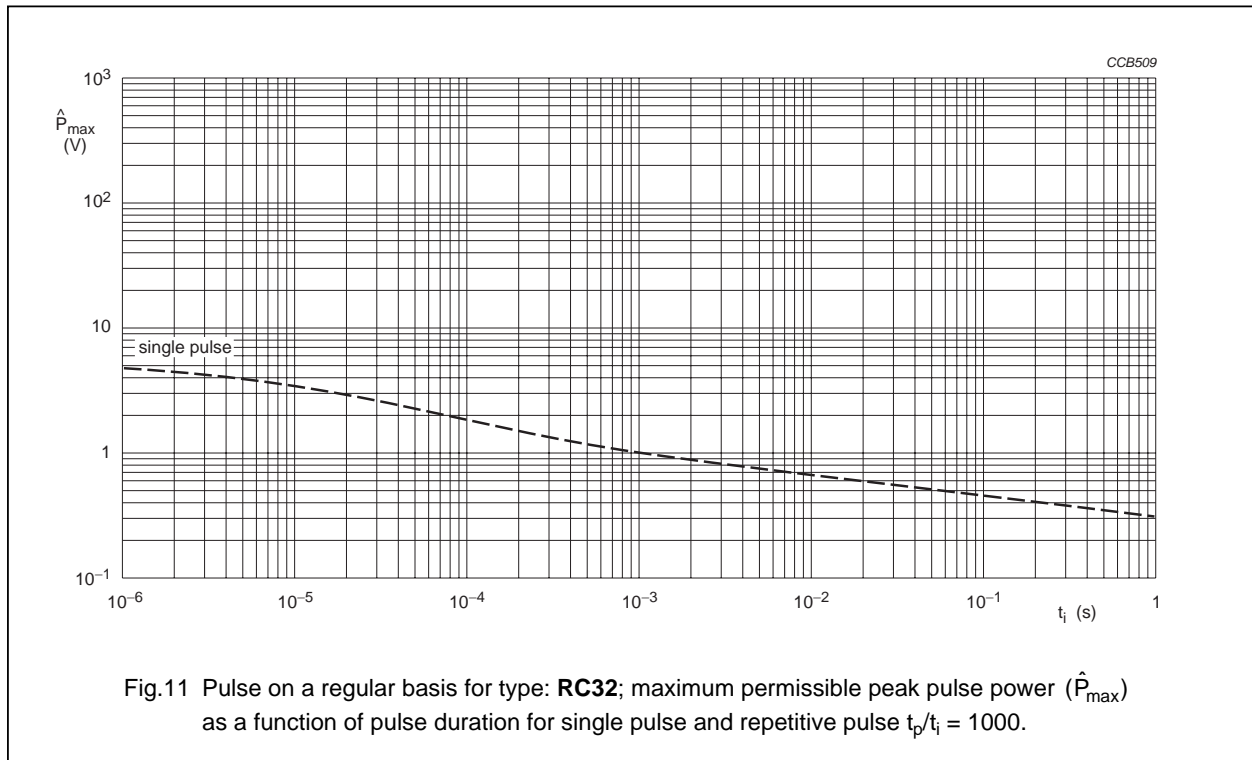
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### MECHANICAL DATA

#### Mass per 100 units

TYPE	MASS (g)
RC02	0.87
RC12	0.44
RC22	0.19
RC32	0.058

#### Marking

All resistors, except RC22 and RC32 are marked with a four-digit code on the protective coating to designate the nominal resistance value.

#### 4-DIGIT MARKING

For values of 1 k $\Omega$  or greater the first 3 digits apply to the resistance value and the fourth indicates the number of zeros to follow.

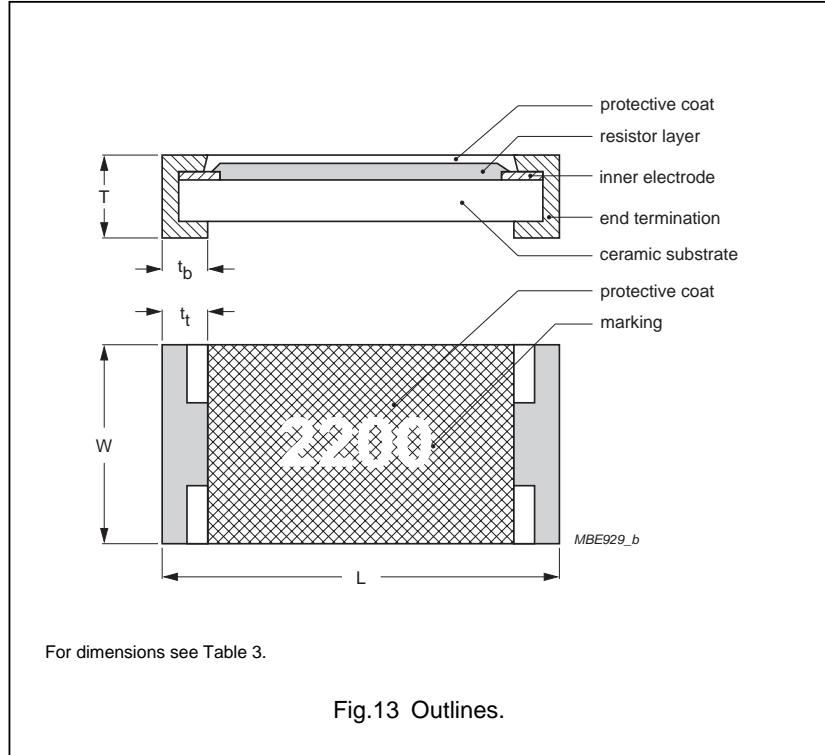
#### Example

MARKING	RESISTANCE
2200	220 $\Omega$
4021	4.02 k $\Omega$
1503	150 k $\Omega$

#### PACKAGE MARKING

The packing is marked with the resistance value, tolerance, catalogue number, quantity, production period, batch number and source code.

### OUTLINES



**Table 3** Chip resistor types and relevant physical dimensions; see Fig.13 ,14

TYPE	L (mm)	W (mm)	T (mm)	t <sub>t</sub> (mm)	t <sub>b</sub> (mm)
RC02	3.10 ±0.10	1.60 ±0.10	0.55 ±0.10	0.45 ±0.20	0.40 ±0.20
RC12	2.00 ±0.10	1.25 ±0.10	0.50 ±0.10	0.35 ±0.20	0.35 ±0.20
RC22	1.60 ±0.10	0.80 ±0.10	0.45 ±0.10	0.25 ±0.15	0.25 ±0.15
RC32	1.00 ±0.05	0.50 ±0.05	0.35 ±0.05	0.20 ±0.10	0.25 ±0.10

# Precision chip resistors sizes 1206, 0805, 0603 and 0402

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### Marking

RC32 is no marking.

RC22 is marked with a three-digit code on the protective coating to designate the nominal resistance value.

#### 3-DIGIT MARKING

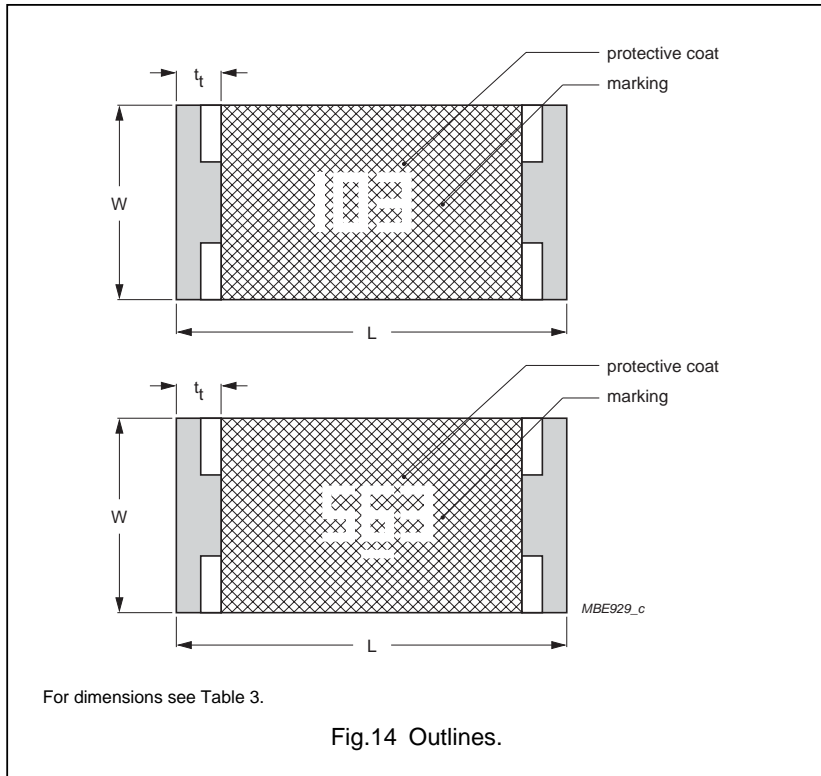
For E-96 series is marked with a 3 digits code for 0603  $\pm 1\%$  EIA-96 marking method; see Table 4.

The third character is a letter multiplier:

$$X=10^{-1}, Y=10^{-2}, A=10^0, B=10^1, \\ C=10^2, D=10^3, E=10^4, F=10^5$$

For 0603  $\pm 1\%$  E-24 series, first 2 digits for significant figure and 3rd digit indicates the number of zeros to follow and one short bar under marking letter.

### OUTLINES



**Table 4** EIA - 96 Marking Rule

Code	Value	Code	Value	Code	Value	Code	Value	Code	Value	Code	Value	Code	Value	Code	Value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41	261	53	348	55	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

### Note

1. Table 4 shows the first two digits of the three-digit EIA-96 part-marking scheme.

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### TESTS AND REQUIREMENTS

Essentially all tests are carried out in accordance with the schedule of "IEC publication 60115-8", category **LCT/UCT/56** (rated temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also covers the requirements specified by EIA and EIAJ.

The tests are carried out in accordance with IEC publication 60068, "Recommended basic

*climatic and mechanical robustness testing procedure for electronic components*" and under standard atmospheric conditions according to "IEC 60068-1", subclause 5.3.

Unless otherwise specified the following values apply:

Temperature: 15 °C to 35 °C

Relative humidity: 45% to 75%

Air pressure: 86 kPa to 106 kPa (860 mbar to 1060 mbar).

In Table 4 the tests and requirements are listed with reference to the relevant clauses of "IEC publications 60115-8 and 60068"; a short description of the test procedure is also given. In some instances deviations from the IEC recommendations were necessary for our method of specifying.

All soldering tests are performed with mildly activated flux.

**Table 5** Test procedures and requirements

IEC 60115-8 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS
<b>Tests in accordance with the schedule of IEC publication 60115-8</b>				
4.4.1		visual examination		no holes; clean surface; no visible damage
4.4.2		dimensions (see Fig.13)	gauge (mm)	see Table 3
4.5		resistance	applied voltage (+0/-10%) R < 10 Ω: 0.1 V 10 Ω ≤ R < 100 Ω: 0.3 V 100 Ω ≤ R < 1 kΩ: 1 V 1 kΩ ≤ R < 10 kΩ: 3 V 10 kΩ ≤ R < 100 kΩ: 10 V 100 kΩ ≤ R < 1 MΩ: 25 V R ≥ 1 MΩ: 50 V	R – R <sub>nom</sub> : max. ±1%
4.18	20 (Tb)	resistance to soldering heat	unmounted chips; 10 ±1 s; 260 ±5 °C <b>RC02H,</b> <b>RC12H, , RC22H</b> <b>RC32</b>	no visible damage ΔR/R max.: ±(0.5% + 0.05 Ω) ΔR/R max.: ±(0.5% + 0.05 Ω)
4.29	45 (Xa)	component solvent resistance	isopropyl alcohol or H <sub>2</sub> O followed by brushing in accordance with "MIL 202 F"	no visible damage
4.17	20 (Ta)	solderability	unmounted chips completely immersed for 2 ±0.5 s in a solder bath at 235 ±2 °C	good tinning (≥95% covered); no visible damage

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IEC 60115-8 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS
4.7		voltage proof on insulation	maximum voltage (RMS) during 1 minute, metal block method	no breakdown or flashover
4.13		short time overload	room temperature; $P = 6.25 \times P_n$ ; 5 s ( $V \leq 2 \times V_{max}$ ); <b>RC02H, RC12H, RC22H, RC32</b>	$\Delta R/R$ max.: $\pm(1\% + 0.05 \Omega)$
4.33		bending	resistors mounted on a 90 mm glass epoxy resin PCB (FR4); bending: <b>RC02H</b> , : 3 mm <b>RC12H, RC22H</b> : 5 mm <b>RC32</b> : 5 mm	no visible damage $\Delta R/R$ max.: $\pm(0.5\% + 0.05 \Omega)$ $\Delta R/R$ max.: $\pm(0.5\% + 0.05 \Omega)$ $\Delta R/R$ max.: $\pm(1\% + 0.05 \Omega)$
4.19	14 (Na)	rapid change of temperature	30 minutes at LCT and 30 minutes at UCT; 5 cycles <b>RC02H, RC12H, RC22H, RC32</b>	no visible damage $\Delta R/R$ max.: $\pm(0.5\% + 0.05 \Omega)$
4.24.2	3 (Ca)	damp heat (steady state)	56 days; $40 \pm 2$ °C; 93 $\pm 2$ -3% RH; loaded with $0.01 P_n$ : <b>RC02H, RC12H, RC22H</b> : <b>RC32</b> : $R \leq 1 M\Omega$	$\Delta R/R$ max.: $\pm(0.5\% + 0.05 \Omega)$ $\Delta R/R$ max.: $\pm(0.5\% + 0.05 \Omega)$
4.25.1		endurance	1000 $\pm 48$ -0 hours; $70 \pm 2$ °C; loaded with $P_n$ or $V_{max}$ ; 1.5 hours on, 0.5 hours off; <b>RC02H, RC12H, RC22H, RC32</b>	$\Delta R/R$ max.: $\pm(1\% + 0.05 \Omega)$
4.23.2	27 (Ba)	endurance at upper category temperature	1000 $\pm 48$ -0 hours; no load: <b>RC02H, RC12H, RC22H</b> : $R \leq 1 M\Omega$ <b>RC02H, RC12H, RC22H</b> : $R > 1 M\Omega$ <b>RC32</b> : $R \leq 1 M\Omega$	$\Delta R/R$ max.: $\pm(1\% + 0.05 \Omega)$ $\Delta R/R$ max.: $\pm(1.5\% + 0.05 \Omega)$ $\Delta R/R$ max.: $\pm(2\% + 0.1 \Omega)$
4.8.4.2		temperature coefficient	at 20/LCT/20 °C and 20/UCT/20 °C <b>RC02H, RC12H, RC22H, RC32</b> : $1 \Omega \leq R \leq 10 \Omega$ <b>RC02H, RC12H, RC22H</b> : $10 \Omega < R \leq 10 M\Omega$ <b>RC32</b> : $10 \Omega < R \leq 10 M\Omega$	$\leq 250 \pm 250$ $\leq \pm 100$ $\leq \pm 200$

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IEC 60115-8 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS
<b>Other tests in accordance with IEC 60115 clauses and IEC 60068 test method</b>				
4.17	20 (Ta)	solderability (after ageing)	8 hours steam or 16 hours 155 °C; unmounted chips completely immersed for $2 \pm 0.5$ s in a solder bath at $235 \pm 2$ °C	good tinning ( $\geq 95\%$ covered); no visible damage
4.6.1.1		insulation resistance	voltage (DC) after 1 minute, metal block method: <b>RC02H, RC12H:</b> 100 V, <b>RC22H, RC32:</b> 50 V	$R_{ins}$ min.: $10^4$ M $\Omega$
4.12		noise	IEC publication 60195 (measured with Quantech-equipment) $R \leq 100 \Omega$ $100 \Omega < R \leq 1 \text{ k}\Omega$ $1 \text{ k}\Omega < R \leq 10 \text{ k}\Omega$ $10 \text{ k}\Omega < R \leq 100 \text{ k}\Omega$ $100 \text{ k}\Omega < R \leq 1 \text{ M}\Omega$ $1 \text{ M}\Omega < R \leq 10 \text{ M}\Omega$	max. 0.316 $\mu\text{V/V}$ (-10 dB) max. 1 $\mu\text{V/V}$ (0 dB) max. 3 $\mu\text{V/V}$ (9.54 dB) max. 6 $\mu\text{V/V}$ (15.56 dB) max. 10 $\mu\text{V/V}$ (20 dB) max. 32 $\mu\text{V/V}$ (30.10 dB)
<b>Other applicable tests</b>				
(JIS) C 5202 7.9		endurance (under damp and load)	1000 +48/-0 hours; $40 \pm 2$ °C; 93 +2/-3% RH; loaded with $P_n$ or $V_{max}$ ; 1.5 hours on and 0.5 hours off $R \leq 1 \text{ M}\Omega$ $R > 1 \text{ M}\Omega$	$\Delta R/R$ max.: $\pm(2\% + 0.1 \Omega)$ $\Delta R/R$ max.: $\pm(3\% + 0.1 \Omega)$
EIA 573 3.13		leaching	unmounted chips: $60 \pm 1$ s; $260 \pm 5$ °C	good tinning; no leaching
EIA/IS 703 4.5		load humidity	1000 +48/-0 hours; $85 \pm 2$ °C; 85 +5% RH; loaded with $00.1 P_n$ or $V_{max}$ ; $R \leq 1 \text{ M}\Omega$ $R > 1 \text{ M}\Omega$	$\Delta R/R$ max.: $\pm(2\% + 0.1 \Omega)$ $\Delta R/R$ max.: $\pm(3\% + 0.1 \Omega)$

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1%****REVISION HISTORY**

<b>Revision</b>	<b>Date</b>	<b>Change Notification</b>	<b>Description</b>
Rev.8	2001 Mar 07	-	- Converted to Phycomp brand - Pulse duration limit for $R \leq 10 \text{ k}\Omega$ removed in Figs.5, 7, 9 and 11
Rev.9	2002 Apr 09	-	- Maximum dissipation for RC22H changed from 0.063 W/0.1 W to 0.1 W
Rev.10	2003 Jul 28	-	- Updated company logo - Marking for RC22 added and - RC02G and RC12G series phased out - Test condition revised