Surface Mount Schottky Power Rectifier

POWERMITE® Power Surface Mount Package

The Schottky Powermite[®] employs the Schottky Barrier principle with a barrier metal and epitaxial construction that produces optimal forward voltage drop–reverse current tradeoff. The advanced packaging techniques provide for a highly efficient micro miniature, space saving surface mount Rectifier. With its unique heatsink design, the Powermite[®] has the same thermal performance as the SMA while being 50% smaller in footprint area, and delivering one of the lowest height profiles, < 1.1 mm in the industry. Because of its small size, it is ideal for use in portable and battery powered products such as cellular and cordless phones, chargers, notebook computers, printers, PDAs and PCMCIA cards. Typical applications are AC–DC and DC–DC converters, reverse battery protection, and "ORing" of multiple supply voltages and any other application where performance and size are critical.

Features

- Ultra Low V_F
- 1st in Marketplace with a 10 V_R Schottky Rectifier
- Low Profile Maximum Height of 1.1 mm
- Small Footprint Footprint Area of 8.45 mm²
- Low Thermal Resistance with Direct Thermal Path of Die on Exposed Cathode Heat Sink
- ESD Protection: Human Body Model >4000 V (Class 3) Machine Model >400 V (Class C)
- Pb-Free Packages are Available

Mechanical Characteristics:

- Powermite® is JEDEC Registered as D0-216AA
- Case: Molded Epoxy
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 62 mg (Approximately)
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Maximum for 10 Seconds



ON Semiconductor®

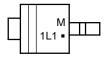
http://onsemi.com

SCHOTTKY BARRIER RECTIFIER 1.0 AMPERES, 10 VOLTS



POWERMITE CASE 457 PLASTIC

MARKING DIAGRAM



M = Date Code1L1 = Device Code■ Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
MBRM110LT1	POWERMITE	3000/Tape & Reel
MBRM110LT1G	POWERMITE (Pb-Free)	3000/Tape & Reel
MBRM110LT3	POWERMITE	12000/Tape & Reel
MBRM110LT3G	POWERMITE (Pb-Free)	12000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	10	V
Average Rectified Forward Current (T _L = 115°C, R _{θJL} = 35°C/W)	Io	1.0	Α
Non-Repetitive Peak Surge Current (Non-Repetitive peak surge current, halfwave, single phase, 60 Hz)	I _{FSM}	50	А
Storage Temperature	T _{stg}	-55 to 125	°C
Operating Junction Temperature	TJ	-55 to 125	°C
Voltage Rate of Change (Rated V _R , T _J = 25°C)	dv/dt	10,000	V/μs

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance, Junction-to-Lead (Anode) (Note 1)	R _{til}	35	°C/W
Thermal Resistance, Junction-to-Tab (Cathode) (Note 1)	R _{tjtab}	23	
Thermal Resistance, Junction-to-Ambient (Note 1)	Ŕ _{tja}	277	

^{1.} Mounted with minimum recommended pad size, PC Board FR4, See Figures 8 and 9.

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (Note 2)	V _F	T _J = 25°C	T _J = 100°C	V
$(I_F = 0.1 \text{ A})$ $(I_F = 1.0 \text{ A})$ $(I_F = 2.0 \text{ A})$		0.280 0.365 0.415	0.175 0.275 0.325	
Maximum Instantaneous Reverse Current (Note 2)	I _R	T _J = 25°C	T _J = 100°C	mA
$(V_R = 5.0 \text{ V})$ $(V_R = 10 \text{ V})$		0.2 0.5	30 60	

^{2.} Pulse Test: Pulse Width ≤ 250 μs, Duty Cycle ≤ 2%.

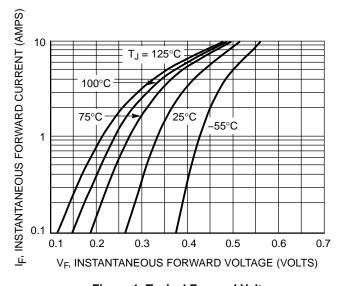


Figure 1. Typical Forward Voltage

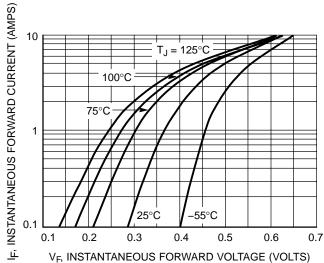


Figure 2. Maximum Forward Voltage

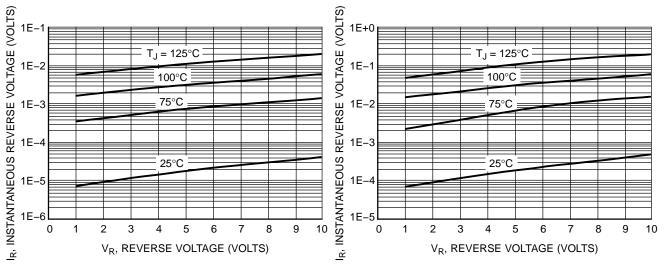


Figure 3. Typical Reverse Current

Figure 4. Maximum Reverse Current

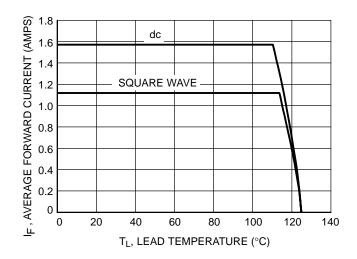


Figure 5. Current Derating - Junction to Lead

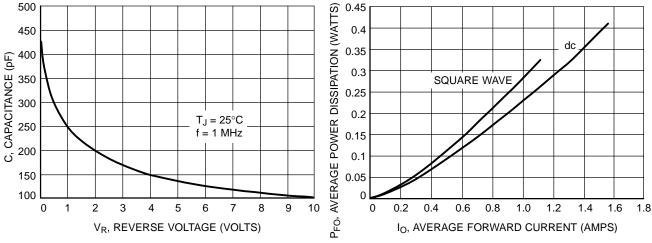


Figure 6. Typical Capacitance

Figure 7. Forward Power Dissipation

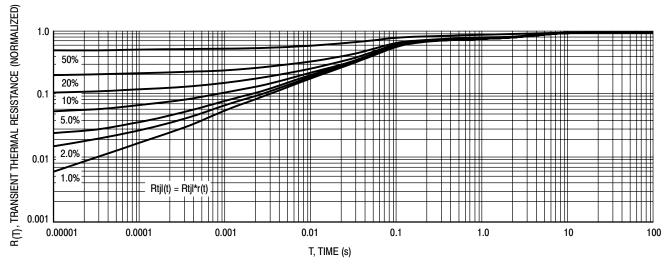


Figure 8. Thermal Response Junction to Lead

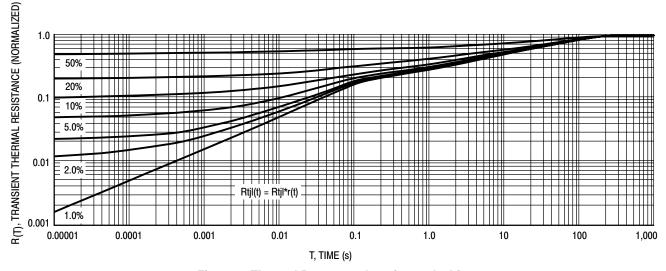
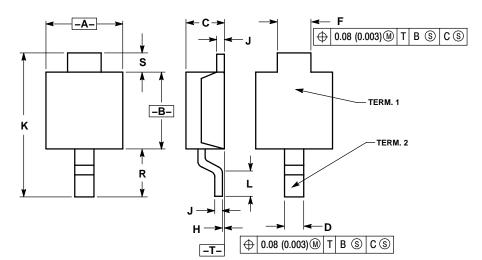


Figure 9. Thermal Response Junction to Ambient

PACKAGE DIMENSIONS

POWERMITE

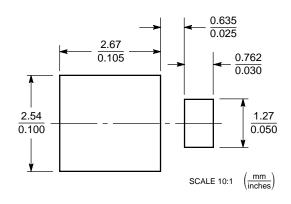
CASE 457-04 ISSUE D



- 11. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER.
- DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	1.75	2.05	0.069	0.081
В	1.75	2.18	0.069	0.086
C	0.85	1.15	0.033	0.045
D	0.40	0.69	0.016	0.027
F	0.70	1.00	0.028	0.039
Н	-0.05	+0.10	-0.002	+0.004
J	0.10	0.25	0.004	0.010
K	3.60	3.90	0.142	0.154
L	0.50	0.80	0.020	0.031
R	1.20	1.50	0.047	0.059
S	0.50 RFF		0.010	RFF

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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