

54LS245/DM54LS245/DM74LS245 TRI-STATE® Octal Bus Transceiver

General Description

These octal bus transceivers are designed for asynchronous two-way communication between data buses. The control function implementation minimizes external timing requirements.

The device allows data transmission from the A bus to the B bus or from the B bus to the A bus depending upon the logic level at the direction control (DIR) input. The enable input ($\bar{G}$) can be used to disable the device so that the buses are effectively isolated.

Features

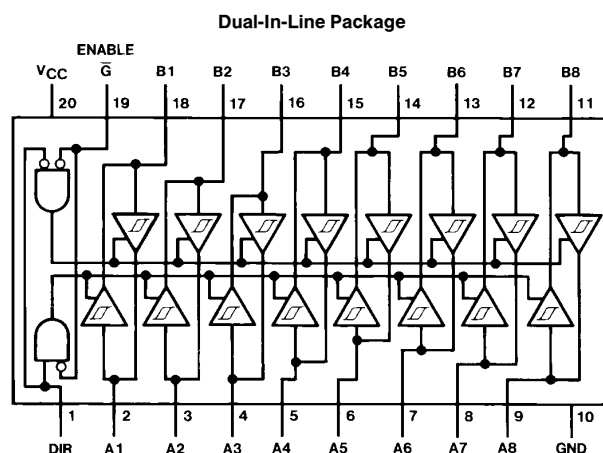
- Bi-Directional bus transceiver in a high-density 20-pin package
- TRI-STATE outputs drive bus lines directly

- PNP inputs reduce DC loading on bus lines
- Hysteresis at bus inputs improve noise margins
- Typical propagation delay times, port-to-port 8 ns
- Typical enable/disable times 17 ns

- I_{OL} (sink current)
 - 54LS 12 mA
 - 74LS 24 mA
- I_{OH} (source current)
 - 54LS -12 mA
 - 74LS -15 mA

- Alternate Military/Aerospace device (54LS245) is available. Contact a National Semiconductor Sales Office/Distributor for specifications.

Connection Diagram



TL/F/6413-1

Order Number 54LS245DMQB, 54LS245FMQB, 54LS245LMQB,
DM54LS245J, DM54LS245W, DM74LS245WM or DM74LS245N
See NS Package Number E20A, J20A, M20B, N20A or W20A

Function Table

Enable $\bar{G}$	Direction Control DIR	Operation
L	L	B data to A bus
L	H	A data to B bus
H	X	Isolation

H = High Level, L = Low Level, X = Irrelevant

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Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	
DIR or $\overline{G}$	7V
A or B	5.5V
Operating Free Air Temperature Range	
DM54LS and 54LS	−55°C to +125°C
DM74LS	0°C to +70°C
Storage Temperature Range	−65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54LS245			DM74LS245			Units
		Min	Nom	Max	Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.7			0.8	V
I _{OH}	High Level Output Current			−12			−15	mA
I _{OL}	Low Level Output Current			12			24	mA
T _A	Free Air Operating Temperature	−55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = −18 mA				−1.5	V
HYS	Hysteresis (V _{T+} − V _{T−})	V _{CC} = Min		0.2	0.4		V
V _{OH}	High Level Output Voltage	V _{CC} = Min, V _{IH} = Min V _{IL} = Max, I _{OH} = −1 mA	DM74	2.7			V
		V _{CC} = Min, V _{IL} = Min V _{IL} = Max, I _{OH} = −3 mA	DM54/DM74	2.4	3.4		
		V _{CC} = Min, V _{IH} = Min V _{IL} = 0.5V, I _{OH} = Max	DM54/DM74	2			
V _{OL}	Low Level Output Voltage	V _{CC} = Min V _{IL} = Max V _{IH} = Min	I _{OL} = 12 mA I _{OL} = Max	DM74 DM54 DM74		0.4 0.4 0.5	V
I _{OZH}	Off-State Output Current, High Level Voltage Applied	V _{CC} = Max V _{IL} = Max V _{IH} = Min	V _O = 2.7V			20	μA
I _{OZL}	Off-State Output Current, Low Level Voltage Applied		V _O = 0.4V			−200	μA
I _I	Input Current at Maximum Input Voltage	V _{CC} = Max	A or B DIR or $\overline{G}$	V _I = 5.5V V _I = 7V		0.1 0.1	mA
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.7V				20	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.4V				−0.2	mA
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 2)		−40		−225	mA
I _{CC}	Supply Current	Outputs High	V _{CC} = Max		48	70	mA
		Outputs Low			62	90	
		Outputs at Hi-Z			64	95	

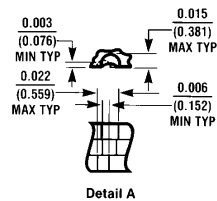
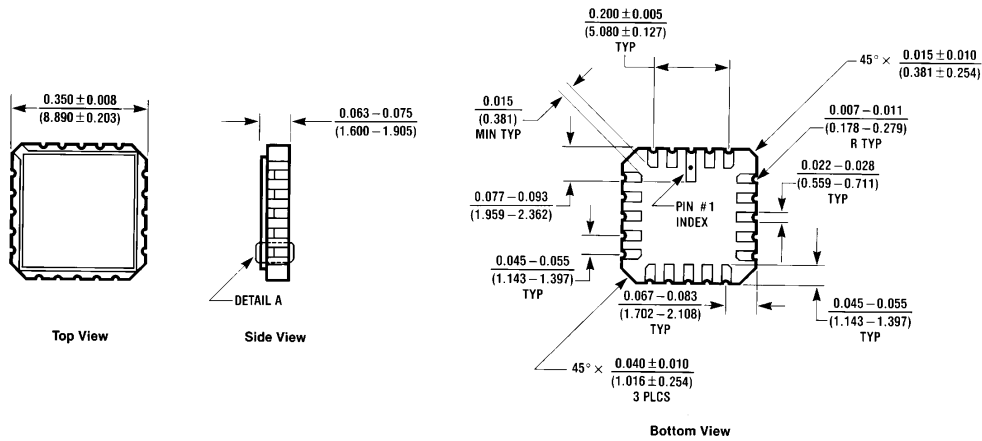
Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time, not to exceed one second duration

Switching Characteristics $V_{CC} = 5V$, $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

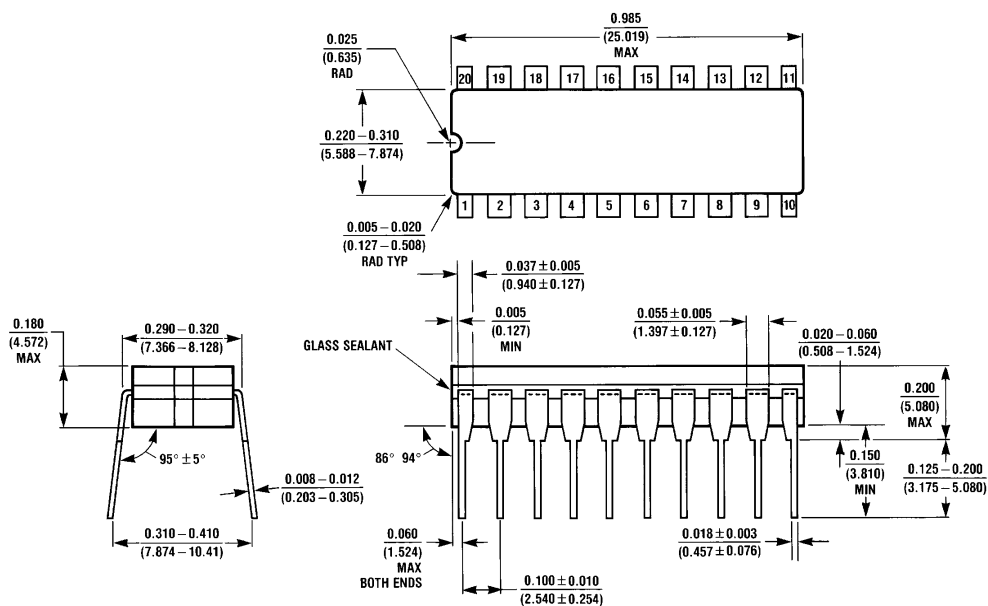
Symbol	Parameter	Conditions	DM54/74		Units
			LS245		
			Min	Max	
t _{PLH}	Propagation Delay Time, Low-to-High-Level Output	C _L = 45 pF R _L = 667Ω		12	ns
t _{PHL}	Propagation Delay Time, High-to-Low-Level Output			12	ns
t _{PZL}	Output Enable Time to Low Level			40	ns
t _{PZH}	Output Enable Time to High Level			40	ns
t _{PLZ}	Output Disable Time from Low Level	C _L = 5 pF R _L = 667Ω		25	ns
t _{PHZ}	Output Disable Time from High Level			25	ns
t _{PLH}	Propagation Delay Time, Low-to-High-Level Output	C _L = 150 pF R _L = 667Ω		16	ns
t _{PHL}	Propagation Delay Time, High-to-Low-Level Output			17	ns
t _{PZL}	Output Enable Time to Low Level			45	ns
t _{PZH}	Output Enable Time to High Level			45	ns

Physical Dimensions inches (millimeters)



Ceramic Leadless Chip Carrier Package (E)
Order Number 54LS245LMQB
NS Package Number E20A

E20A (REV D)



20-Lead Ceramic Dual-In-Line Package (J)
Order Number 54LS245DMQB or DM54LS245J
NS Package Number J20A

J20A (REV M)

[illegible]

Technical drawing of a 10-pin connector. The drawing includes two views: a top view and a side view. The top view shows a rectangular component with 10 pins on the bottom and 10 pins on the top. The pins are numbered 1 through 10. The top view includes dimensions for the overall length (1.013-1.040 (25.73-26.42)), the pin pitch (0.092 ± 0.030 (2.337 ± 0.762) MAX DP), and the pin diameter (0.032 ± 0.005 (0.813 ± 0.127) RAD). The side view shows the profile of the connector with dimensions for the pin height (0.065 (1.651)), the pin pitch (0.060 (1.524) TYP), the pin diameter (0.032 ± 0.005 (0.813 ± 0.127) RAD), and the overall height (0.145-0.200 (3.683-5.080)). The side view also shows the pin angle (90° ± 0.004°) and the pin diameter (0.020 (0.508) MIN). The drawing includes callouts for 'OPTION 1' and 'OPTION 2'.

0.092 ± 0.030
(2.337 ± 0.762)
MAX DP

PIN NO. 1 IDENT

OPTION 1

1.013-1.040
(25.73-26.42)

20 19 18 17 16 15 14 13 12 11

0.260 ± 0.005
(6.604 ± 0.127)

0.032 ± 0.005
(0.813 ± 0.127)
RAD

PIN NO. 1 IDENT

OPTION 2

1 2 3 4 5 6 7 8 9 10

0.090
(2.286)

0.060 NOM
(1.524)
TYP

0.040
(1.016)
TYP

OPTION 2
4° (4X)

0.130 0.005
(3.302 ± 0.127)

0.065
(1.651)

0.009-0.015
(0.229-0.381)
TYP

0.060 ± 0.005
(1.524 ± 0.127)

0.100 ± 0.010
(2.540 ± 0.254)

0.018 ± 0.003
(0.457 ± 0.076)

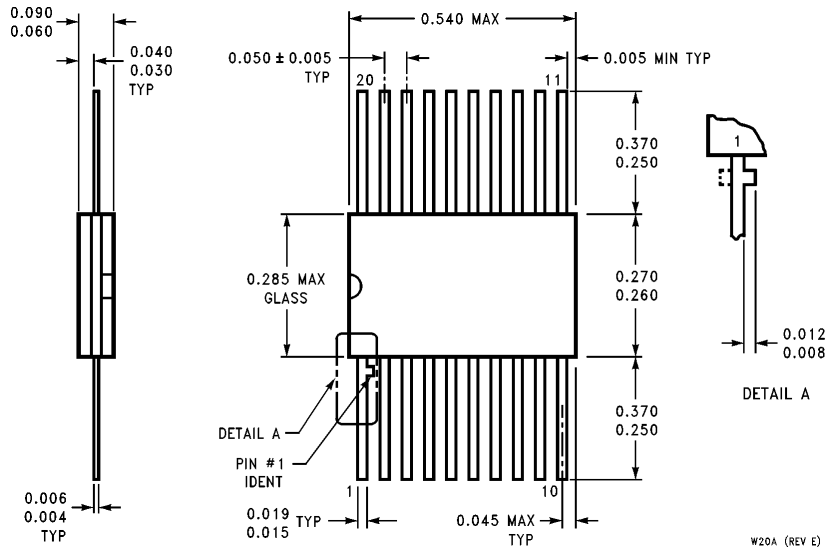
90° ± 0.004°

0.125-0.140
(3.175-3.556)

0.020
(0.508)
MIN

0.145-0.200
(3.683-5.080)

N20A (REV G)

Physical Dimensions inches (millimeters) (Continued)

20-Lead Ceramic Flat Package (W)
Order Number 54LS245FMQB or DM54LS245W
NS Package Number W20A

LIFE SUPPORT POLICY

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



National Semiconductor Corporation
 1111 West Bardin Road
 Arlington, TX 76017
 Tel: 1(800) 272-9959
 Fax: 1(800) 737-7018

National Semiconductor Europe
 Fax: (+49) 0-180-530 85 86
 Email: cnjwge@tevm2.nsc.com
 Deutsch Tel: (+49) 0-180-530 85 85
 English Tel: (+49) 0-180-532 78 32
 Français Tel: (+49) 0-180-532 93 58
 Italiano Tel: (+49) 0-180-534 16 80

National Semiconductor Hong Kong Ltd.
 19th Floor, Straight Block,
 Ocean Centre, 5 Canton Rd.
 Tsimshatsui, Kowloon
 Hong Kong
 Tel: (852) 2737-1600
 Fax: (852) 2736-9960

National Semiconductor Japan Ltd.
 Tel: 81-043-299-2309
 Fax: 81-043-299-2408

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