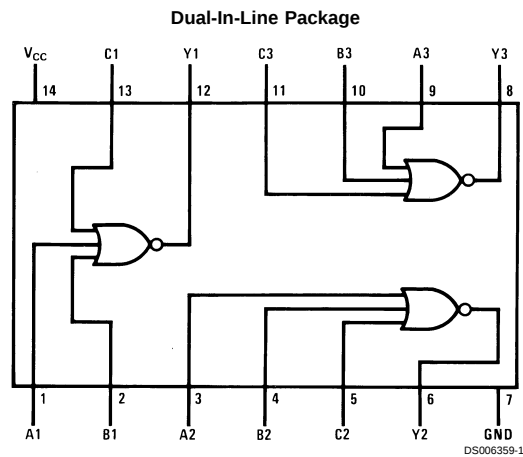


DM74LS27 Triple 3-Input NOR Gates

General Description

This device contains three independent gates each of which performs the logic NOR function.

Connection Diagram



Order Number DM54LS27J, DM54LS27W,
DM54LS27E, DM74LS27M or DM74LS27N
See Package Number E20A, J14A, M14A, N14A or W14B

Function Table

$$Y = \overline{A + B + C}$$

Inputs			Output
A	B	C	Y
L	L	L	H
X	X	H	L
X	H	X	L
H	X	X	L

H = High Logic Level
L = Low Logic Level
X = Either Low or High Logic Level

Absolute Maximum Ratings (Note 1)

Supply Voltage

Input Voltage

Operating Free Air Temperature Range

7V

7V

DM54LS and 54LS

DM74LS

Storage Temperature Range

-55°C to +125°C

0°C to +70°C

-65°C to +150°C

Recommended Operating Conditions

Symbol	Parameter	DM54LS27			DM74LS27			Units
		Min	Nom	Max	Min	Nom	Max	
V_{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V_{IH}	High Level Input Voltage	2			2			V
V_{IL}	Low Level Input Voltage			0.7			0.8	V
I_{OH}	High Level Output Current			-0.4			-0.4	mA
I_{OL}	Low Level Output Current			4			8	mA
T_A	Free Air Operating Temperature	-55		125	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
V_I	Input Clamp Voltage	$V_{CC} = \text{Min}, I_I = -18 \text{ mA}$			-1.5	V
V_{OH}	High Level Output Voltage	$V_{CC} = \text{Min}, I_{OH} = \text{Max}, V_{IL} = \text{Max}$	DM54 2.5			V
			DM74 2.7	3.4		
V_{OL}	Low Level Output Voltage	$V_{CC} = \text{Min}, I_{OL} = \text{Max}, V_{IH} = \text{Min}$	DM54		0.4	V
			DM74	0.35	0.5	
		$I_{OL} = 4 \text{ mA}, V_{CC} = \text{Min}$	DM74	0.25	0.4	
I_I	Input Current @ Max Input Voltage	$V_{CC} = \text{Max}, V_I = 7 \text{ V}$			0.1	mA
I_{IH}	High Level Input Current	$V_{CC} = \text{Max}, V_I = 2.7 \text{ V}$			20	μA
I_{IL}	Low Level Input Current	$V_{CC} = \text{Max}, V_I = 0.4 \text{ V}$			-0.36	mA
I_{OS}	Short Circuit Output Current	$V_{CC} = \text{Max}$	DM54	-20	-100	mA
		(Note 3)	DM74	-20	-100	
I_{CCH}	Supply Current with Outputs High	$V_{CC} = \text{Max}$		2	4	mA
I_{CCL}	Supply Current with Outputs Low	$V_{CC} = \text{Max}$		3.4	6.8	mA

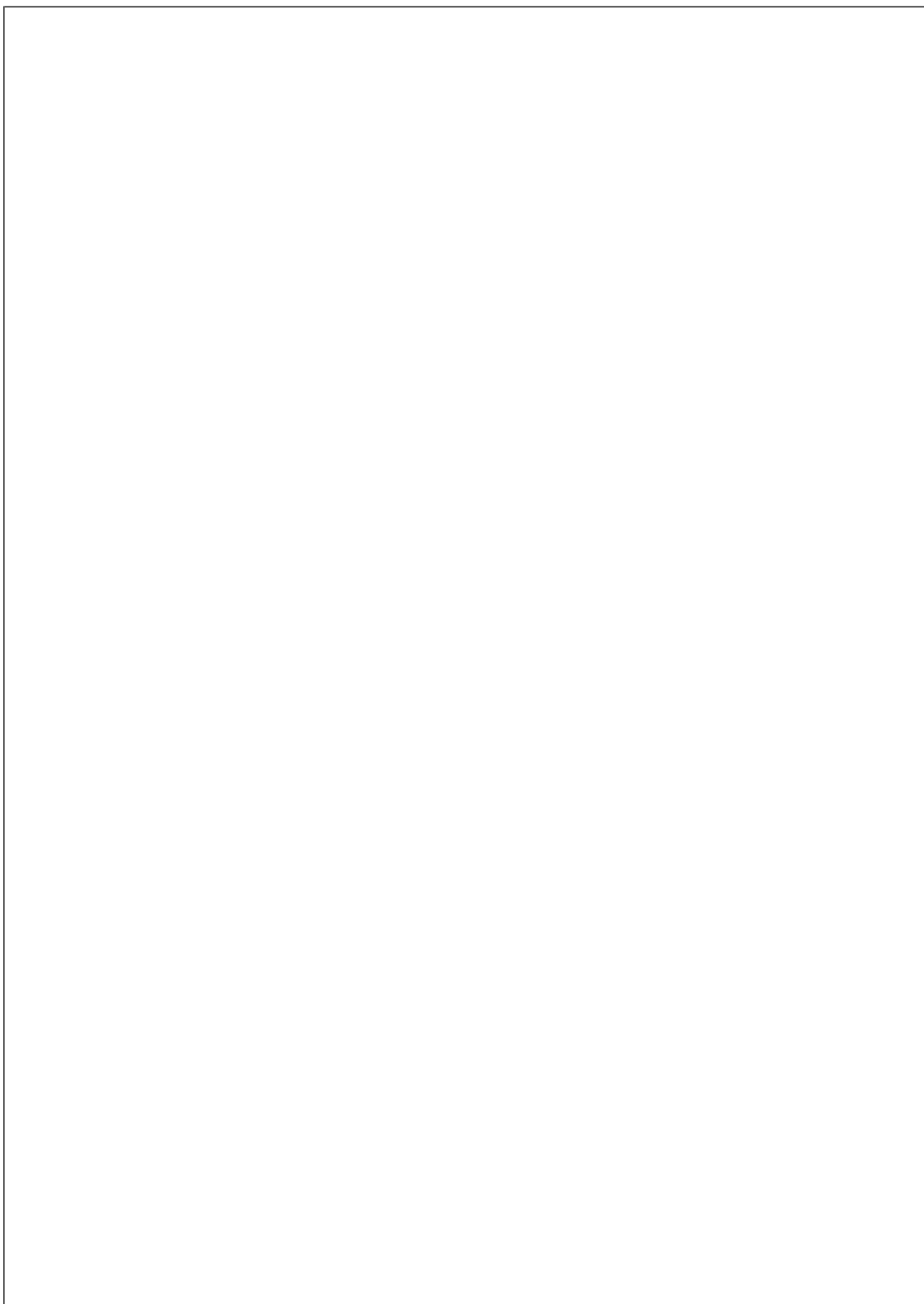
Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^\circ C$

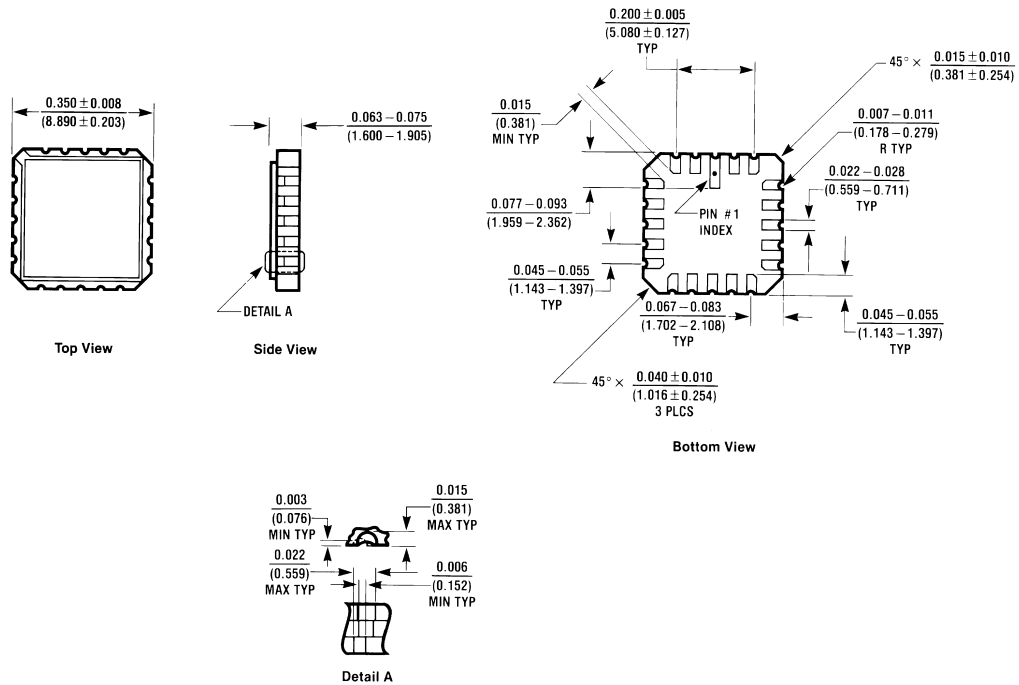
Symbol	Parameter	DM54		DM74				Units
		R _L = 2 kΩ						
		C _L = 15 pF		C _L = 15 pF		C _L = 50 pF		
		Min	Max	Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time Low to High Level Output	3	13	3	13	5	18	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	3	13	3	10	4	15	ns

Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25^\circ C$.

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.



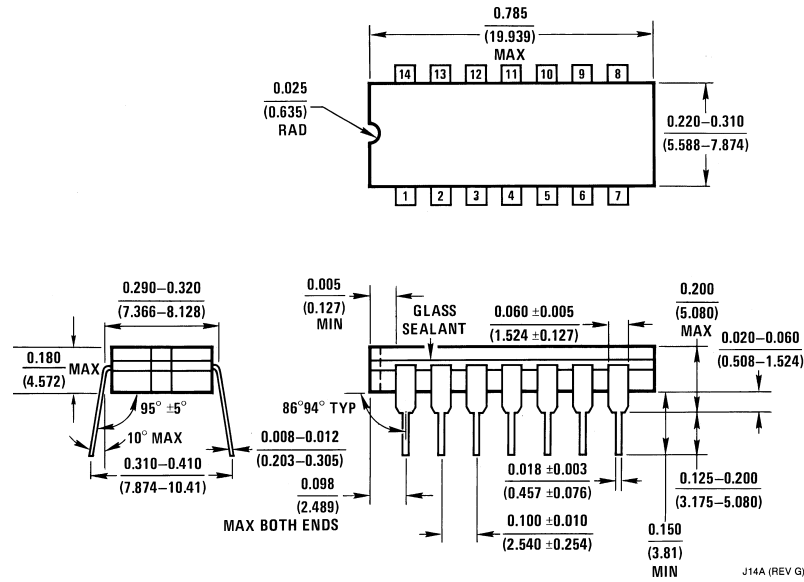
Physical Dimensions inches (millimeters) unless otherwise noted



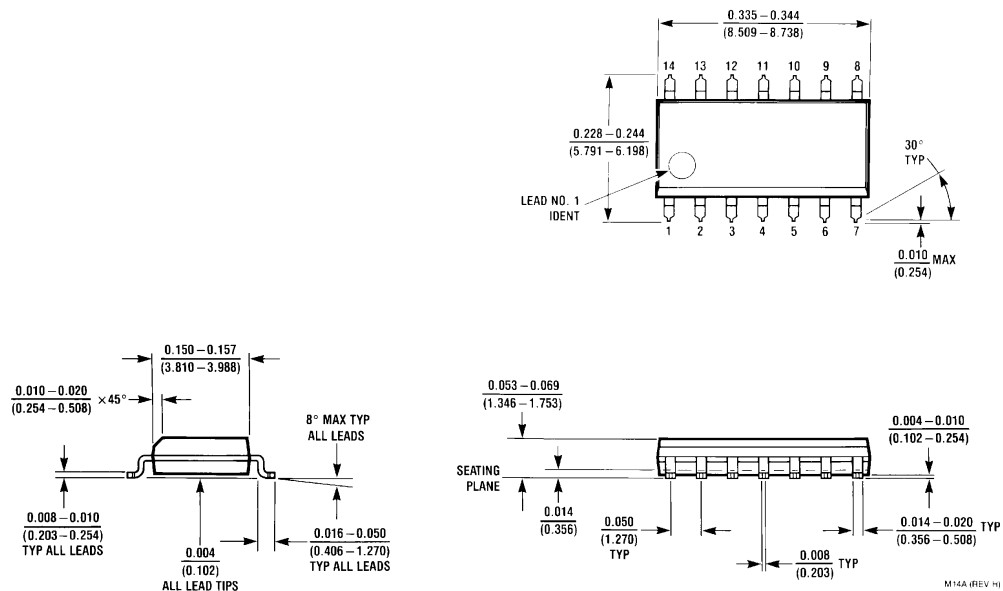
Ceramic Leadless Chip Carrier Package (E)
Order Number DM54LS27E
Package Number E20A

E20A (REV D)

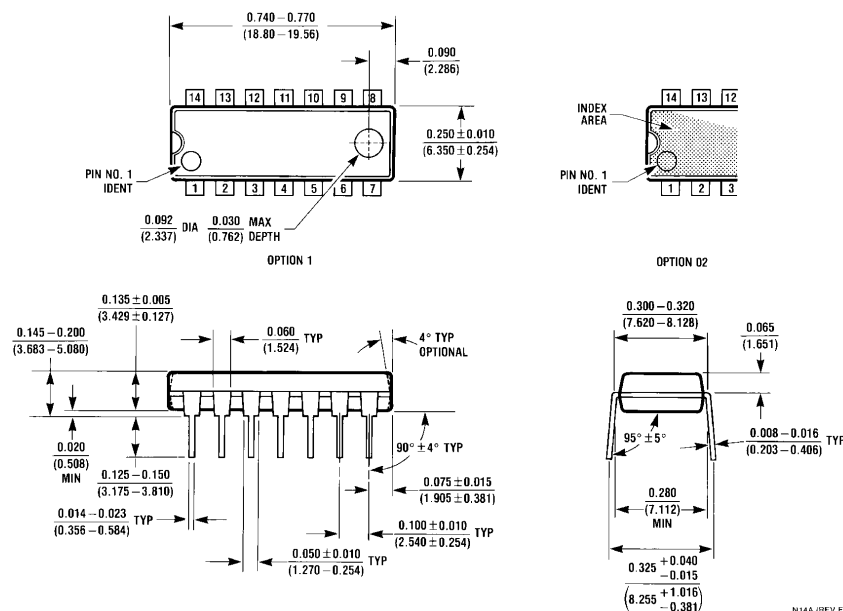
Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



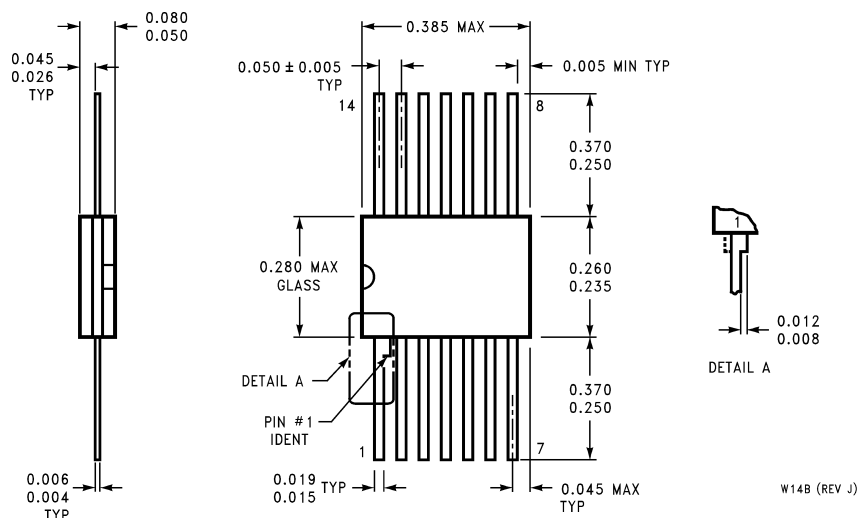
14-Lead Ceramic Dual-In-Line Package (J)
Order Number DM54LS27J
Package Number J14A



14-Lead Small Outline Molded Package (M)
Order Number DM74LS27M
Package Number M14A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)

14-Lead Molded Dual-In-Line Package (N)
Order Number DM74LS27N
Package Number N14A



14-Lead Ceramic Flat Package (W)
Order Number DM54LS27W
Package Number W14B

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