MOTOROLA SEMICONDUCTOR TECHNICAL DATA

Highly Flexible Voltage Comparators

The ability to operate from a single power supply of 5.0 V to 30 V or \pm 15 V split supplies, as commonly used with operational amplifiers, makes the LM111/LM211/LM311 a truly versatile comparator. Moreover, the inputs of the device can be isolated from system ground while the output can drive loads referenced either to ground, the V_{CC} or the V_{EE} supply. This flexibility makes it possible to drive DTL, RTL, TTL, or MOS logic. The output can also switch voltages to 50 V at currents to 50 mA. Thus the LM111/LM211/LM311 can be used to drive relays, lamps or solenoids.



LM111

LM211

LM311

HIGH PERFORMANCE VOLTAGE COMPARATORS

> SILICON MONOLITHIC INTEGRATED CIRCUIT

> > N SUFFIX

PLASTIC PACKAGE

CASE 626

MOTOROLA LINEAR/INTERFACE ICs DEVICE DATA

MAXIMUM RATINGS (T_A = +25°C, unless otherwise noted.)

| Rating | Symbol | LM111/LM211 | LM311 | Unit | |
|---|-----------------------------------|--|---------------------------------------|--|------------------|
| Total Supply Voltage | V _{CC} + V _{EE} | 36 | 36 | Vdc | |
| Output to Negative Supply Voltage | VO-VEE | 50 | 40 | Vdc | |
| Ground to Negative Supply Voltage | VEE | 30 | 30 | Vdc | |
| Input Differential Voltage | VID | ±30 | ±30 | Vdc | |
| Input Voltage (Note 2) | Vin | ±15 | ±15 | Vdc | |
| Voltage at Strobe Pin | | V _{CC} to V _{CC} -5 | V _{CC} to V _{CC} -5 | Vdc | |
| Power Dissipation and Thermal Characteristics Plastic and Ceramic Dual-In-Line Packages Derate Above $T_A = +25^{\circ}C$ | Р _D 1/θЈА | 62 5 | 25 .0 | mW mW/°C | 1910 |
| Operating Ambient Temperature Range | TA | -55 to +125 -25 to +85 | 0 to +70 | °C | 10 ^{fr} |
| Operating Junction Temperature | T _{J(max)} | +150 | +150 | °C | |
| Storage Temperature Range | T _{stg} | -65 to +150 | -65 to +150 | °C | |
| | • • - • - • - • - • - • - • - • | ······································ | • | the second secon | - |

ELECTRICAL CHARACTERISTICS ($V_{CC} = +15 \text{ V}$, $V_{EE} = -15 \text{ V}$, $T_A = 25^{\circ}\text{C}$, unless otherwise noted [Note 1].)

| | | LM111/LM211 | | | di. | | | |
|--|-----------------|-------------|------------------|----------------|--------|------------------|------------|----------------|
| Characteristics | Symbol | Min | Тур 🔬 | Max | Min | Тур | Max | Unit |
| Input Offset Voltage (Note 3) $R_{S} \leq 50 \text{ k}\Omega, T_{A} = +25^{\circ}C$ $R_{S} \leq 50 \text{ k}\Omega, T_{Iow} \leq T_{A} \leq T_{high}^{*}$ | V _{IO} | | 0.7 | 3.0 4.0 | | 2.0 | 7.5 10 | mV |
| Input Offset Current (Note 3) $T_A = +25^{\circ}C$ $T_{Iow} \le T_A \le T_{high}^*$ | lio | - 4 | 1.7 | 10 20 | | 1.7 | 50 70 | nA |
| Input Bias Current $T_A = +25^{\circ}C$ $T_{Iow} \le T_A \le T_{high}^*$ | l IB | | 45 — | 100 150 | | 45 — | 250 300 | nA |
| Voltage Gain | Av | 4 0 | 200 | — | 40 | 200 | | V/mV |
| Response Time (Note 4) | | | 200 | | — | 200 | _ | ns |
| Saturation Voltage $V_{ID} \le -5.0 \text{ mV}, I_O = 50 \text{ mA}, T_A = 25^{\circ}\text{C}$ $V_{ID} \le -10 \text{ mV}, I_O = 50 \text{ mA}, T_A = 25^{\circ}\text{C}$ $V_{CC} \ge 4.5 \text{ V}, V = 0, T_{IOW} \le T_A \le T_{Pirch}^{*}$ | VØL | _ | 0.75 | 1.5 — | | 0.75 | 1.5 | V |
| V _{ID} ∠≤6.0 mV, I _{sink} ≤ 8.0 mA V _{ID} ∠≤10 mV, I _{sink} ≤ 8.0 mA | | | 0.23 | 0.4 | — — | 0.23 | 0.4 | |
| Strobe "On" Current (Note 5) | IS | | 3.0 | | — | 3.0 | _ | mA |
| Output Leakage Current $V_{ID} \ge 5.0 \text{ mV}, V_{O}= 35 \text{ V}, T_{A} = 25^{\circ}\text{C}, \text{Istrobe}= 3.0 \text{ mA}$ $V_{ID} \ge 10 \text{ mV}, V_{O}= 35 \text{ V}, T_{A} = 25^{\circ}\text{C}, \text{Istrobe}= 3.0 \text{ mA}$ $V_{ID} \ge 5.0 \text{ mV}, V_{O}= 35 \text{ V}, \text{T}_{IOW} \le T_{A} \le \text{Thigh}^{*}$ | | | 0.2 0.1 | 10 — 0.5 | | 0.2 | 50 | nA nA μA |
| Input Voltage Range $(T_{Iow} \leq T_A \leq T_{high}^*)$ | VICR | -14.5 | -14.7 to 13.8 | +13.0 | -14.5 | -14.7 to 13.8 | +13.0 | V |
| Positive Supply Current | Icc | | +2.4 | +6.0 | — | +2.4 | +7.5 | mA |
| Negative Supply Current | IEE | | -1.3 | -5.0 | — | -1.3 | 5.0 | mA |

 $T_{IOW} = -55^{\circ}C \text{ for LM111} T_{IOW}$ = -25°C for LM211

M111 T_{high} = +125°C for LM111 M211 = +85°C for LM211

= 0°C for LM311

= +70°C for LM311

NOTES: 1. Offset voltage, offset current and bias current specifications apply for a supply voltage range from a single 5.0 V supply up to ±15 V supplies.

 This rating applies for ±15 V supplies. The positive input voltage limit is 30 V above the negative supply. The negative input voltage limit is equal to the negative supply voltage or 30 V below the positive supply, whichever is less.

3. The Offset voltages and offset currents given are the maximum values required to drive the output within a volt of either supply with a 1.0 mA load. Thus, these parameters define an error band and take into account the "worst case" effects of voltage gain and input impedance.

4. The response time specified is for a 100 mV input step with 5.0 mV overdrive.

5. Do not short the strobe pin to ground; it should be current driven at 3.0 mA to 5.0 mA.

Figure 1. Circuit Schematic



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TECHNIQUES FOR AVOIDING OSCILLATIONS IN COMPARATOR APPLICATIONS

When a high speed comparator such as the LM111 is used with high speed input signals and low source impedances, the output response will normally be fast and stable, providing the power supplies have been bypassed (with 0.1 μ F disc capacitors), and that the output signal is routed well away from the inputs (Pins 2 and 3) and also away from Pins 5 and 6.

However, when the input signal is a voltage ramp or a slow sine wave, or if the signal source impedance is high (1.0 k Ω to 100 k Ω), the comparator may burst into oscillation near the crossing-point. This is due to the high gain and wide bandwidth of comparators like the LM111 series. To avoid oscillation or instability in such a usage, several precautions are recommended, as shown in Figure 15.

The trim pins (Pins 5 and 6) act as unwanted auxiliary inputs. If these pins are not connected to a trim-pot, they should be shorted together. If they are connected to a trim-pot, a 0.01 μ F capacitor (C1) between Pins 5 and 6 will minimize the susceptibility to ac coupling. A smaller capacitor is used if Pin 5 is used for positive feedback as in Figure 15.

Certain sources will produce a cleaner comparator output waveform if a 100 pF to 1000 pF capacitor (C2) is connected directly across the input pins. When the signal source is applied through a resistive network, R1, it is usually advantageous to choose R2 of the same value, both for dc and for dynamic (AC) considerations. Carbon, tin-oxide, and metal-film resistors have all been used with good results in comparator input circuitry, but inductive wirewound resistors should be avoided.

When comparator circuits use input resistors (e.g., summing resistors), their value and placement are particularly important. In all cases the body of the resistor should be close to the device or socket. In other words, there should be a very short lead length or printed-circuit foil run between comparator and resistor to radiate or pick up signals. The same applies to capacitors, pots, etc. For example, if R1 = 10 k Ω , as little as 5 inches of lead between the resistors and the input pins can result in oscillations that are very hard to dampen, Twisting these input leads tightly is the best alternative to placing resistors close to the comparator.

Since feedback to almost any pin of a comparator can result in oscillation, the printed-circuit layout should be engineered thoughtfully. Preferably there should be a groundplane under the LM111 circuitry (e.g., one side of a double layer printed circuit board). Ground, positive supply or negative supply foil should extend between the output and the inputs, to act as a guard. The foil connections for the inputs should be as small and compact as possible, and should be essentially surrounded by ground foil on all sides, to guard against capacitive coupling from any fast high-level signals (such as the output). If Pins 5 and 6 are not used, they should be shorted together. If they are connected to a trim-pot, the trim-pot should be located no more than a few inches away from the LM111, and a 0.01 µF capacitor should be installed across Pins 5 and 6. If this capacitor cannot be used, a shielding printed-circuit foil may be advisable between Pins 6 and 7. The power supply bypass capacitors should be located within a couple inches of the LM111.

A standard procedure is to add hysteresis to a comparator to prevent oscillation, and to avoid excessive noise on the output. In the circuit of Figure 16, the feedback resistor of 510 k Ω from the output to the positive input will cause about 3.0 mV of hysteresis. However, if R2 is larger than 100 Ω , such as 50 k Ω , it would not be practical to simply increase the value of the positive feedback resistor proportionally above 510 k Ω to maintain the same amount of hysteresis.

When both inputs of the LM111 are connected to active signals, or if a high-impedance signal is driving the positive input of the LM111 so that positive feedback would be disruptive, the circuit of Figure 15 is ideal. The positive feedback is applied to Pin 5 (one of the offset adjustment pins). This will be sufficient to cause 1.0 mV to 2.0 mV hysteresis and sharp transitions with input triangle waves from a few Hz to hundreds of kHz. The positive-feedback signal across the 82 Ω resistor swings 240 mV below the positive supply. This signal is centered around the nominal voltage at Pin 5, so this feedback does not add to the offset voltage can be trimmed out, using the 5.0 k Ω pot and 3.0 k Ω resistor as shown.



Figure 17. Zero-Crossing Detector Driving CMOS Logic

Figure 18. Relay Driver with Strobe Capability

