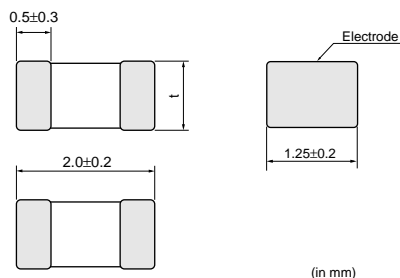


# Chip Coils for Choke Monolithic Type

## LQM21F\_00 Series (0805 Size)

### ■ Dimension



Dimension of t	Inductance : 1.0 to 2.2μH	0.85±0.2
	Inductance : 4.7 to 47μH	1.25±0.2

### ■ Packaging

Code	Packaging	Minimum Quantity
D	180mm Paper Tape	4000 *1
L	180mm Embossed Tape	3000 *2
J	330mm Paper Tape	10000 *1
K	330mm Embossed Tape	10000 *2
B	Bulk(Bag)	1000

\*1: only LQM21D (1.0 to 10μH)/LQM21F (1.0 to 2.2μH)

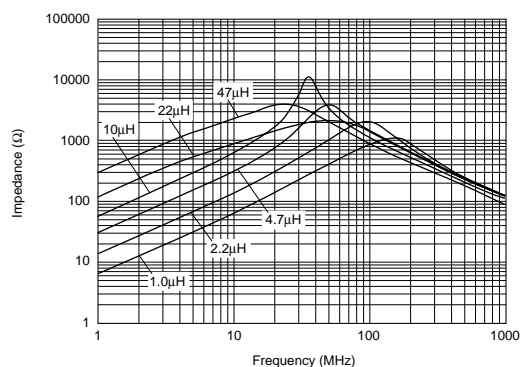
\*2: only LQM21D (22 to 47μH)/LQM21F (4.7 to 47μH)

### ■ Rated Value (□: packaging code)

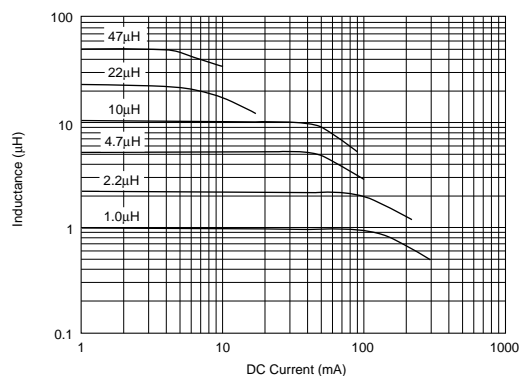
Part Number	Inductance	Test Frequency	Rated Current	DC Resistance	Self Resonance Frequency (min.)
LQM21FN1R0N00□	1.0μH±30%	1MHz	220mA	0.20ohm±30%	105MHz
LQM21FN2R2N00□	2.2μH±30%	1MHz	150mA	0.28ohm±30%	70MHz
LQM21FN4R7N00□	4.7μH±30%	1MHz	80mA	0.30ohm±30%	25MHz
LQM21FN100N00□	10μH±30%	1MHz	60mA	0.50ohm±30%	15MHz
LQM21FN220N00□	22μH±30%	1MHz	13mA	0.35ohm±30%	15MHz
LQM21FN470N00□	47μH±30%	1MHz	7mA	0.60ohm±30%	7.5MHz

Operating Temperature Range: -40°C to +85°C

### ■ Impedance - Frequency Characteristics



### ■ Inductance - Current Characteristics




Continued on the following page.

● This data sheet is applied for CHIP COIL used for General Electronics equipment for your design.

### ⚠ Note:

1. This datasheet is downloaded from the website of Murata Manufacturing co., Ltd. Therefore, it's specifications are subject to change or our products in it may be discontinued without advance notice. Please check with our sales representatives or product engineers before ordering.
2. This datasheet has only typical specifications because there is no space for detailed specifications. Therefore, please approve our product specifications or transact the approval sheet for product specifications before ordering.

 Continued from the preceding page.

## ■ ⚠ Caution/Notice

### ⚠ Caution (Rating)

Do not use products beyond the rated current as this may create excessive heat.

### Notice

Solderability of Tin plating termination chip might be deteriorated when low temperature soldering profile where peak solder temperature is below the Tin melting point is used. Please confirm the solderability of Tin plating termination chip before use.

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