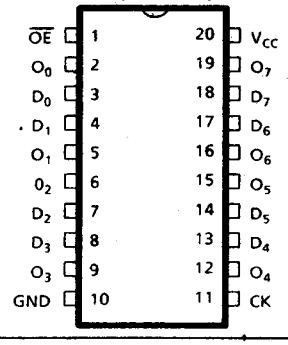
MD54/74HCT374R
Octal D-Type Flip Flop
February '85

Features

- High latch-up immunity
- High current outputs can drive 30 LSTTL loads
- Low power ISO-CMOS technology
- Bus oriented 3-state outputs
- Meets or exceeds all proposed JEDEC 40.2 specifications
- Fully TTL compatible inputs and outputs
- Pin compatible with 54/74LS374 types

CONNECTION DIAGRAM
DIP (TOP VIEW)



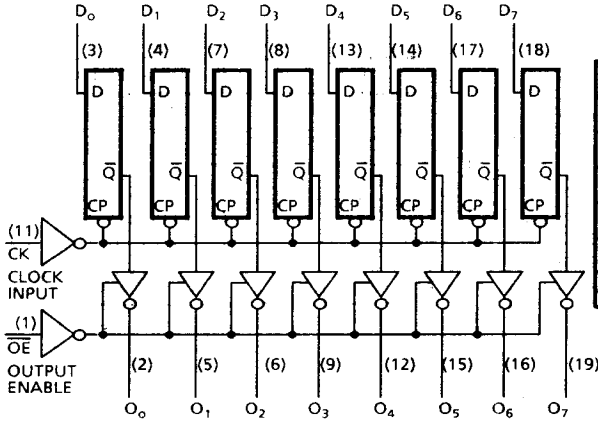
Description

This 8-bit latch features 3-state operation and is designed for use in high speed, bus oriented, systems. The latches hold their individual data when meeting set up times with the clock (CK) LOW to HIGH transition. The state of the latches is unaffected by the active low Output Enable ($\overline{OE}$) pin, but when $\overline{OE}$ is HIGH the outputs are put into high impedance. Data may thus be latched even when the device is deselected. The '374R is functionally identical to the '574R but has a non-bus-oriented pin-out.

Ordering Information

MD54HCT374RC, Cerdip
-55°C to 125°C

MD74HCT374RE, Plastic Dip
-40°C to 85°C



PIN FUNCTION

PIN	DESCRIPTION
D_0 to D_7	Data Inputs
O_0 to O_7	Data Outputs
CK	Clock Input
$\overline{OE}$	Output Enable
V_{CC}	Supply Voltage
GND	System Ground