

±15kV ESD-Protected, 1μA, 1Mbps, 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

General Description

The MAX3224E/MAX3225E/MAX3226E/MAX3227E/ MAX3244E/MAX3245E are 3V-powered EIA/TIA-232 and V.28/V.24 communications interfaces with automatic shutdown/wakeup features, high data-rate capabilities, and enhanced electrostatic discharge (ESD) protection. All transmitter outputs and receiver inputs are protected to ±15kV using IEC 1000-4-2 Air-Gap Discharge, ±8kV using IEC 1000-4-2 Contact Discharge, and ±15kV using the Human Body Model.

All devices achieve a 1µA supply current using Maxim's revolutionary AutoShutdown Plus[™] feature. These devices automatically enter a low-power shutdown mode when the RS-232 cable is disconnected or the transmitters of the connected peripherals are inactive, and the UART driving the transmitter inputs is inactive for more than 30 seconds. They turn on again when they sense a valid transition at any transmitter or receiver input. AutoShutdown Plus saves power without changes to the existing BIOS or operating system.

The MAX3225E/MAX3227E/MAX3245E also feature MegaBaud[™] operation, guaranteeing 1Mbps for high-speed applications such as communicating with ISDN modems. The MAX3224E/MAX3226E/MAX3244E guarantee 250kbps operation. The transceivers have a proprietary low-dropout transmitter output stage enabling true RS-232 performance from a +3.0V to +5.5V supply with a dual charge pump. The charge pump requires only four small 0.1µF capacitors for operation from a 3.3V supply. The MAX3224E–MAX3227E feature a logic-level output (READY) that asserts when the charge pump is regulating and the device is ready to begin transmitting.

All devices are available in a space-saving TQFN, SSOP, and TSSOP (MAX3224E/MAX3225E/MAX3244E/MAX3245E) packages.

Applications

- Notebook, Subnotebook, and Palmtop Computers
- Cellular Phones
- Battery-Powered Equipment
- Hand-Held Equipment
- Peripherals
- Printers

Next Generation Device Features

- ♦ For Space-Constrained Applications: MAX3228E/MAX3229E: ±15kV ESD-Protected, +2.5V to +5.5V, RS-232 Transceivers in UCSP™ MAX3222E/MAX3232E/MAX3241E/MAX3246E: ±15kV ESD-Protected, Down to 10nA, +3.0V to +5.5V, Up to 1Mbps, True RS-232 Transceivers (MAX3246E Available in UCSP)
- ♦ For Low-Voltage or Data Cable Applications: MAX3380E/MAX3381E: +2.35V to +5.5V, 1µA, 2Tx/2Rx RS-232 Transceivers with ±15kV ESD-Protected I/O and Logic Pins

PART	TEMP RANGE	PIN-PACKAGE				
MAX3224ECTP+	0°C to +70°C	20 TQFN-EP*				
MAX3224ECUP+	0°C to +70°C	20 TSSOP				
MAX3224ECAP+	0°C to +70°C	20 SSOP				
MAX3224ECPP+	0°C to +70°C	20 Plastic DIP				
MAX3224EETP+	-40°C to +85°C	20 TQFN-EP*				
MAX3224EEUP+	-40°C to +85°C	20 TSSOP				
MAX3224EEAP+	-40°C to +85°C	20 SSOP				
MAX3224EEPP+	-40°C to +85°C	20 Plastic DIP				
MAX3224EAAP+	-40°C to +125°C	20 SSOP				
+Denotes a lead(Pb)-free/RoHS-compliant package.						

Ordering Information

*EP = Exposed pad.

Ordering Information continued at end of data sheet.

	•			r Guiue
PART	NO. OF DRIVERS/ RECEIVERS	GUARANTEED DATA RATE (bps)	READY OUTPUT	Auto- Shutdown Plus
MAX3224E	2/2	250k	~	~
MAX3225E	2/2	1M	~	~
MAX3226E	1/1	250k	~	~
MAX3227E	1/1	1M	~	~
MAX3244E	3/5	250k	—	~
MAX3245E	3/5	1M		~

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For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maximintegrated.com.

Selector Guide

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ABSOLUTE MAXIMUM RATINGS

V _{CC} to GND0.3V to +6V V+ to GND (Note 1)0.3V to +7V V- to GND (Note 1)+0.3V to -7V V+ + V- (Note 1)+13V
Input Voltages
T_IN, FORCEON, FORCEOFF to GND0.3V to +6V
R_IN to GND±25V
Output Voltages
T_OUT to GND±13.2V
R_OUT, INVALID, READY to GND0.3V to (V _{CC} + 0.3V)
Short-Circuit Duration
T_OUT to GNDContinuous
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
16-Pin SSOP (derate 7.14mW/°C above +70°C)571mW
16-Pin TSSOP (derate 9.4mW/°C above +70°C)754.7mW
16-Pin TQFN (derate 20.8mW/°C above +70°C)1666.7mW
Note 1. V. and V. can have maximum magnitudes of 7V but th

20-Pin TQFN (derate 21.3mW/°C above +70°C)1702.1mW 20-Pin Plastic DIP (derate 11.11mW/°C above +70°C)889mW 20-Pin SSOP (derate 8.00mW/°C above +70°C)640mW 20-Pin TSSOP (derate 10.9mW/°C above +70°C)1W 28-Pin Wide SO (derate 12.5mW/°C above +70°C)1W 28-Pin SSOP (derate 9.52mW/°C above +70°C)1026mW 28-Pin TSSOP (derate 12.8mW/°C above +70°C)
Operating Temperature Ranges MAX32EC0°C to +70°C MAX32_EE40°C to +85°C MAX32_EAA40°C to +125°C Storage Temperature Range65°C to +160°C Lead Temperature (soldering, 10s)+300°C Soldering Temperature (reflow)+260°C

Note 1: V+ and V- can have maximum magnitudes of 7V, but their absolute difference cannot exceed 13V.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC} = +3V \text{ to } +5.5V, C1-C4 = 0.1\mu\text{F}, \text{ tested at } 3.3V \pm 10\%; C_1 = 0.047\mu\text{F}, C2-C4 = 0.33\mu\text{F}, \text{ tested at } 5.0V \pm 10\%; T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}\text{C}.)$

PARAMETER S	YMBOL	COND	ITIONS	MIN	ТҮР	MAX	UNITS
DC CHARACTERISTICS (V _{CC} = 3.3	3V or 5.0\	/, T _A = +25°C)					
Supply Current, AutoShutdown Plus		FORCEON = GND, FC all R_IN idle, all T_IN id			1	10	μA
Supply Current, Shutdown		$\overline{FORCEOFF} = GND$			1	10	μA
Supply Current, AutoShutdown Plus Disabled		FORCEON = FORCEC	$\overline{\text{PFF}} = \text{V}_{\text{CC}}$, no load		0.3	1	mA
LOGIC INPUTS AND RECEIVER O	UTPUTS			I			1
Input Logic Threshold Low		T_IN, FORCEON, FOR	CEOFF			0.8	V
Input Logic Threshold High		T_IN, FORCEON,	$V_{CC} = 3.3V$	2			V
Input Logic Threshold High		FORCEOFF	$V_{CC} = 5.0V$	2.4			
Transmitter Input Hysteresis					0.5		V
Input Leakage Current		T_IN, FORCEON, FOR	CEOFF		±0.01	±1	μA
Output Leakage Current		R_OUT (MAX3244E/M disabled	R_OUT (MAX3244E/MAX3245E), receivers disabled		±0.05	±10	μA
Output Voltage Low		$I_{OUT} = 1.6 mA$				0.4	V
Output Voltage High		$I_{OUT} = -1.0 mA$		V _{CC} - 0.6	6 V _{CC} - 0.1		V
RECEIVER INPUTS							
Input Voltage Range				-25		+25	V
Input Threshold Low		T _A = +25°C	$V_{CC} = 3.3V$	0.6	1.2		v
		TA = +23 C	$V_{CC} = 5.0V$	0.8	1.5		
Input Threshold High		T _A = +25°C	$V_{CC} = 3.3V$		1.5	2.4	V
		$V_{CC} = 5.0V$			1.8	2.4	v
Input Hysteresis					0.5		V
Input Resistance		$T_A = +25^{\circ}C$		3	5	7	kΩ

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ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = +3V \text{ to } +5.5V, C1-C4 = 0.1\mu\text{F}, \text{tested at } 3.3V \pm 10\%; C_1 = 0.047\mu\text{F}, C2-C4 = 0.33\mu\text{F}, \text{tested at } 5.0V \pm 10\%; T_A = T_{MIN} \text{ to } T_{MAX}, \text{unless otherwise noted}$. Typical values are at $T_A = +25^{\circ}\text{C}$.)

PARAMETER	SYMBOL	CONDIT	MIN	ТҮР	MAX	UNITS		
TRANSMITTER OUTPUTS	1							
Output Voltage Swing		All transmitter outputs loa ground	aded with 3k Ω to	±5	±5.4		V	
Output Resistance		$V_{CC} = V + = V - = 0V$, trar	smitter outputs = $\pm 2V$	300	10M		Ω	
Output Short-Circuit Current						±60	mA	
Output Leakage Current		V _{CC} = 0V or 3V to 5.5V, V Transmitters disabled	$V_{OUT} = \pm 12V,$			±25	μA	
MOUSE DRIVEABILITY (MAX32	44E/MAX32	45E)						
Transmitter Output Voltage		T1IN = T2IN = GND, T3II T3OUT loaded with $3k\Omega$ T1OUT and T2OUT loade 2.5mA each	±5			V		
ESD PROTECTION								
		IEC 1000-4-2 Air Dischar	ge		±15			
R_IN, T_OUT		IEC 1000-4-2 Contact Dis	scharge		±8] kV	
		Human Body Model			±15			
AutoShutdown Plus (FORCEON	I = GND, FC	$PRCEOFF = V_{CC}$	T					
Receiver Input Threshold to INVALID Output High		Figure 4a	Positive threshold Negative threshold	-2.7		2.7	V	
Receiver Input Threshold to INVALID Output Low		Figure 4a	1	-0.3		+0.3	V	
INVALID, READY Output Voltage Low (MAX3224E–MAX3227E)		I _{OUT} = -1.6mA				0.4	V	
ÎNVALID, READY Output Voltage High (MAX3224E–MAX3227E)		I _{OUT} = -1.0mA		V _{CC} - 0.6			V	
Receiver Positive or Negative Threshold to INVALID High	tinvh	V _{CC} = 5V, Figure 4b			1		μs	
Receiver Positive or Negative Threshold to INVALID Low	tinvl	V _{CC} = 5V, Figure 4b			30		μs	
Receiver or Transmitter Edge to Transmitters Enabled	twu	V _{CC} = 5V, Figure 5b (Note 2)			100		μs	
Receiver or Transmitter Edge to Transmitters Shutdown	t _{AUTOSHDN}	V _{CC} = 5V, Figure 5b (No	te 2)	15	30	60	S	

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TIMING CHARACTERISTICS—MAX3224E/MAX3226E/MAX3244E

 $(V_{CC} = +3V \text{ to } +5.5V, C1-C4 = 0.1\mu\text{F}, \text{ tested at } 3.3V \pm 10\%; C_1 = 0.047\mu\text{F}, C2-C4 = 0.33\mu\text{F}, \text{ tested at } 5.0V \pm 10\%; T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted}$. Typical values are at T_A = +25°C.)

PARAMETER	SYMBOL	CONDITIONS		MIN	ТҮР	MAX	UNITS
Maximum Data Rate		$R_L = 3k\Omega$, $C_L = 1000pF$, one transmitter switching	250			kbps	
Receiver Propagation Delay	t _{PHL}	R IN to R OUT, CI = 150p	E		0.15		110
neceiver riopagation Delay	t _{PLH}	R_{IN} to R_{OUT} , C_{L} = 150pr		0.15			- µs
Receiver Output Enable Time		Normal operation (MAX324	4E only)		200		ns
Receiver Output Disable Time		Normal operation (MAX324		200		ns	
Transmitter Skew	tphl - tplh	(Note 3)			100		ns
Receiver Skew	tphl - tplh				50		ns
Transition-Region Slew Rate		$\begin{array}{l} V_{CC}=3.3V, \ T_{A}=+25^{\circ}C, \\ R_{L}=3k\Omega \ to \ 7k\Omega, \\ measured \ from \ +3V \ to \ -3V \\ or \ -3V \ to \ +3V, \ one \ transmitter \ switching \end{array}$	C _L = 150pF to 1000pF	6		30	V/µs

TIMING CHARACTERISTICS—MAX3225E/MAX3227E/MAX3245E

 $(V_{CC} = +3V \text{ to } +5.5V, C1-C4 = 0.1\mu\text{F}, \text{tested at } 3.3V \pm 10\%; C_1 = 0.047\mu\text{F}, C2-C4 = 0.33\mu\text{F}, \text{tested at } 5.0V \pm 10\%; T_A = T_{MIN} \text{ to } T_{MAX}, \text{unless otherwise noted. Typical values are at } T_A = +25^{\circ}\text{C}.)$

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
		$R_L = 3k\Omega$, $C_L = 1000pF$, one transmitter switching	250			
Maximum Data Rate		V_{CC} = 3.0V to 4.5V, R_L = 3k Ω , C_L = 250pF, one transmitter switching	1000			kbps
		$V_{CC} = 4.5V$ to 5.5V, $R_L = 3k\Omega$, $C_L = 1000pF$, one transmitter switching	1000			-
Pagaiver Propagation Delay	t _{PHL}	R IN to R OUT C 150pE		0.15		
Receiver Propagation Delay	t _{PLH}	$R_IN \text{ to } R_OUT, C_I = 150pF$		0.15		μs
Receiver Output Enable Time		Normal operation (MAX3245E only)		200		ns
Receiver Output Disable Time		Normal operation (MAX3245E only)		200		ns
Transmitter Skew	tphl - tplh	(Note 3)		25		ns
Receiver Skew	tphl - tplh			50		ns
Transition-Region Slew Rate		$\label{eq:VCC} \begin{array}{l} V_{CC} = 3.3 \text{V}, \text{T}_{\text{A}} = +25^{\circ}\text{C}, \\ \text{R}_{\text{L}} = 3 \text{k} \Omega \text{ to } 7 \text{k} \Omega, \text{C}_{\text{L}} = 150 \text{pF} \text{ to } 1000 \text{pF}, \\ \text{measured from +3V to -3V or -3V to +3V}, \\ \text{one transmitter switching} \end{array}$	24		150	V/µs

Note 2: A transmitter/receiver edge is defined as a transition through the transmitter/receiver input logic thresholds. **Note 3:** Transmitter skew is measured at the transmitter zero cross points.

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MAX3224E/MAX3225E/ MAX3226E/MAX3227E/MAX3244E/MAX3245E ±15kV ESD-Protected, 1µA, 1Mbps, 3.0V to 5.5V, **RS-232 Transceivers with AutoShutdown Plus**

Typical Operating Characteristics

 $(V_{CC} = +3.3V, 250 \text{ kbps} \text{ data rate}, 0.1 \mu\text{F} \text{ capacitors}, all transmitters loaded with 3k\Omega and CL, T_A = +25^{\circ}C, unless otherwise noted.)$ MAX3224E/MAX3226E MAX3224E/MAX3226E TRANSMITTER OUTPUT VOLTAGE MAX3224E/MAX3226E **OPERATING SUPPLY CURRENT** vs. LOAD CAPACITANCE **SLEW RATE vs. LOAD CAPACITANCE** vs. LOAD CAPACITANCE 6 16 45 5 40 14 S Volit+ 4 250kbps TRANSMITTER OUTPUT VOLTAGE 35 3 12 SUPPLY CURRENT (mA) T1 TRANSMITTING AT 250kbps 2 -SLÈW 30 SLEW RATE (V/µs) T2 (MAX3224E) TRANSMITTING AT 15.6kbps 120kbps 10 1 +SI FW 25 0 8 20 -1 20kbps 6 -2 15 7 -3 4 10 -4 T1 TRANSMITTING AT 250kbps VOUT-2 5 -5 FOR DATA RATES UP TO 250kbps T2 (MAX3224E) TRANSMITTING AT 15.6kbps -6 0 0 0 1000 2000 3000 4000 5000 0 5000 0 5000 1000 2000 3000 4000 1000 2000 3000 4000 LOAD CAPACITANCE (pF) LOAD CAPACITANCE (pF) LOAD CAPACITANCE (pF) MAX3225E/MAX3227E MAX3225E/MAX3227E TRANSMITTER OUTPUT VOLTAGE MAX3225E/MAX3227E **OPERATING SUPPLY CURRENT** vs. LOAD CAPACITANCE **SLEW RATE vs. LOAD CAPACITANCE** vs. LOAD CAPACITANCE 100 7.5 80 1 TRANSMITTER AT FULL DATA RATE 90 70 1 TRANSMITTER AT 1/16 DATA RATE FRANSMITTER OUTPUT VOLTAGE (V) 5.0 80 (MAX3225E) 60 2Mbps $LOAD = 3k\Omega + Ci$ (mA) 70 .5Mbps -SI FW 2.5 1Mbps 50 2Mbps 60 1 TRANSMITTER AT FULL DATA RATE +SLEW 0 1 TRANSMITTER AT 1/16 DATA RATE 50 40 1 5Mbps (MAX3225E) 40

SLEW RATE (V/us) SUPPLY CURRENT $LOAD = 3k\Omega + C_1$ 30 1.5Mbps-1Mbps -2.5 2Mbps 30 20 20 -5.0 1 TRANSMITTER AT 1Mbps 10 10 1 TRANSMITTER AT 62.5kbps (MAX3225E -7.5 0 0 0 500 1000 1500 2000 2500 0 500 1000 1500 2000 2500 0 500 1000 LOAD CAPACITANCE (pF) LOAD CAPACITANCE (pF) LOAD CAPACITANCE (pF) MAX3225E/MAX3227E MAX3224E-MAX3227E MAX3224E-MAX3227E **TRANSMITTER SKEW READY TURN-ON TIME READY TURN-OFF TIME** vs. LOAD CAPACITANCE vs. TEMPERATURE vs. TEMPERATURE 50 200 38 1 TRANSMITTER AT 512kbps 45 180 36 1 TRANSMITTER AT 30kbps 40 (MAX3225E) 160 34 READY TURN-ON TIME (µs) TURN-OFF TIME (ns) (us) $LOAD = 3k\Omega + CI$ 35 140 32 **FRANSMITTER SKEW** 30 120 30 25 100 28 20 80 READY -26 AVERAGE: 10 PARTS 15 60 24 10 40 22 5 20 0 20 0 0 500 1500 2000 3000 -20 20 -20 1000 2500 -40 0 40 60 80 100 -40 0

TEMPERATURE (°C)

Maxim Integrated

LOAD CAPACITANCE (pF)

5

1Mbps

1500

20 40 60 80 100

TEMPERATURE (°C)

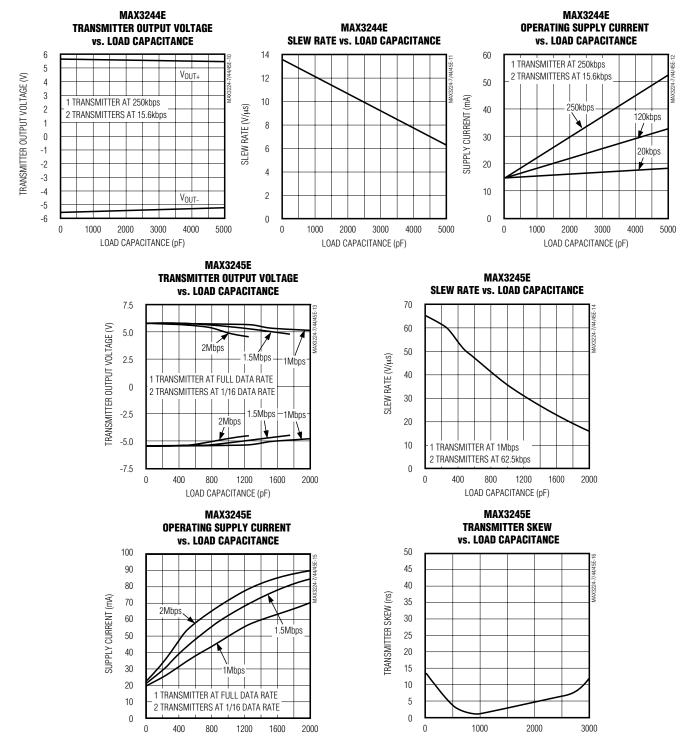
2000

2500

MAX3224E/MAX3225E/ MAX3226E/MAX3227E/MAX3244E/MAX3245E ±15kV ESD-Protected, 1µA, 1Mbps, 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

Typical Operating Characteristics (continued)

 $(V_{CC} = +3.3V, 250 \text{kbps} \text{ data rate}, 0.1 \mu \text{F} \text{ capacitors}, all transmitters loaded with 3k\Omega and CL, TA = +25°C, unless otherwise noted.)$



MAX3224E/MAX3225E/ MAX3226E/MAX3227E/MAX3244E/MAX3245E +15kV ESD-Protected 1uA 1Mbps 3 0V to 5 5V

±15kV ESD-Protected, 1μA, 1Mbps, 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

Pin Description

			PIN				
MAX3 MAX3			3226E 3227E	MAX3244E MAX3245E		NAME	FUNCTION
TQFN	DIP/ SSOP/ TSSOP	TQFN	SSOP/ TSSOP	MAX3245E SO/ SSOP/ TSSOP	TQFN		
19	1	14	1	_	_	READY	Ready to Transmit Output, active-high. READY is enabled high when V- goes below -4V and the device is ready to transmit.
1	2	16	2	28	31	C1+	Positive Terminal of Voltage- Doubler Charge-Pump Capacitor
20	3	15	3	27	30	V+	+5.5V generated by the charge pump
2	4	1	4	24	26	C1-	Negative Terminal of Voltage- Doubler Charge-Pump Capacitor
3	5	2	5	1	33	C2+	Positive Terminal of Inverting Charge-Pump Capacitor
4	6	3	6	2	34	C2-	Negative Terminal of Inverting Charge-Pump Capacitor
5	7	4	7	3	35	V-	-5.5V Generated by the Charge Pump
6, 15	8, 17	10	13	9, 10, 11	7, 8, 9	T_OUT	RS-232 Transmitter Outputs
7, 14	9, 16	5	8	4-8	1-5	R_IN	RS-232 Receiver Inputs
8, 13	10, 15	6	9	15-19	15, 16, 17, 19, 20	R_OUT	TTL/CMOS Receiver Outputs
9	11	7	10	21	23	INVALID	Valid Signal Detector Output, active low. A logic high indicates that a valid RS-232 level is present on a receiver input.
10, 11	12, 13	8	11	12-14	11, 12, 13	T_IN	TTL/CMOS Transmitter Inputs

±15kV ESD-Protected, 1μA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

Pin Description (continued)

			PIN				
	MAX3224E MAX3225E		3226E 3227E	MAX3244E MAX3245E		NAME	FUNCTION
TQFN	DIP/ SSOP/ TSSOP	TQFN	SSOP/ TSSOP	MAX3245E SO/ SSOP/ TSSOP	TQFN		
12	14	9	12	23	25	FORCEON	Force-On Input, Active High. Drive high to override AutoShutdown Plus, keeping transmitters and receivers on (FORCEOFF must be high) (Table 1).
16	18	11	14	25	27	GND	Ground
17	19	12	15	26	29	VCC	+3.0V to +5.5V Single Supply Voltage
18	20	13	16	22	22 24 FORCEOFF		Force-Off Input, Active Low. Drive low to shut down transmitters, receivers (except R2OUTB), and charge pump. This overrides AutoShutdown Plus and FORCEON (Table 1).
_	_	_	_	20	21	R2OUTB	TTL/CMOS Noninverting Complementary Receiver Outputs. Always active.
_	_		_	_	6, 10, 14, 18, 22, 28, 32, 36	N.C.	No Connection. Not internally connected.
_				_		EP	Exposed Pad. Solder the exposed pad to the ground plane or leave unconnected.

_Detailed Description

Dual Charge-Pump Voltage Converter

The MAX3224E–MAX3227E/MAX3244E/MAX3245E's internal power supply consists of a regulated dual charge pump that provides output voltages of +5.5V (doubling charge pump) and -5.5V (inverting charge pump), over the +3.0V to +5.5V range. The charge pump operates in discontinuous mode: if the output voltages are less than 5.5V, the charge pump is

enabled; if the output voltages exceed 5.5V, the charge-pump is disabled. Each charge pump requires a flying capacitor (C1, C2) and a reservoir capacitor (C3, C4) to generate the V+ and V- supplies.

The READY output (MAX3224E–MAX3227E) is low when the charge pumps are disabled in shutdown mode. The READY signal asserts high when V- goes below -4V.

±15kV ESD-Protected, 1μA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

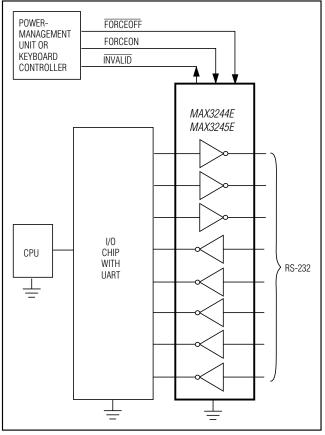


Figure 1. Interface Under Control of PMU

RS-232 Transmitters

The transmitters are inverting level translators that convert CMOS-logic levels to 5.0V EIA/TIA-232 levels. The MAX3224E/MAX3226E/MAX3244E guarantee a 250kbps data rate (1Mbps, for the MAX3225E/MAX3227E/MAX3245E) with worst-case loads of 3k Ω in parallel with 1000pF, providing compatibility with PC-to-PC communication software (such as LapLink[®]). Transmitters can be paralleled to drive multiple receivers. Figure 1 shows a complete system connection.

When FORCEOFF is driven to ground or when the Auto-Shutdown Plus circuitry senses that all receiver and transmitter inputs are inactive for more than 30s, the transmitters are disabled and the outputs go into a high-impedance state. When powered off or shut down, the outputs can be driven to $\pm 12V$. The transmitter inputs do not have pullup resistors. Connect unused inputs to GND or V_{CC}.

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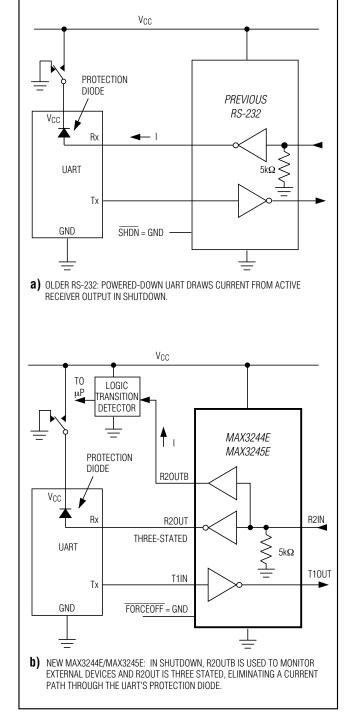


Figure 2. The MAX3244E/MAX3245E detect RS-232 activity when the UART and interface are shut down.

±15kV ESD-Protected, 1μA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

Table 1. Output Control Truth Table

OPERATION STATUS	FORCEON	FORCEOFF	VALID RECEIVER LEVEL	RECEIVER OR TRANSMITTER EDGE WITHIN 30s	T_OUT	R_OUT (MAX3224E/ MAX3225E/ MAX3226E/ MAX3227E)	R_OUT (MAX3244E/ MAX3245E)	R2OUTB (MAX3244E/ MAX3245E)
Shutdown (Forced Off)	Х	0	Х	Х	High-Z	Active	High-Z	Active
Normal Operation (Forced On)	1	1	Х	Х	Active	Active	Active	Active
Normal Operation (AutoShutdown Plus)	0	1	Х	Yes	Active	Active	Active	Active
Shutdown (Auto- Shutdown Plus)	0	1	Х	No	High-Z	Active	Active	Active
Normal Operation	INVALID*	1	Yes	Х	Active	Active	Active	Active
Normal Operation	INVALID*	1	Х	Yes	Active	Active	Active	Active
Shutdown	INVALID*	1	No	No	High-Z	Active	Active	Active
Normal Operation (AutoShutdown)	INVALID*	INVALID**	Yes	Х	Active	Active	Active	Active
Shutdown (AutoShutdown)	INVALID*	INVALID**	No	Х	High-Z	Active	High-Z	Active

X = Don't care

* INVALID connected to FORCEON

** INVALID connected to FORCEON and FORCEOFF

RS-232 Receivers

The receivers convert RS-232 signals to CMOS-logic output levels. The MAX3224E–MAX3227E feature inverting outputs that always remain active (Table 1). The MAX3244E/MAX3245E have inverting three-state outputs that are high impedance when shut down (FORCEOFF = GND) (Table 1).

The MAX3244E/MAX3245E feature an extra, always active, noninverting output, R2OUTB. R2OUTB output monitors receiver activity while the other receivers are high impedance, allowing ring indicator applications to be monitored without forward biasing other devices connected to the receiver outputs. This is ideal for systems where V_{CC} is set to ground in shutdown to accommodate peripherals such as UARTs (Figure 2).

The MAX3224E–MAX3227E/MAX3244E/MAX3245E feature an INVALID output that is enabled low when no valid RS-232 voltage levels have been detected on all receiver inputs. Because INVALID indicates the receiver input's condition, it is independent of FORCEON and FORCEOFF states (Figures 3 and 4).

AutoShutdown Plus Mode

The MAX3224E–MAX3227E/MAX3244E/MAX3245E achieve a 1 μ A supply current with Maxim's AutoShutdown Plus feature, which operates when FORCEOFF is high and a FORCEON is low. When these devices do not sense a valid signal transition on any receiver and transmitter input for 30s, the on-board charge pumps are shut down, reducing supply current to 1 μ A. This occurs if the RS-232 cable is disconnected or if the connected

±15kV ESD-Protected, 1μA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

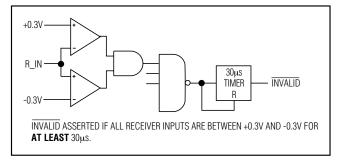


Figure 3a. INVALID Functional Diagram, INVALID Low

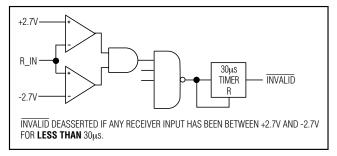


Figure 3b. INVALID Functional Diagram, INVALID High

Table 2. INVALID Truth Table

RS-232 SIGNAL PRESENT AT ANY RECEIVER INPUT	INVALID OUTPUT				
Yes	High				
No	Low				

peripheral transmitters are turned off, and the UART driving the transmitter inputs is inactive. The system turns on again when a valid transition is applied to any RS-232 receiver or transmitter input. As a result, the system saves power without changes to the existing BIOS or operating system.

Figures 3a and 3b depict valid and invalid RS-232 receiver voltage levels. INVALID indicates the receiver input's condition, and is independent of FORCEON and FORCEOFF states. Figure 3 and Tables 1 and 2 summarize the operating modes of the MAX3224E-MAX3227E/MAX3244E/MAX3245E. FORCEON and FORCEOFF override AutoShutdown Plus circuitry. When neither control is asserted, the IC selects between these states automatically based on the last receiver or transmitter input edge received.

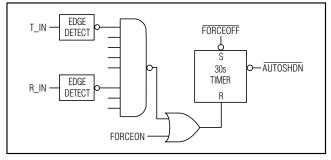


Figure 3c. AutoShutdown Plus Logic

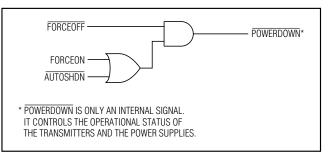


Figure 3d. Power-Down Logic

When shut down, the device's charge pumps turn off, V+ is pulled to V_{CC}, V- is pulled to ground, the transmitter outputs are high impedance, and READY (MAX3224E–MAX3227E) is driven low. The time required to exit shutdown is typically 100 μ s (Figure 8).

By connecting FORCEON to INVALID, the MAX3224E– MAX3227E/MAX3244E/MAX3245E shut down when no valid receiver level and no receiver or transmitter edge is detected for 30s, and wake up when a valid receiver level or receiver or transmitter edge is detected.

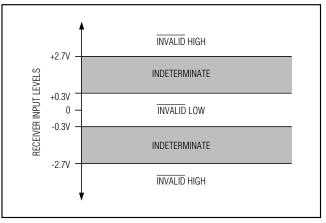


Figure 4a. Receiver Positive/Negative Thresholds for INVALID

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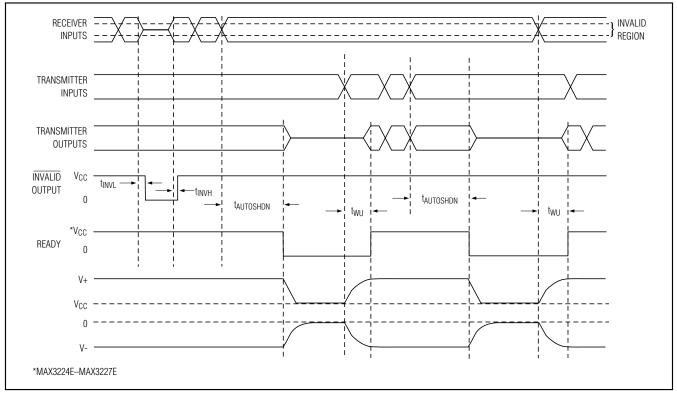


Figure 4b. AutoShutdown Plus, INVALID, and READY Timing Diagram

By connecting FORCEON and FORCEOFF to INVALID, the MAX3224E–MAX3227E/MAX3244E/MAX3245E shut down when no valid receiver level is detected and wake up when a valid receiver level is detected (same functionality as AutoShutdown feature on MAX3221E/MAX3223E/MAX3243E).

A mouse or other system with AutoShutdown Plus may need time to wake up. Figure 5 shows a circuit that forces the transmitters on for 100ms, allowing enough time for the other system to realize that the MAX3244E/ MAX3245E is awake. If the other system outputs valid RS-232 signal transitions within that time, the RS-232 ports on both systems remain enabled.

Software-Controlled Shutdown

If direct software control is desired, use INVALID to indicate DTR or ring indicator signal. Tie FORCEOFF and FORCEON together to bypass the AutoShutdown Plus so the line acts like a SHDN input.

±15kV ESD Protection

As with all Maxim devices, ESD-protection structures are incorporated on all pins to protect against electrostatic discharges encountered during handling and assembly. The driver outputs and receiver inputs of the MAX3224E-MAX3227E/MAX3244E/MAX3245E have extra protection against static electricity. Maxim's engineers have developed state-of-the-art structures to protect

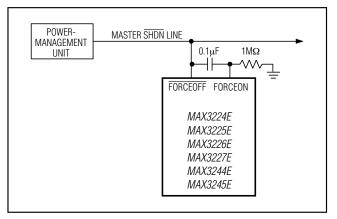


Figure 5. AutoShutdown Plus Initial Turn-On to Wake Up a Mouse or Another System

±15kV ESD-Protected, 1μA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

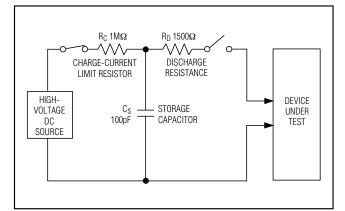


Figure 6a. Human Body ESD Test Model

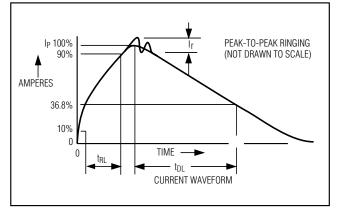


Figure 6b. Human Body Current Waveform

these pins against ESD of ± 15 kV without damage. The ESD structures withstand high ESD in all states: normal operation, shutdown, and powered down. After an ESD event, Maxim's E versions keep working without latchup, whereas competing RS-232 products can latch and must be powered down to remove latchup.

ESD protection can be tested in various ways; the transmitter outputs and receiver inputs of this product family are characterized for protection to the following limits:

- 1) ±15kV using the Human Body Model
- 2) ±8kV using the Contact-Discharge Method specified in IEC 1000-4-2
- 3) ±15kV using IEC 1000-4-2's Air-Gap Method.

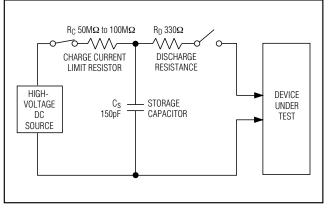


Figure 7a. IEC 1000-4-2 ESD Test Model

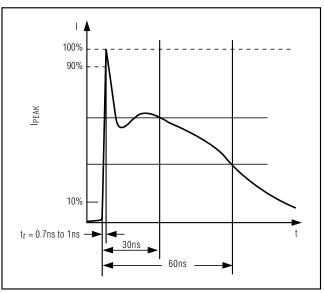


Figure 7b. IEC 1000-4-2 ESD Generator Current Waveform

ESD Test Conditions

ESD performance depends on a variety of conditions. Contact Maxim for a reliability report that documents test setup, test methodology, and test results.

Human Body Model

Figure 6a shows the Human Body Model and Figure 6b shows the current waveform it generates when discharged into a low impedance. This model consists of a 100pF capacitor charged to the ESD voltage of interest, which is then discharged into the test device through a $1.5k\Omega$ resistor.

MAX3224E/MAX3225E/ MAX3226E/MAX3227E/MAX3244E/MAX3245E ±15kV ESD-Protected, 1μA, 1Mbps 3.0V to 5.5V,

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IEC 1000-4-2

The IEC 1000-4-2 standard covers ESD testing and performance of finished equipment; it does not specifically refer to integrated circuits. The MAX3224E–MAX3227E, MAX3244E/MAX3245E help you design equipment that meets Level 4 (the highest level) of IEC 1000-4-2, without the need for additional ESD-protection components.

The major difference between tests done using the Human Body Model and IEC 1000-4-2 is higher peak current in IEC 1000-4-2, because series resistance is lower in the IEC 1000-4-2 model. Hence, the ESD withstand voltage measured to IEC 1000-4-2 is generally lower than that measured using the Human Body Model. Figure 7a shows the IEC 1000-4-2 model and Figure 7b shows the current waveform for the 8kV, IEC 1000-4-2, Level 4, ESD Contact-Discharge Method.

The Air-Gap Method involves approaching the device with a charged probe. The Contact-Discharge Method connects the probe to the device before the probe is energized.

Machine Model The Machine Model for ESD tests all pins using a 200pF storage capacitor and zero discharge resistance. Its objective is to emulate the stress caused by contact that occurs with handling and assembly during manufacturing. Of course, all pins require this protection during manufacturing, not just RS-232 inputs and outputs. Therefore, after PC board assembly, the Machine Model is less relevant to I/O ports.

Applications Information

Capacitor Selection

The capacitor type used for C1–C4 is not critical for proper operation; polarized or nonpolarized capacitors

Table 3. Required Minimum CapacitanceValues

V _{CC} (V)	C1, C _{BYPASS} (µF)	C2, C3, C4 (μF)
3.0 to 3.6	0.22	0.22
3.15 to 3.6	0.1	0.1
4.5 to 5.5	0.047	0.33
3.0 to 5.5	0.22	1

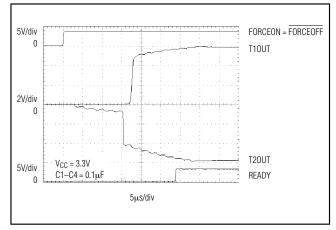


Figure 8. Transmitter Outputs when Exiting Shutdown or Powering Up

can be used. The charge pump requires 0.1μ F capacitors for 3.3V operation. For other supply voltages, see Table 3 for required capacitor values. Do not use values smaller than those listed in Table 3. Increasing the capacitor values (e.g., by a factor of 2) reduces ripple on the transmitter outputs and slightly reduces power consumption. C2, C3, and C4 can be increased without changing C1's value. However, do not increase C1 without also increasing the values of C2, C3, C4, and CBYPASS, to maintain the proper ratios (C1 to the other capacitors).

When using the minimum required capacitor values, make sure the capacitor value does not degrade excessively with temperature. If in doubt, use capacitors with a larger nominal value. The capacitor's equivalent series resistance (ESR), which usually rises at low temperatures, influences the amount of ripple on V+ and V-.

Power-Supply Decoupling

In most circumstances, a 0.1μ F V_{CC} bypass capacitor is adequate. In applications that are sensitive to powersupply noise, use a capacitor of the same value as charge-pump capacitor C1. Connect bypass capacitors as close to the IC as possible.

Transmitter Outputs when Exiting Shutdown

Figure 8 shows two transmitter outputs when exiting shutdown mode. As they become active, the two transmitter outputs are shown going to opposite RS-232 levels (one transmitter input is high, the other is low). Each

MAX3224E/MAX3225E/ MAX3226E/MAX3227E/MAX3244E/MAX3245E ±15kV ESD-Protected, 1µA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

transmitter is loaded with $3k\Omega$ in parallel with 1000pF. The transmitter outputs display no ringing or undesirable transients as they come out of shutdown. Note that the transmitters are enabled only when the magnitude of V- exceeds approximately -3V.

High Data Rates

The MAX3224E/MAX3226E/MAX3244E maintain the RS-232 ±5.0V minimum transmitter output voltage even at high data rates. Figure 9 shows a transmitter loopback test circuit. Figure 10 shows a loopback test result at 120kbps, and Figure 11 shows the same test at 250kbps. For Figure 10, all transmitters were driven simultaneously at 120kbps into RS-232 loads in parallel with 1000pF. For Figure 11, a single transmitter was driven at 250kbps, and all transmitters were loaded with an RS-232 receiver in parallel with 250pF.

The MAX3225E/MAX3227E/MAX3245E maintain the RS-232 ±5.0V minimum transmitter output voltage at data rates up to 1Mbps (MegaBaud). Figure 12 shows a loopback test result with a single transmitter driven at 1Mbps and all transmitters loaded with an RS-232 receiver in parallel with 250pF.

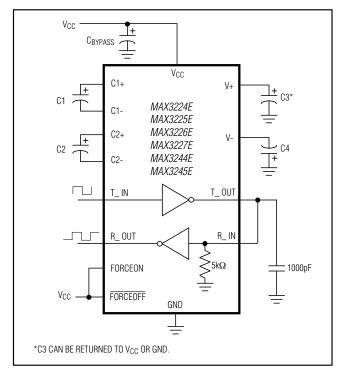
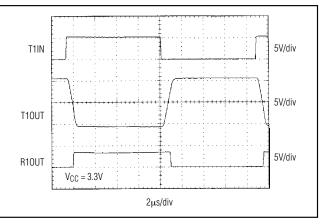
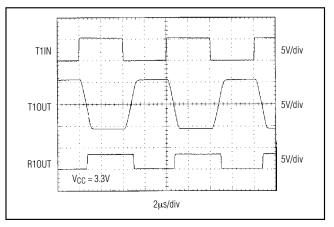
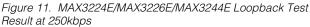


Figure 9. Loopback Test Circuit









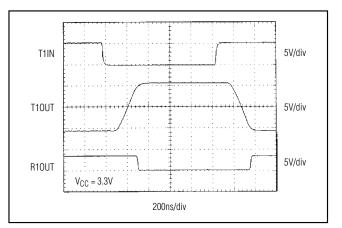


Figure 12. MAX3225E/MAX3227E/MAX3245E Loopback Test Result at 1Mbps

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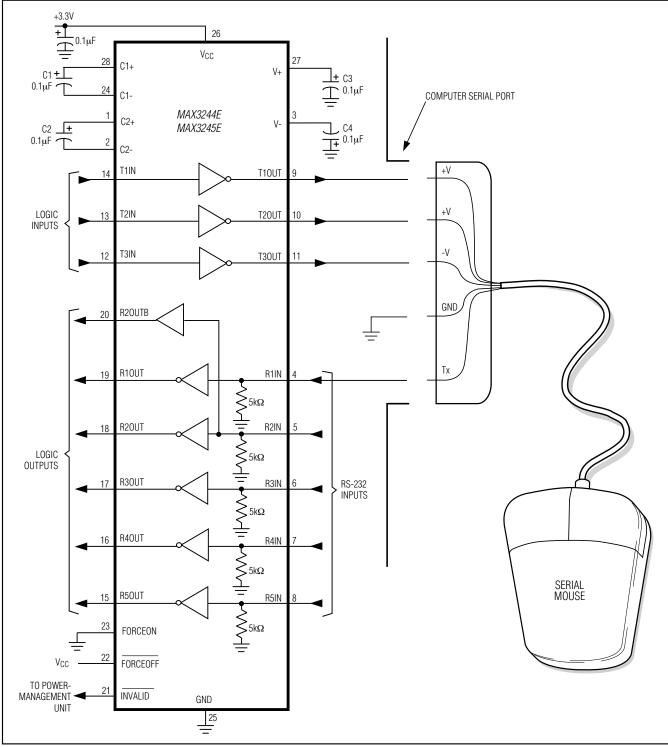


Figure 13a. Mouse Driver Test Circuit

MAX3224E/MAX3225E/ MAX3226E/MAX3227E/MAX3244E/MAX3245E ±15kV ESD-Protected, 1µA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

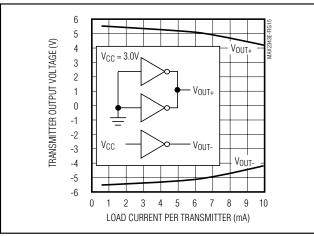


Figure 13b. MAX324_E Transmitter Output Voltage vs. Load Current per Transmitter

Mouse Driveability

The MAX3244E/MAX3245E are specifically designed to power serial mice while operating from low-voltage power supplies. They have been tested with leading mouse brands from manufacturers such as Microsoft and Logitech. The MAX3244E/MAX3245E successfully drove all serial mice tested and met their respective current and voltage requirements. The MAX3244E/ MAX3245E dual charge pump ensures the transmitters supply at least ±5V during worst-case conditions. Figure 13b shows the transmitter output voltages under increasing load current. Figure 13a shows a typical mouse connection.

Interconnection with 3V and 5V Logic The MAX3224E–MAX3227E/MAX3244E/MAX3245E can directly interface with various 5V logic families, including ACT and HCT CMOS. See Table 4 for more information on possible combinations of interconnections.

Table 5 lists other Maxim ESD-powered transceivers.

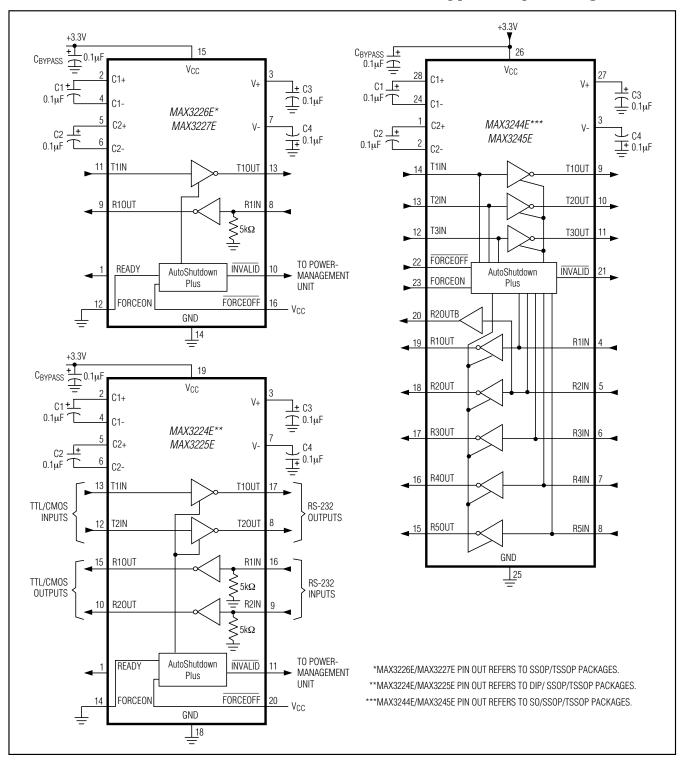
Table 4. Logic Family Compatibility with Various Supply Voltages

SYSTEM POWER-SUPPLY VOLTAGE (V)	V _{CC} SUPPLY VOLTAGE (V)	COMPATIBILITY
3.3	3.3	Compatible with all CMOS families
5	5	Compatible with all TTL and CMOS families
5	3.3	Compatible with ACT and HCT CMOS, and with AC, HC, or CD4000 CMOS

Table 5. ±15kV ESD-Protected, 3.0V to 5.5V Powered RS-232 Transceivers from Maxim

PART	SUPPLY VOLTAGE RANGE (V)	NO. OF Tx/Rx	SUPPLY CURRENT (µA)	Auto- Shutdown Plus	Auto- Shutdown	Human Body Model (kV)	IEC 1000-4-2 Contact Discharge (kV)	IEC 1000-4-2 Air-Gap Discharge (kV)	GUARANTEED DATA RATE (kbps)
MAX3241E	+3.0 to +5.5	3/5	300	_	_	±15	±8	±15	250
MAX3243E	+3.0 to +5.5	3/5	1	_	Yes	±15	±8	±15	250
MAX3244E	+3.0 to +5.5	3/5	1	Yes	—	±15	±8	±15	250
MAX3245E	+3.0 to +5.5	3/5	1	Yes		±15	±8	±15	1Mbps
MAX3232E	+3.0 to +5.5	2/2	300	_	_	±15	±8	±15	250
MAX3222E	+3.0 to +5.5	2/2	300	_		±15	±8	±15	250
MAX3223E	+3.0 to +5.5	2/2	1	—	Yes	±15	±8	±15	250
MAX3224E	+3.0 to +5.5	2/2	1	Yes		±15	±8	±15	250
MAX3225E	+3.0 to +5.5	2/2	1	Yes	_	±15	±8	±15	1Mbps
MAX3221E	+3.0 to +5.5	1/1	1		Yes	±15	±8	±15	250
MAX3226E	+3.0 to +5.5	1/1	1	Yes	_	±15	±8	±15	250
MAX3227E	+3.0 to +5.5	1/1	1	Yes		±15	±8	±15	1Mbps

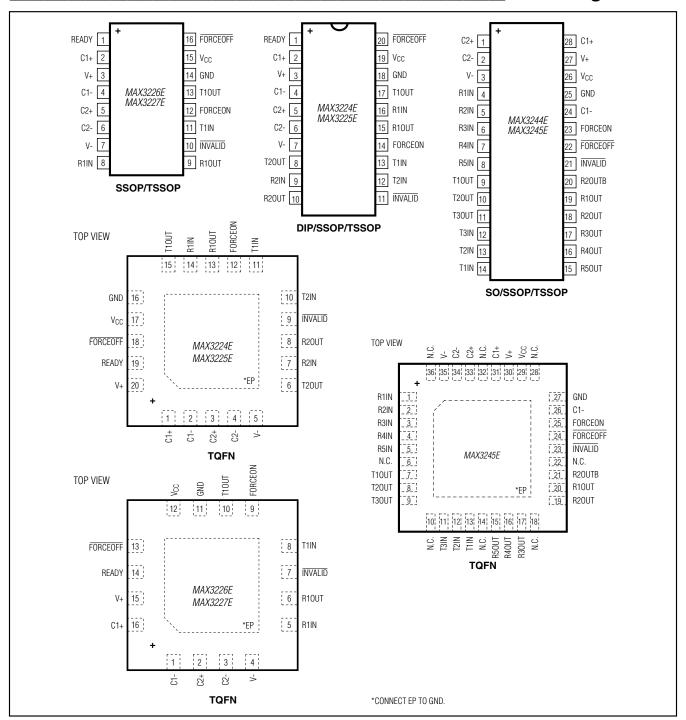
±15kV ESD-Protected, 1μA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus



Typical Operating Circuits

MAX3224E/MAX3225E/ MAX3226E/MAX3227E/MAX3244E/MAX3245E ±15kV ESD-Protected, 1µA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

Pin Configurations



±15kV ESD-Protected, 1μA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

_Ordering Information (continued)

Chip Information

Package Information

For the latest package outline information and land patterns (footprints), go to <u>www.maxim-ic.com/packages</u>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

		-	
PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
16 SSOP	A16+2	<u>21-0056</u>	<u>90-0106</u>
16 TSSOP	U16+1	<u>21-0066</u>	<u>90-0117</u>
16 TQFN-EP	T1655+2	<u>21-0140</u>	<u>90-0072</u>
20 DIP	P20+3	<u>21-0043</u>	—
20 SSOP	A20+1	<u>21-0056</u>	<u>90-0094</u>
20 TSSOP	U20+2	<u>21-0066</u>	<u>90-0116</u>
20 TQFN-EP	T2055+5	<u>21-0140</u>	<u>90-0010</u>
28 Wide SO	W28+6	<u>21-0042</u>	<u>90-0109</u>
28 SSOP	A28+1	<u>21-0056</u>	<u>90-0095</u>
28 TSSOP	U28+2	<u>21-0066</u>	<u>90-0171</u>
36 TQFN	T3666+3	<u>21-0141</u>	<u>90-0050</u>

MAX3225ECTP+ 0°C to +70°C MAX3225ECAP+ 0°C to +70°C MAX3225ECPP+ 0°C to +70°C MAX3225ECPP+ 0°C to +70°C MAX3225EETP+ -40°C to +85°C	20 TSSOP 20 TQFN-EP* 20 SSOP 20 Plastic DIP 20 TQFN -EP* 20 TSSOP 20 SSOP
MAX3225ECAP+ 0°C to +70°C MAX3225ECPP+ 0°C to +70°C MAX3225EETP+ -40°C to +85°C	20 SSOP 20 Plastic DIP 20 TQFN -EP* 20 TSSOP
MAX3225ECPP+ 0°C to +70°C MAX3225EETP+ -40°C to +85°C	20 Plastic DIP 20 TQFN -EP* 20 TSSOP
MAX3225EETP+ -40°C to +85°C	20 TQFN -EP* 20 TSSOP
	20 TSSOP
MAX3225EEUP+ -40°C to +85°C	
	20 SSOP
MAX3225EEAP+ -40°C to +85°C	
MAX3225EEPP+ -40°C to +85°C	20 Plastic DIP
MAX3225EAAP+ -40°C to +125°C	20 SSOP
MAX3226ECTE+ 0°C to +70°C	16 TQFN-EP*
MAX3226ECUE+ 0°C to +70°C	16 TSSOP
MAX3226ECAE+ 0°C to +70°C	16 SSOP
MAX3226EEAE+ -40°C to +85°C	16 SSOP
MAX3226EETE+ -40°C to +85°C	16 TQFN-EP*
MAX3226EEUE+ -40°C to +85°C	16 TSSOP
MAX3226EAAE+ -40°C to +125°C	16 SSOP
MAX3227E CAE+ 0°C to +70°C	16 SSOP
MAX3227ECTE+ 0°C to +70°C	16 TQFN-EP*
MAX3227ECUE+ 0°C to +70°C	16 TSSOP
MAX3227EEAE + -40°C to +85°C	16 SSOP
MAX3227EEAE/V+ -40°C to +85°C	16 SSOP
MAX3227EETE+ -40°C to +85°C	16 TQFN-EP*
MAX3227EEUE+ -40°C to +85°C	16 TSSOP
MAX3227EAAE+ -40°C to +125°C	16 SSOP
MAX3244ECWI+ 0°C to +70°C	28 Wide SO
MAX3244ECAI+ 0°C to +70°C	28 SSOP
MAX3244ECUI+ 0°C to +70°C	28 TSSOP
MAX3244EEWI+ -40°C to +85°C	28 Wide SO
MAX3244EEAI+ -40°C to +85°C	28 SSOP
MAX3244EEUI+ -40°C to +85°C	28 TSSOP
MAX3245ECWI+ 0°C to +70°C	28 Wide SO
MAX3245ECAI+ 0°C to +70°C	28 SSOP
MAX3245ECTX+ 0°C to +70°C	36 TQFN- EP*
MAX3245EEAI+ -40°C to +85°C	28 SSOP
MAX3245EEWI+ -40°C to +85°C	28 WIDE SO
MAX3245EEUI+ -40°C to +85°C	28 TSSOP
MAX3245EETX+ -40°C to +85°C	36 TQFN- EP*

+Denotes a lead(Pb)-free/RoHS-compliant package.

*EP = Exposed pad.

/V denotes an automotive qualified part.

±15kV ESD-Protected, 1μA, 1Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

Revision History

REVISION	REVISION	DESCRIPTION	PAGES
NUMBER	DATE		CHANGED
10	3/11	Added an automotive qualified part to the Ordering Information; changed all the parts listed in the Ordering Information to lead free	1, 20



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