

JW050F, JW075F, JW100F, JW150F Power Modules: dc-dc Converters; 36 to 75 Vdc Input, 3.3 Vdc Output; 33 W to 99 W



The JW050F, JW075F, JW100F, and JW150F Power Modules use advanced, surface-mount technology and deliver high-quality, efficient, and compact dc-dc conversion.

Applications

- Distributed power architectures
- Workstations
- Computer equipment
- Communications equipment

Options

- Heat sinks available for extended operation
- Choice of remote on/off logic configuration

Description

The JW050F, JW075F, JW100F, and JW150F Power Modules are dc-dc converters that operate over an input voltage range of 36 Vdc to 75 Vdc and provide a precisely regulated dc output. The outputs are fully isolated from the inputs, allowing versatile polarity configurations and grounding connections. The modules have maximum power ratings from 33 W to 99 W at a typical full-load efficiency of 80%.

The sealed modules offer a metal baseplate for excellent thermal performance. Threaded-through holes are provided to allow easy mounting or addition of a heat sink for high-temperature applications. The standard feature set includes remote sensing, output trim, and remote on/off for convenient flexibility in distributed power applications.

Features

- Small size: 61.0 mm x 57.9 mm x 12.7 mm (2.40 in. x 2.28 in. x 0.50 in.)
- High power density
- High efficiency: 80% typical
- Low output noise
- Constant frequency
- Industry-standard pinout
- Metal baseplate
- 2:1 input voltage range
- Overtemperature protection (66 W and 99 W only)
- Overcurrent and overvoltage protection
- Remote sense
- Remote on/off
- Adjustable output voltage: 60% to 110% of $V_{O, nom}$
- Case ground pin
- ISO9001 Certified manufacturing facilities
- *UL** 1950 Recognized, *CSA*† C22.2 No. 950-95 Certified, and VDE 0805 (EN60950, IEC950) Licensed
- CE mark meets 73/23/EEC and 93/68/EEC directives‡

* *UL* is a registered trademark of Underwriters Laboratories, Inc.

† *CSA* is a registered trademark of Canadian Standards Assn.

‡ This product is intended for integration into end-use equipment. All the required procedures for CE marking of end-use equipment should be followed. (The CE mark is placed on selected products.)

Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Symbol	Min	Max	Unit
Input Voltage:				
Continuous:				
JW050F, JW075F	V_I	—	75	Vdc
JW100F, JW150F	V_I	—	80	Vdc
Transient (100 ms; JW100F, JW150F only)	$V_{I, trans}$	—	100	V
I/O Isolation Voltage (for 1 minute)	—	—	1500	Vdc
Operating Case Temperature (See Thermal Considerations section.)	T_C	-40	100	°C
Storage Temperature	T_{stg}	-55	125	°C

Electrical Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

Table 1. Input Specifications

Parameter	Symbol	Min	Typ	Max	Unit
Operating Input Voltage	V_I	36	48	75	Vdc
Maximum Input Current ($V_I = 0$ V to 75 V; $I_o = I_{o, max}$):					
JW050F (See Figure 1.)	$I_{I, max}$	—	—	1.2	A
JW075F (See Figure 2.)	$I_{I, max}$	—	—	1.8	A
JW100F (See Figure 3.)	$I_{I, max}$	—	—	2.4	A
JW150F (See Figure 4.)	$I_{I, max}$	—	—	3.7	A
Inrush Transient	i^2t	—	—	1.0	A ² s
Input Reflected-ripple Current, Peak-to-peak (5 Hz to 20 MHz, 12 μ H source impedance; see Figure 17.)	I_I	—	5	—	mAp-p
Input Ripple Rejection (120 Hz)	—	—	60	—	dB

Fusing Considerations

CAUTION: This power module is not internally fused. An input line fuse must always be used.

This encapsulated power module can be used in a wide variety of applications, ranging from simple stand-alone operation to an integrated part of a sophisticated power architecture. To preserve maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a normal-blow, dc fuse with a maximum rating of 20 A (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data for further information.

Electrical Specifications (continued)

Table 2. Output Specifications

Parameter	Device	Symbol	Min	Typ	Max	Unit
Output Voltage Set Point ($V_I = 48\text{ V}$; $I_O = I_{O, \text{max}}$; $T_C = 25\text{ }^\circ\text{C}$)	All	$V_{O, \text{set}}$	3.25	3.3	3.35	Vdc
Output Voltage (Over all operating input voltage, resistive load, and temperature conditions until end of life. See Figure 19.)	All	V_O	3.20	—	3.40	Vdc
Output Regulation:						
Line ($V_I = 36\text{ V}$ to 75 V)	All	—	—	0.01	0.1	% V_O
Load ($I_O = I_{O, \text{min}}$ to $I_{O, \text{max}}$)	All	—	—	0.05	0.2	% V_O
Temperature ($T_C = -40\text{ }^\circ\text{C}$ to $+100\text{ }^\circ\text{C}$)	All	—	—	15	50	mV
Output Ripple and Noise Voltage (See Figure 18.):						
RMS	All	—	—	—	40	mVrms
Peak-to-peak (5 Hz to 20 MHz)	All	—	—	—	150	mVp-p
External Load Capacitance	All	—	0	—	*	μF
Output Current (At $I_O < I_{O, \text{min}}$, the modules may exceed output ripple specifications.)	JW050F JW075F JW100F JW150F	I_O I_O I_O I_O	0.5 0.5 0.5 0.5	— — — —	10 15 20 30	A A A A
Output Current-limit Inception ($V_O = 90\%$ of $V_{O, \text{nom}}$)	JW050F JW075F JW100F JW150F	$I_{O, \text{cli}}$ $I_{O, \text{cli}}$ $I_{O, \text{cli}}$ $I_{O, \text{cli}}$	— — — —	12.0 18.0 23.0 34.5	14 [†] 21 [†] 26 [†] 39 [†]	A A A A
Output Short-circuit Current ($V_O = 250\text{ mV}$)	All	—	—	170	—	% $I_{O, \text{max}}$
Efficiency ($V_I = 48\text{ V}$; $I_O = I_{O, \text{max}}$; $T_C = 70\text{ }^\circ\text{C}$)	JW050F JW075F JW100F JW150F	η η η η	— — — —	80 80 80 80	— — — —	% % % %
Switching Frequency	All	—	—	500	—	kHz
Dynamic Response ($\dot{I}_O/\dot{I}_T = 1\text{ A}/10\text{ }\mu\text{s}$, $V_I = 48\text{ V}$, $T_C = 25\text{ }^\circ\text{C}$; tested with a $10\text{ }\mu\text{F}$ aluminum and a $1.0\text{ }\mu\text{F}$ ceramic capacitor across the load; see Figures 14 and 15):						
Load Change from $I_O = 50\%$ to 75% of $I_{O, \text{max}}$:						
Peak Deviation	All	—	—	3.8	—	% $V_{O, \text{set}}$
Settling Time ($V_O < 10\%$ of peak deviation)	All	—	—	300	—	μs
Load Change from $I_O = 50\%$ to 25% of $I_{O, \text{max}}$:						
Peak Deviation	All	—	—	3.8	—	% $V_{O, \text{set}}$
Settling Time ($V_O < 10\%$ of peak deviation)	All	—	—	300	—	μs

* Consult your sales representative or the factory.

† These are manufacturing test limits. In some situations, results may differ.

Electrical Specifications (continued)

Table 3. Isolation Specifications

Parameter	Min	Typ	Max	Unit
Isolation Capacitance	—	2500	—	pF
Isolation Resistance	10	—	—	M ³ / ₄

General Specifications

Parameter	Min	Typ	Max	Unit
Calculated MTBF ($I_o = 80\%$ of $I_{o, max}$; $T_c = 40^\circ\text{C}$)	2,600,000			hr.
Weight	—	—	100 (3.5)	g (oz.)

Feature Specifications

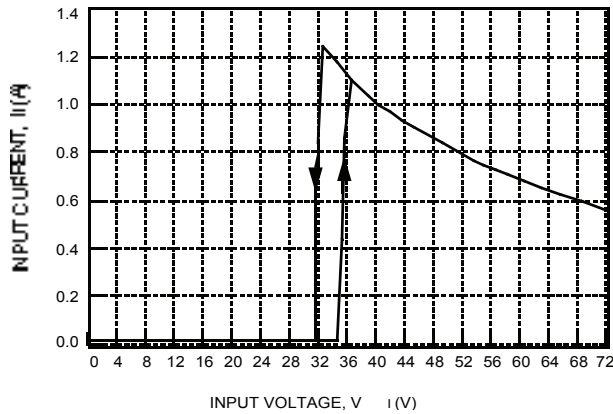
Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for additional information.

Parameter	Symbol	Min	Typ	Max	Unit
Remote On/Off Signal Interface ($V_i = 0\text{ V}$ to 75 V ; open collector or equivalent compatible; signal referenced to $V_i(-)$ terminal; see Figure 20 and Feature Descriptions.): JWxxxF1 Preferred Logic: Logic Low—Module On Logic High—Module Off JWxxxF Optional Logic: Logic Low—Module Off Logic High—Module On Logic Low: At $I_{on/off} = 1.0\text{ mA}$ At $V_{on/off} = 0.0\text{ V}$ Logic High: At $I_{on/off} = 0.0\text{ }\mu\text{A}$ Leakage Current Turn-on Time (See Figure 16.) ($I_o = 80\%$ of $I_{o, max}$; V_o within $\pm 1\%$ of steady state)					
Remote On/Off Signal Interface ($V_i = 0\text{ V}$ to 75 V ; open collector or equivalent compatible; signal referenced to $V_i(-)$ terminal; see Figure 20 and Feature Descriptions.): JWxxxF1 Preferred Logic: Logic Low—Module On Logic High—Module Off JWxxxF Optional Logic: Logic Low—Module Off Logic High—Module On Logic Low: At $I_{on/off} = 1.0\text{ mA}$ At $V_{on/off} = 0.0\text{ V}$ Logic High: At $I_{on/off} = 0.0\text{ }\mu\text{A}$ Leakage Current Turn-on Time (See Figure 16.) ($I_o = 80\%$ of $I_{o, max}$; V_o within $\pm 1\%$ of steady state)	$V_{on/off}$	0	—	1.2	V
Remote On/Off Signal Interface ($V_i = 0\text{ V}$ to 75 V ; open collector or equivalent compatible; signal referenced to $V_i(-)$ terminal; see Figure 20 and Feature Descriptions.): JWxxxF1 Preferred Logic: Logic Low—Module On Logic High—Module Off JWxxxF Optional Logic: Logic Low—Module Off Logic High—Module On Logic Low: At $I_{on/off} = 1.0\text{ mA}$ At $V_{on/off} = 0.0\text{ V}$ Logic High: At $I_{on/off} = 0.0\text{ }\mu\text{A}$ Leakage Current Turn-on Time (See Figure 16.) ($I_o = 80\%$ of $I_{o, max}$; V_o within $\pm 1\%$ of steady state)	$I_{on/off}$	—	—	1.0	mA
Remote On/Off Signal Interface ($V_i = 0\text{ V}$ to 75 V ; open collector or equivalent compatible; signal referenced to $V_i(-)$ terminal; see Figure 20 and Feature Descriptions.): JWxxxF1 Preferred Logic: Logic Low—Module On Logic High—Module Off JWxxxF Optional Logic: Logic Low—Module Off Logic High—Module On Logic Low: At $I_{on/off} = 1.0\text{ mA}$ At $V_{on/off} = 0.0\text{ V}$ Logic High: At $I_{on/off} = 0.0\text{ }\mu\text{A}$ Leakage Current Turn-on Time (See Figure 16.) ($I_o = 80\%$ of $I_{o, max}$; V_o within $\pm 1\%$ of steady state)	$V_{on/off}$	—	—	15	V
Remote On/Off Signal Interface ($V_i = 0\text{ V}$ to 75 V ; open collector or equivalent compatible; signal referenced to $V_i(-)$ terminal; see Figure 20 and Feature Descriptions.): JWxxxF1 Preferred Logic: Logic Low—Module On Logic High—Module Off JWxxxF Optional Logic: Logic Low—Module Off Logic High—Module On Logic Low: At $I_{on/off} = 1.0\text{ mA}$ At $V_{on/off} = 0.0\text{ V}$ Logic High: At $I_{on/off} = 0.0\text{ }\mu\text{A}$ Leakage Current Turn-on Time (See Figure 16.) ($I_o = 80\%$ of $I_{o, max}$; V_o within $\pm 1\%$ of steady state)	$I_{on/off}$	—	—	50	μA
Remote On/Off Signal Interface ($V_i = 0\text{ V}$ to 75 V ; open collector or equivalent compatible; signal referenced to $V_i(-)$ terminal; see Figure 20 and Feature Descriptions.): JWxxxF1 Preferred Logic: Logic Low—Module On Logic High—Module Off JWxxxF Optional Logic: Logic Low—Module Off Logic High—Module On Logic Low: At $I_{on/off} = 1.0\text{ mA}$ At $V_{on/off} = 0.0\text{ V}$ Logic High: At $I_{on/off} = 0.0\text{ }\mu\text{A}$ Leakage Current Turn-on Time (See Figure 16.) ($I_o = 80\%$ of $I_{o, max}$; V_o within $\pm 1\%$ of steady state)	—	—	20	35	ms
Output Voltage Adjustment (See Feature Descriptions.):					
Output Voltage Remote-sense Range	—	—	—	0.5	V
Output Voltage Set-point Adjustment Range (trim)	—	60	—	110	$\%V_{O, nom}$
Output Overvoltage Protection	$V_{O, clamp}$	4.0*	—	5.0*	V
Overtemperature Protection (shutdown) (66 W and 99 W only; see Feature Descriptions.)	T_c	—	105	—	$^\circ\text{C}$

* These are manufacturing test limits. In some situations, results may differ.

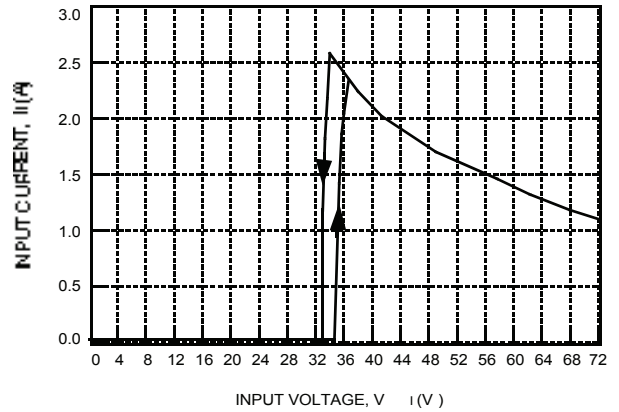
Characteristic Curves

The following figures provide typical characteristics for the power modules. The figures are identical for both on/off configurations.



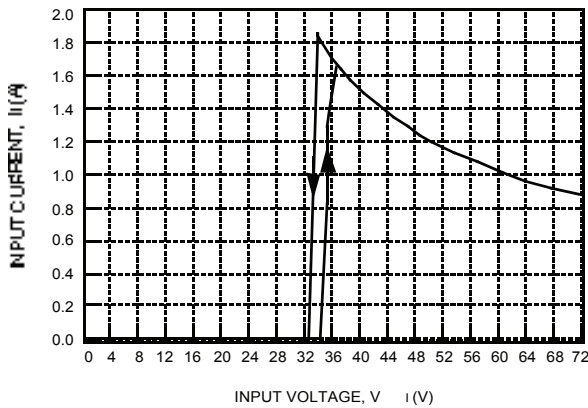
8-1446 (C)

Figure 1. Typical JW050F Input Characteristics at Room Temperature



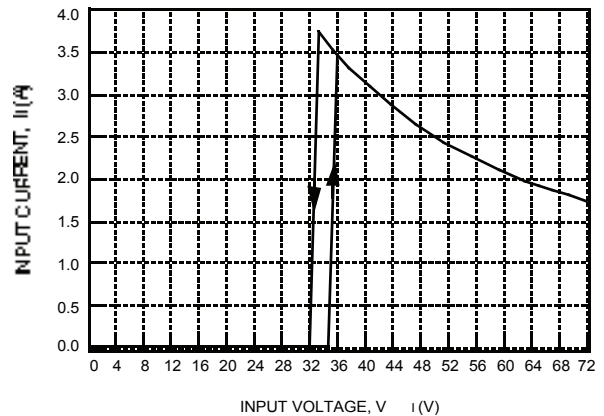
8-1448 (C)

Figure 3. Typical JW100F Input Characteristics at Room Temperature



8-1447 (C)

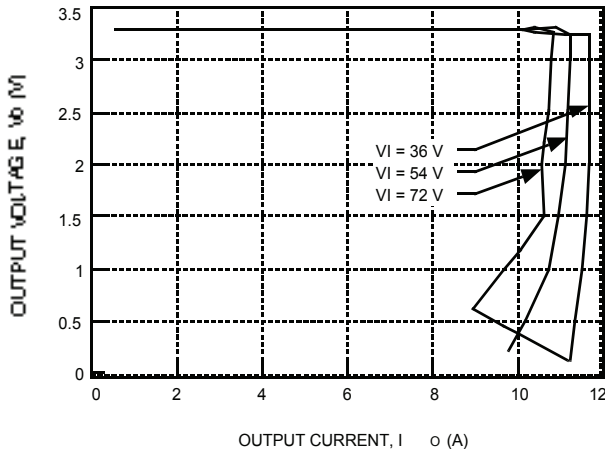
Figure 2. Typical JW075F Input Characteristics at Room Temperature



8-1449 (C)

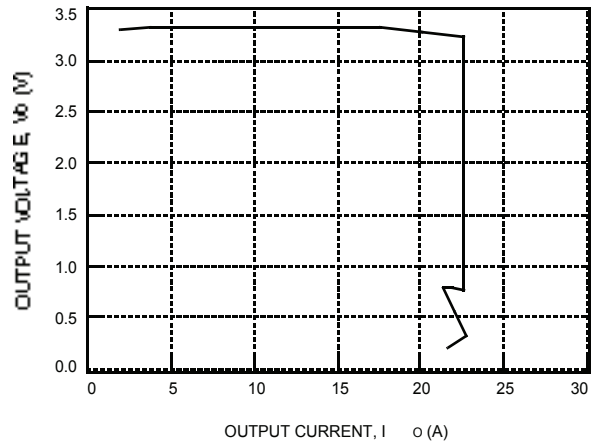
Figure 4. Typical JW150F Input Characteristics at Room Temperature

Characteristic Curves (continued)



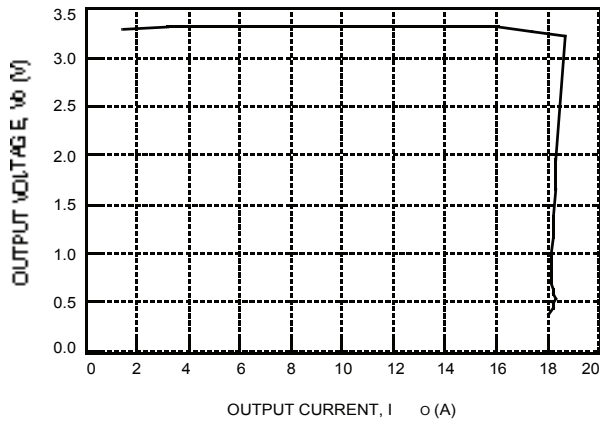
8-2159 (C)

Figure 5. Typical JW050F Output Characteristics at Room Temperature



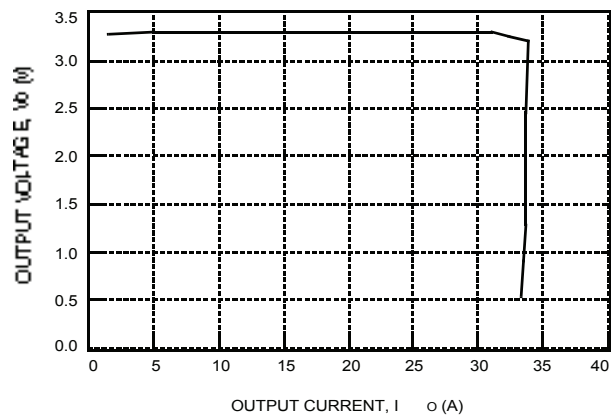
8-1452 (C)

Figure 7. Typical JW100F Output Characteristics at Room Temperature



8-1451 (C)

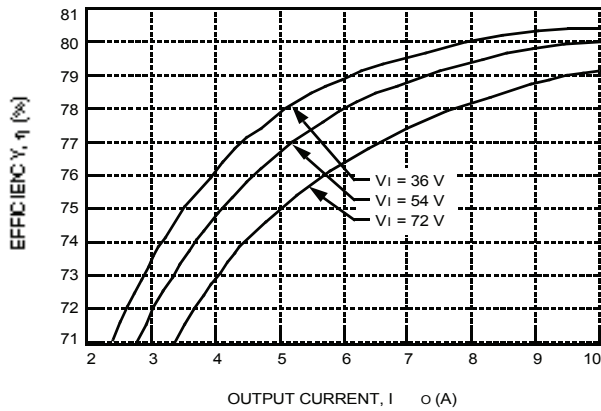
Figure 6. Typical JW075F Output Characteristics at Room Temperature



8-1453 (C)

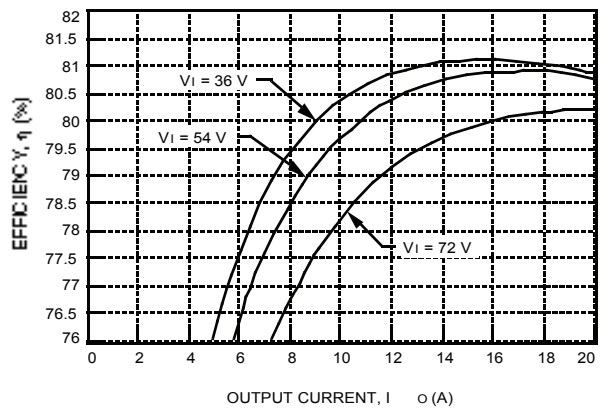
Figure 8. Typical JW150F Output Characteristics at Room Temperature

Characteristic Curves (continued)



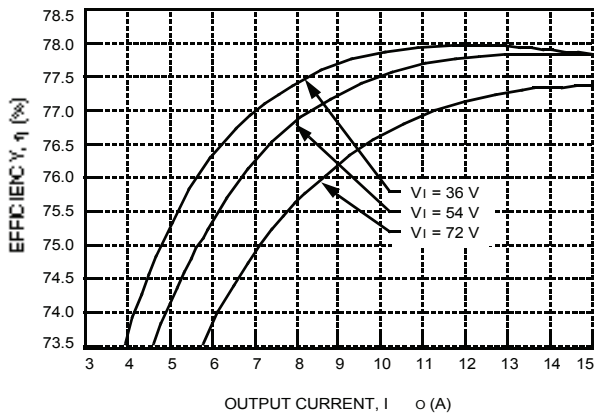
8-1454 (C)

Figure 9. Typical JW050F Converter Efficiency vs. Output Current at Room Temperature



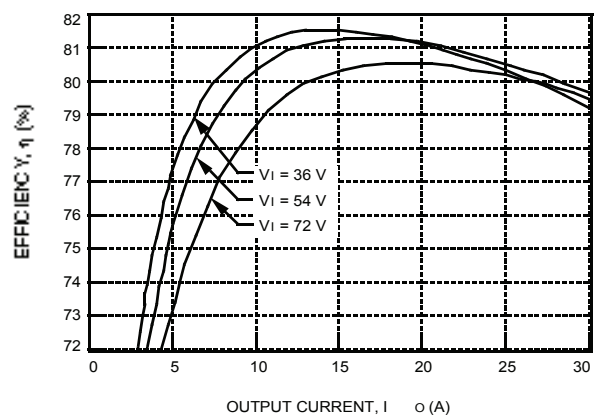
8-1456 (C)

Figure 11. Typical JW100F Converter Efficiency vs. Output Current at Room Temperature



8-1455 (C)

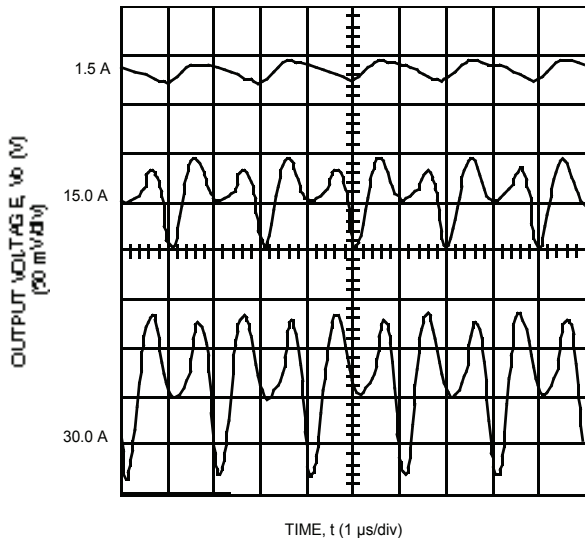
Figure 10. Typical JW075F Converter Efficiency vs. Output Current at Room Temperature



8-1457 (C)

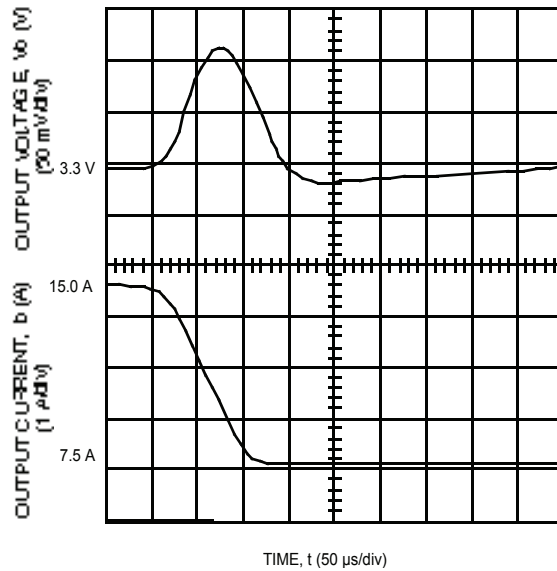
Figure 12. Typical JW150F Converter Efficiency vs. Output Current at Room Temperature

Characteristic Curves (continued)



8-2002 (C)

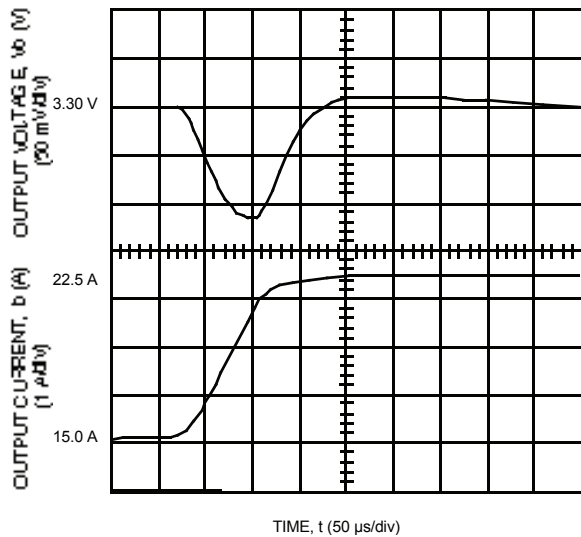
Figure 13. Typical JW150F Output Ripple Voltage at Room Temperature, $I_o = \text{Full Load}$



8-2001 (C)

Note: Tested with a 10 μF aluminum and a 1.0 μF ceramic capacitor across the load.

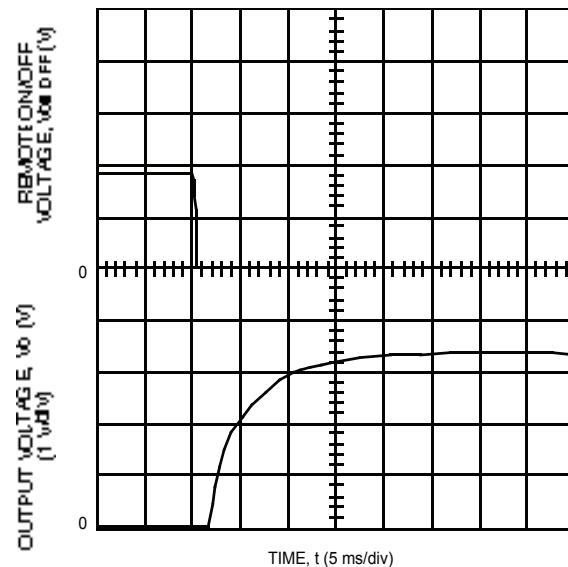
Figure 15. Typical JW150F Transient Response to Step Decrease in Load from 50% to 25% of Full Load at Room Temperature and 48 V Input (Waveform Averaged to Eliminate Ripple Component.)



8-2000 (C)

Note: Tested with a 10 μF aluminum and a 1.0 μF ceramic capacitor across the load.

Figure 14. Typical JW150F Transient Response to Step Increase in Load from 50% to 75% of Full Load at Room Temperature and 48 V Input (Waveform Averaged to Eliminate Ripple Component.)

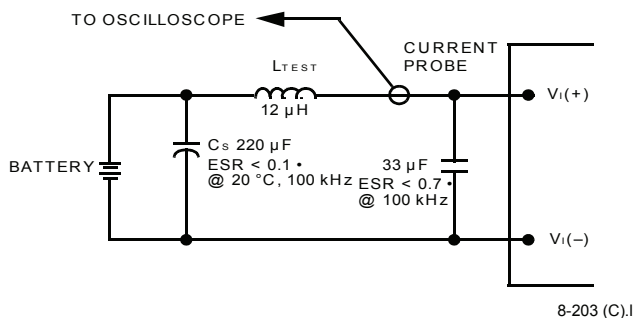


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Note: Tested with a 10 μF aluminum and a 1.0 μF ceramic capacitor across the load.

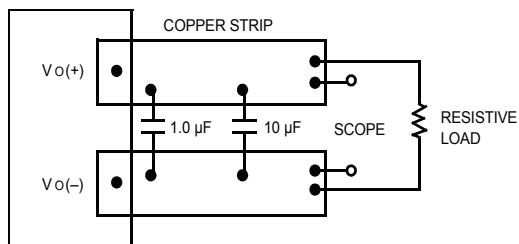
Figure 16. Typical Start-Up from Remote On/Off JW150F1; $I_o = I_{o, \text{max}}$

Test Configurations



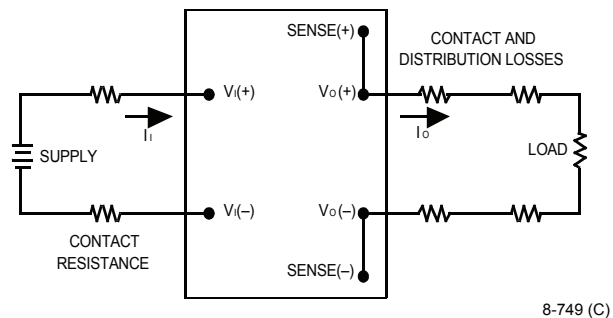
Note: Measure input reflected-ripple current with a simulated source inductance (L_{TEST}) of 12 μH . Capacitor C_S offsets possible battery impedance. Measure current as shown above.

Figure 17. Input Reflected-Ripple Test Setup



Note: Use a 1.0 μF ceramic capacitor and a 10 μF aluminum or tantalum capacitor. Scope measurement should be made using a BNC socket. Position the load between 51 mm and 76 mm (2 in. and 3 in.) from the module.

Figure 18. Peak-to-Peak Output Noise Measurement Test Setup



Note: All measurements are taken at the module terminals. When socketing, place Kelvin connections at module terminals to avoid measurement errors due to socket contact resistance.

$$\eta = \left(\frac{[V_O(+)-V_O(-)]I_O}{[V_I(+)-V_I(-)]I_I} \right) \times 100 \quad \%$$

Figure 19. Output Voltage and Efficiency Measurement Test Setup

Design Considerations

Input Source Impedance

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. For the test configuration in Figure 17, a 33 μF electrolytic capacitor (ESR < 0.7 $\frac{3}{4}$ at 100 kHz) mounted close to the power module helps ensure stability of the unit. For other highly inductive source impedances, consult the factory for further application guidelines.

Safety Considerations

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e., *UL1950*, *CSA C22.2 No. 950-95*, and *VDE 0805 (EN60950, IEC950)*.

If the input source is non-SELV (ELV or a hazardous voltage greater than 60 Vdc and less than or equal to 75 Vdc), for the module's output to be considered meeting the requirements of safety extra-low voltage (SELV), all of the following must be true:

- The input source is to be provided with reinforced insulation from any hazardous voltages, including the ac mains.
- One V_I pin and one V_O pin are to be grounded or both the input and output pins are to be kept floating.
- The input pins of the module are not operator accessible.
- Another SELV reliability test is conducted on the whole system, as required by the safety agencies, on the combination of supply source and the subject module to verify that under a single fault, hazardous voltages do not appear at the module's output.

Note: Do not ground either of the input pins of the module without grounding one of the output pins. This may allow a non-SELV voltage to appear between the output pin and ground.

The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

The input to these units is to be provided with a maximum 20 A normal-blow fuse in the ungrounded lead.

Feature Descriptions

Overcurrent Protection

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current limiting for an unlimited duration. At the point of current-limit inception, the unit shifts from voltage control to current control. If the output voltage is pulled very low during a severe fault, the current-limit circuit can exhibit either foldback or tailout characteristics (output current decrease or increase). The unit operates normally once the output current is brought back into its specified range.

Remote On/Off

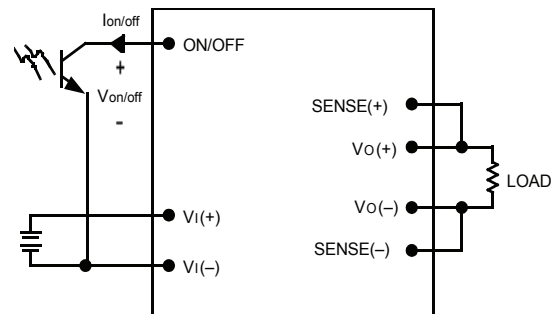
Two remote on/off options are available. Positive logic remote on/off turns the module on during a logic-high voltage on the ON/OFF pin, and off during a logic low. Negative logic remote on/off turns the module off during a logic high and on during a logic low. Negative logic (code suffix 1) is the factory-preferred configuration.

To turn the power module on and off, the user must supply a switch to control the voltage between the on/off terminal and the $V_I(-)$ terminal ($V_{on/off}$). The switch can be an open collector or equivalent (see Figure 20). A logic low is $V_{on/off} = 0$ V to 1.2 V. The maximum $I_{on/off}$ during a logic low is 1 mA. The switch should maintain a logic-low voltage while sinking 1 mA.

During a logic high, the maximum $V_{on/off}$ generated by the power module is 15 V. The maximum allowable leakage current of the switch at $V_{on/off} = 15$ V is 50 μ A.

If not using the remote on/off feature, do one of the following:

- For negative logic, short ON/OFF pin to $V_I(-)$.
- For positive logic, leave ON/OFF pin open.



8-720 (C).c

Figure 20. Remote On/Off Implementation

Feature Descriptions (continued)

Remote Sense

Remote sense minimizes the effects of distribution losses by regulating the voltage at the remote-sense connections. The voltage between the remote-sense pins and the output terminals must not exceed the output voltage sense range given in the Feature Specifications table, i.e.:

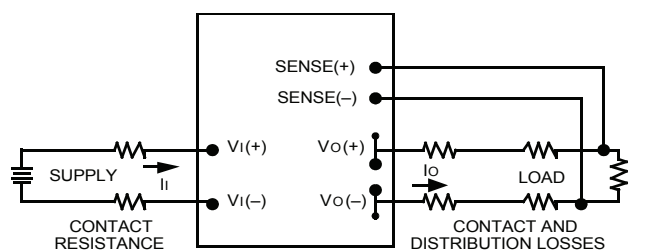
$$[V_{O(+)} - V_{O(-)}] - [SENSE(+)-SENSE(-)] \delta 0.5 V$$

The voltage between the $V_{O(+)}$ and $V_{O(-)}$ terminals must not exceed the minimum value of the output over-voltage protection. This limit includes any increase in voltage due to remote-sense compensation and output voltage set-point adjustment (trim). See Figure 21.

If not using the remote-sense feature to regulate the output at the point of load, then connect SENSE(+) to $V_{O(+)}$ and SENSE(-) to $V_{O(-)}$ at the module.

Although the output voltage can be increased by both the remote sense and by the trim, the maximum increase for the output voltage is not the sum of both. The maximum increase is the larger of either the remote sense or the trim. Consult the factory if you need to increase the output voltage more than the above limitation.

The amount of power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. When using remote sense and trim, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.



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Figure 21. Effective Circuit Configuration for Single-Module Remote-Sense Operation

Output Voltage Set-Point Adjustment (Trim)

Output voltage trim allows the user to increase or decrease the output voltage set point of a module. This is accomplished by connecting an external resistor between the TRIM pin and either the SENSE(+) or SENSE(-) pins. The trim resistor should be positioned close to the module.

If not using the trim feature, leave the TRIM pin open.

With an external resistor between the TRIM and SENSE(-) pins ($R_{adj-down}$), the output voltage set point ($V_{O, adj}$) decreases (see Figure 22). The following equation determines the required external-resistor value to obtain a percentage output voltage change of $\Delta\%$.

$$R_{adj-down} = \left(\frac{100}{\Delta\%} - 2 \right) k\Omega$$

The test results for this configuration are displayed in Figure 23. This figure applies to all output voltages.

With an external resistor connected between the TRIM and SENSE(+) pins (R_{adj-up}), the output voltage set point ($V_{O, adj}$) increases (see Figure 24).

The following equation determines the required external-resistor value to obtain a percentage output voltage change of $\Delta\%$.

$$R_{adj-up} = \left(\frac{V_O(100 + \Delta\%)}{1.225\Delta\%} - \frac{(100 + 2\Delta\%) V_O}{\Delta\%} \right) k\Omega$$

The test results for this configuration are displayed in Figure 25.

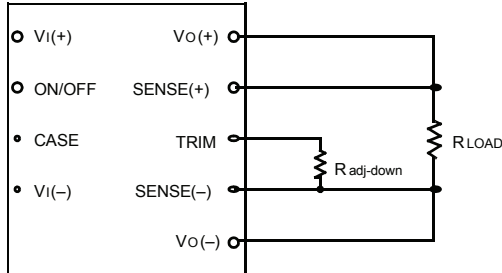
The voltage between the $V_{O(+)}$ and $V_{O(-)}$ terminals must not exceed the minimum value of the output over-voltage protection. This limit includes any increase in voltage due to remote-sense compensation and output voltage set-point adjustment (trim). See Figure 21.

Although the output voltage can be increased by both the remote sense and by the trim, the maximum increase for the output voltage is not the sum of both. The maximum increase is the larger of either the remote sense or the trim. Consult the factory if you need to increase the output voltage more than the above limitation.

The amount of power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. When using remote sense and trim, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

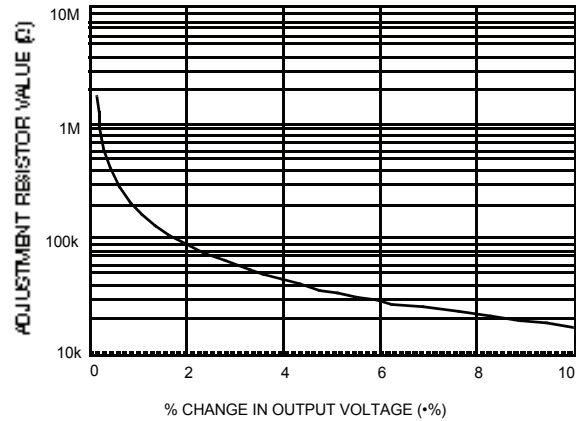
Feature Descriptions (continued)

Output Voltage Set-Point Adjustment (Trim) (continued)



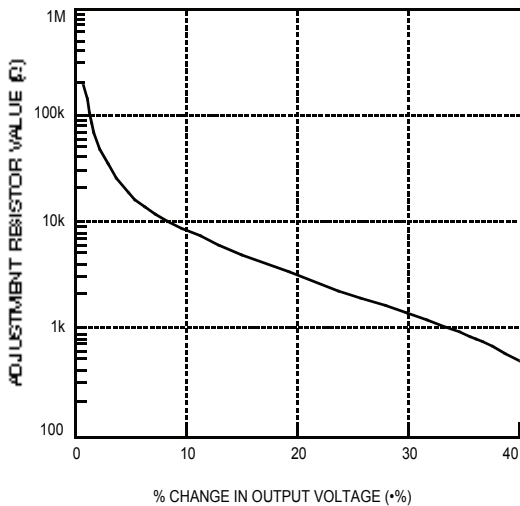
8-748 (C).b

Figure 22. Circuit Configuration to Decrease Output Voltage



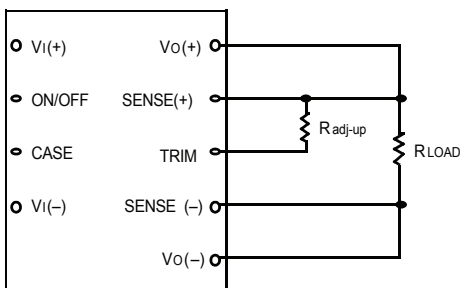
8-2090 (C)

Figure 25. Resistor Selection for Increased Output Voltage



8-879 (C)

Figure 23. Resistor Selection for Decreased Output Voltage



8-715 (C).b

Figure 24. Circuit Configuration to Increase Output Voltage

Output Overvoltage Protection

The output overvoltage clamp consists of control circuitry, independent of the primary regulation loop, that monitors the voltage on the output terminals. The control loop of the clamp has a higher voltage set point than the primary loop (see Feature Specifications table). This provides a redundant voltage control that reduces the risk of output overvoltage.

Overtemperature Protection

The 100 W and 150 W modules feature an overtemperature protection circuit to safeguard against thermal damage.

The circuit shuts down the module when the maximum case temperature is exceeded. The module restarts automatically after cooling.

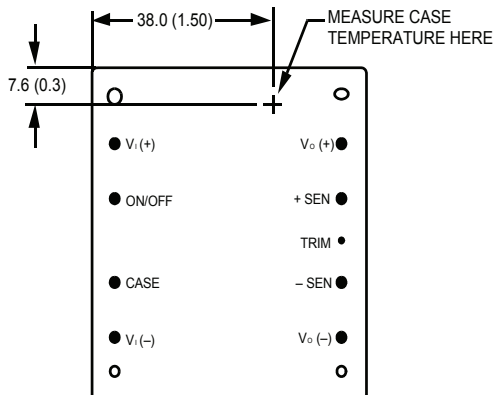
Thermal Considerations

Introduction

The power modules operate in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat-dissipating components inside the unit are thermally coupled to the case. Heat is removed by conduction, convection, and radiation to the surrounding environment. Proper cooling can be verified by measuring the case temperature. Peak temperature (T_c) occurs at the position indicated in Figure 26.

Thermal Considerations (continued)

Introduction (continued)



8-716 (C),f

Note: Top view, pin locations are for reference only.
Measurements shown in millimeters and (inches).

Figure 26. Case Temperature Measurement Location

The temperature at this location should not exceed 100 °C. The output power of the module should not exceed the rated power for the module as listed in the Ordering Information table.

Although the maximum case temperature of the power modules is 100 °C, you can limit this temperature to a lower value for extremely high reliability.

For additional information on these modules, refer to the *Thermal Management JC-, JFC-, JW-, and JFW-Series 50 W to 150 W Board-Mounted Power Modules* Technical Note (TN97-008EPS).

Heat Transfer Without Heat Sinks

Increasing airflow over the module enhances the heat transfer via convection. Figure 31 shows the maximum power that can be dissipated by the module without exceeding the maximum case temperature versus local ambient temperature (T_A) for natural convection through 4 m/s (800 ft./min.).

Note that the natural convection condition was measured at 0.05 m/s to 0.1 m/s (10 ft./min. to 20 ft./min.); however, systems in which these power modules may be used typically generate natural convection airflow rates of 0.3 m/s (60 ft./min.) due to other heat dissipating components in the system. The use of Figure 31 is shown in the following example.

Example

What is the minimum airflow necessary for a JW100F operating at $V_I = 54$ V, an output current of 20 A, and a maximum ambient temperature of 40 °C?

Solution

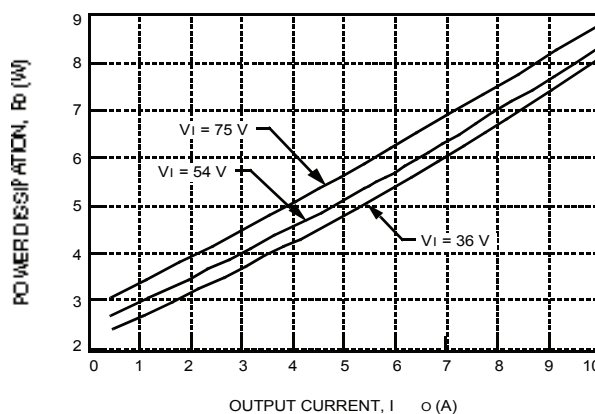
Given: $V_I = 54$ V
 $I_O = 20$ A
 $T_A = 40$ °C

Determine P_D (Use Figure 29.):

$$P_D = 15.8 \text{ W}$$

Determine airflow (v) (Use Figure 31.):

$$v = 1.7 \text{ m/s (340 ft./min.)}$$

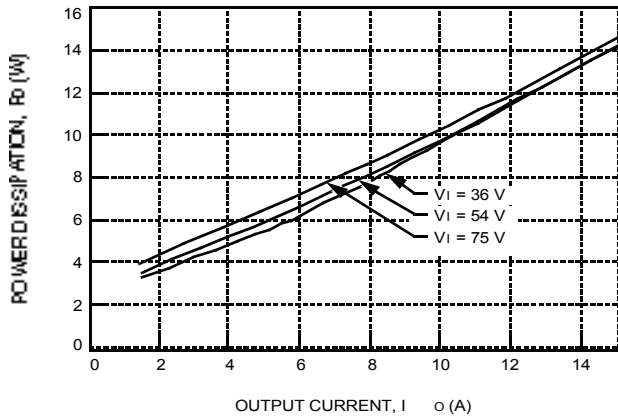


8-1459 (C)

Figure 27. JW050F Power Dissipation vs. Output Current

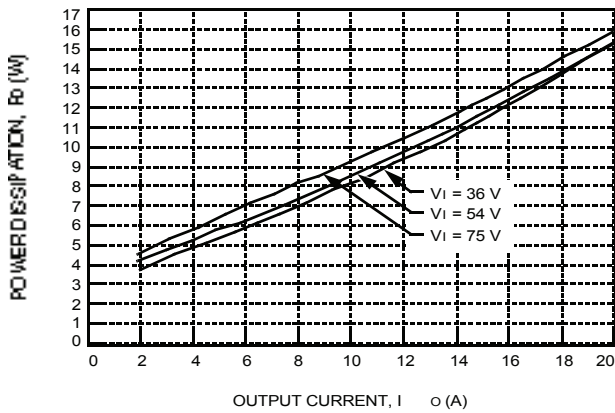
Thermal Considerations (continued)

Heat Transfer Without Heat Sinks (continued)



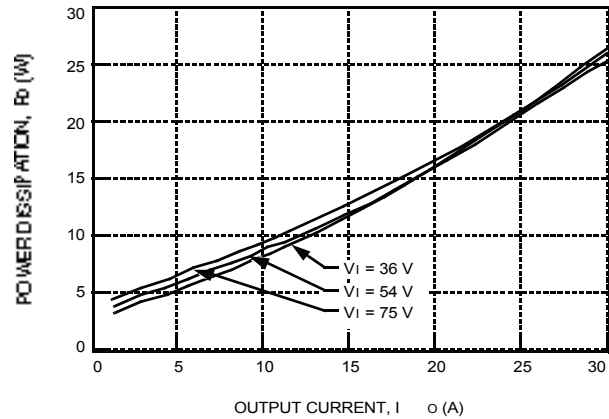
8-1460 (C)

Figure 28. JW075F Power Dissipation vs. Output Current



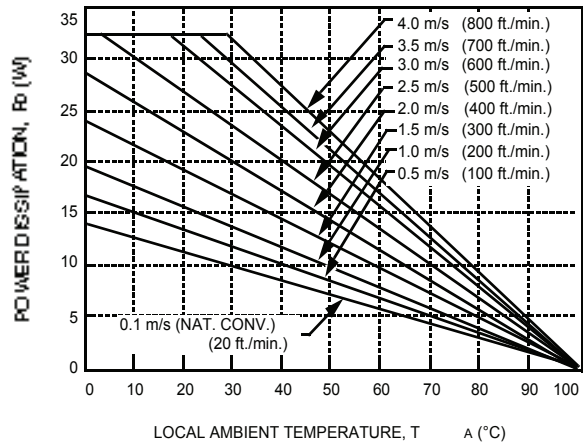
8-1461 (C)

Figure 29. JW100F Power Dissipation vs. Output Current



8-1462 (C)

Figure 30. JW150F Power Dissipation vs. Output Current



8-1150 (C).a

Figure 31. Forced Convection Power Derating with No Heat Sink; Either Orientation

Heat Transfer with Heat Sinks

The power modules have through-threaded, M3 x 0.5 mounting holes, which enable heat sinks or cold plates to attach to the module. The mounting torque must not exceed 0.56 N-m (5 in.-lb.). For a screw attachment from the pin side, the recommended hole size on the customer's PWB around the mounting holes is 0.130 ± 0.005 inches. If a larger hole is used, the mounting torque from the pin side must not exceed 0.25 N-m (2.2 in.-lb.).

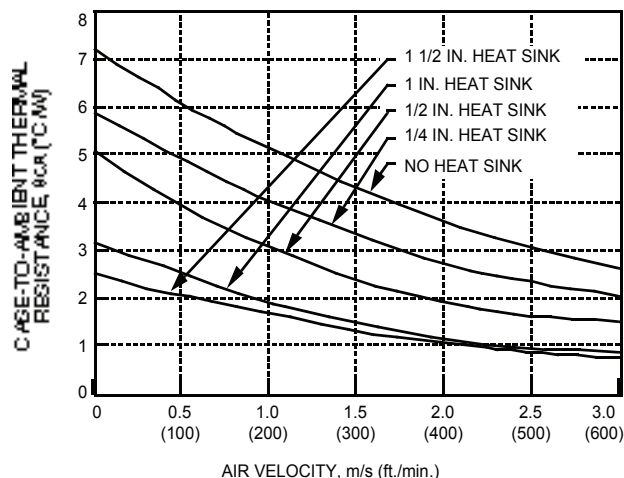
Thermal Considerations (continued)

Heat Transfer with Heat Sinks (continued)

Thermal derating with heat sinks is expressed by using the overall thermal resistance of the module. Total module thermal resistance (θ_{ca}) is defined as the maximum case temperature rise ($\Delta T_{C, \max}$) divided by the module power dissipation (P_D):

$$\theta_{ca} = \left[\frac{\Delta T_{C, \max}}{P_D} \right] = \left[\frac{(T_C - T_A)}{P_D} \right]$$

The location to measure case temperature (T_C) is shown in Figure 26. Case-to-ambient thermal resistance vs. airflow is shown, for various heat sink configurations and heights, in Figure 32. These curves were obtained by experimental testing of heat sinks, which are offered in the product catalog.



8-1153 (C)

Figure 32. Case-to-Ambient Thermal Resistance Curves; Either Orientation

These measured resistances are from heat transfer from the sides and bottom of the module as well as the top side with the attached heat sink; therefore, the case-to-ambient thermal resistances shown are generally lower than the resistance of the heat sink by itself. The module used to collect the data in Figure 32 had a thermal-conductive dry pad between the case and the heat sink to minimize contact resistance. The use of Figure 32 is shown in the following example.

Example

If an 85 °C case temperature is desired, what is the minimum airflow necessary? Assume the JW100F module is operating at $V_I = 54$ V and an output current of 20 A, maximum ambient air temperature of 40 °C, and the heat sink is 1/2 inch.

Solution

Given: $V_I = 54$ V
 $I_O = 20$ A
 $T_A = 40$ °C
 $T_C = 85$ °C
Heat sink = 1/2 in.

Determine P_D by using Figure 29:

$$P_D = 15.8 \text{ W}$$

Then solve the following equation:

$$\theta_{ca} = \left[\frac{(T_C - T_A)}{P_D} \right]$$

$$\theta_{ca} = \left[\frac{(85 - 40)}{15.8} \right]$$

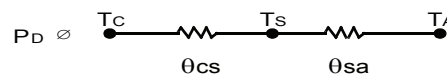
$$\theta_{ca} = 2.8 \text{ °C/W}$$

Use Figure 32 to determine air velocity for the 1/2 inch heat sink.

The minimum airflow necessary for the JW100F module is 1.1 m/s (220 ft./min.).

Custom Heat Sinks

A more detailed model can be used to determine the required thermal resistance of a heat sink to provide necessary cooling. The total module resistance can be separated into a resistance from case-to-sink (θ_{cs}) and sink-to-ambient (θ_{sa}) shown below (Figure 33).



8-1304 (C)

Figure 33. Resistance from Case-to-Sink and Sink-to-Ambient

Thermal Considerations (continued)

Custom Heat Sinks (continued)

For a managed interface using thermal grease or foils, a value of $\theta_{cs} = 0.1 \text{ }^\circ\text{C/W}$ to $0.3 \text{ }^\circ\text{C/W}$ is typical. The solution for heat sink resistance is:

$$\theta_{sa} = \left[\frac{(T_C - T_A)}{P_D} \right] - \theta_{cs}$$

This equation assumes that all dissipated power must be shed by the heat sink. Depending on the user-defined application environment, a more accurate model, including heat transfer from the sides and bottom of the module, can be used. This equation provides a conservative estimate for such instances.

Solder, Cleaning, and Drying Considerations

Post solder cleaning is usually the final circuit-board assembly process prior to electrical testing. The result of inadequate circuit-board cleaning and drying can affect both the reliability of a power module and the testability of the finished circuit-board assembly. For guidance on appropriate soldering, cleaning, and drying procedures, refer to the *Board-Mounted Power Modules Soldering and Cleaning* Application Note (AP97-021EPS).

EMC Considerations

For assistance with designing for EMC compliance, please refer to the FLTR100V10 data sheet (DS98-152EPS).

Layout Considerations

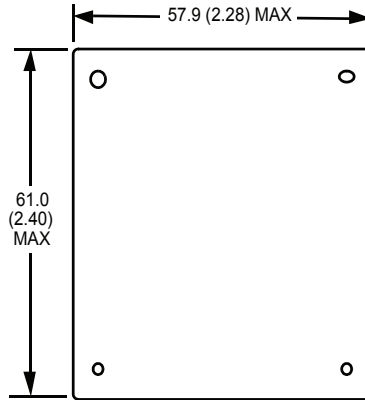
Copper paths must not be routed beneath the power module mounting inserts. For additional layout guidelines, refer to the FLTR100V10 data sheet (DS98-152EPS).

Outline Diagram

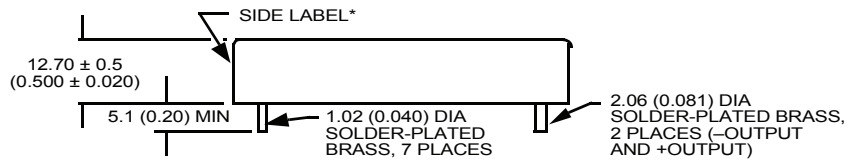
Dimensions are in millimeters and (inches).

Tolerances: x.x mm \pm 0.5 mm (x.xx in. \pm 0.02 in.)
x.xx mm \pm 0.25 mm (x.xxx in. \pm 0.010 in.)

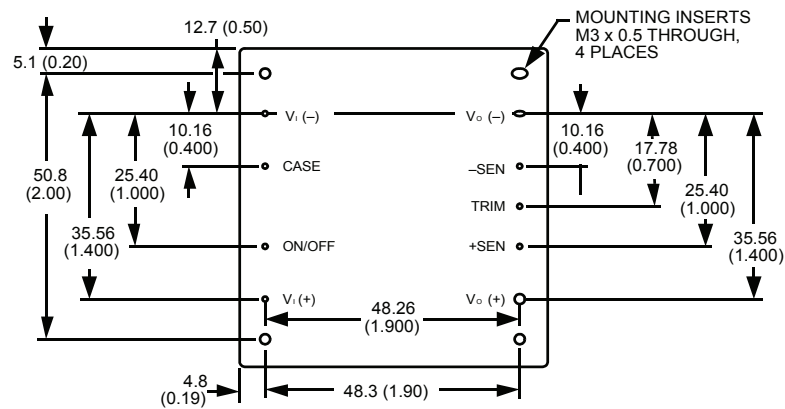
Top View



Side View



Bottom View



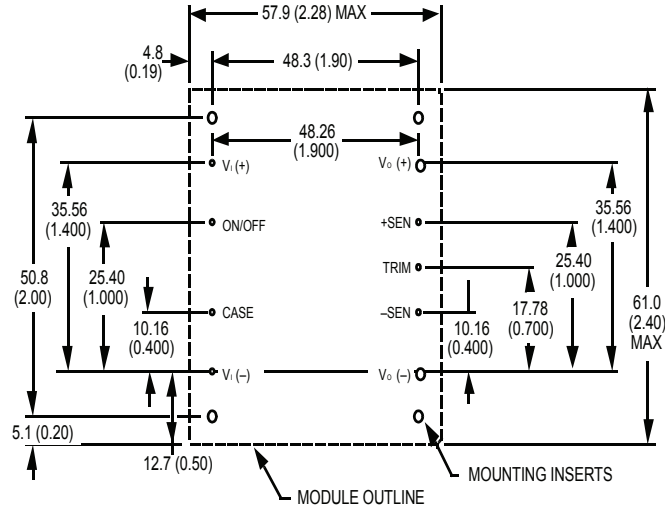
8-1945 (C).c

* Side label includes Lineage name, product designation, safety agency markings, input/output voltage and current ratings, and bar code.

Recommended Hole Pattern

Component-side footprint.

Dimensions are in millimeters and (inches).



8-1945 (C).c

Ordering Information

Table 4. Device Codes

Input Voltage	Output Voltage	Output Power	Remote On/Off Logic	Device Code	Comcode
48 V	3.3 V	33 W	Negative	JW050F1	107253171
48 V	3.3 V	49.5 W	Negative	JW075F1	107431256
48 V	3.3 V	66 W	Negative	JW100F1	107253189
48 V	3.3 V	99 W	Negative	JW150F1	107361461
48 V	3.3 V	33 W	Positive	JW050F	107309775
48 V	3.3 V	49.5 W	Positive	JW075F	107477374
48 V	3.3 V	66 W	Positive	JW100F	107309791
48 V	3.3 V	99 W	Positive	JW150F	107018962

Ordering Information (continued)

Table 5. Device Accessories

Accessory	Comcode
1/4 in. transverse kit (heat sink, thermal pad, and screws)	407243989
1/4 in. longitudinal kit (heat sink, thermal pad, and screws)	407243997
1/2 in. transverse kit (heat sink, thermal pad, and screws)	407244706
1/2 in. longitudinal kit (heat sink, thermal pad, and screws)	407244714
1 in. transverse kit (heat sink, thermal pad, and screws)	407244722
1 in. longitudinal kit (heat sink, thermal pad, and screws)	407244730
1 1/2 in. transverse kit (heat sink, thermal pad, and screws)	407244748
1 1/2 in. longitudinal kit (heat sink, thermal pad, and screws)	407244755

Dimensions are in millimeters and (inches).

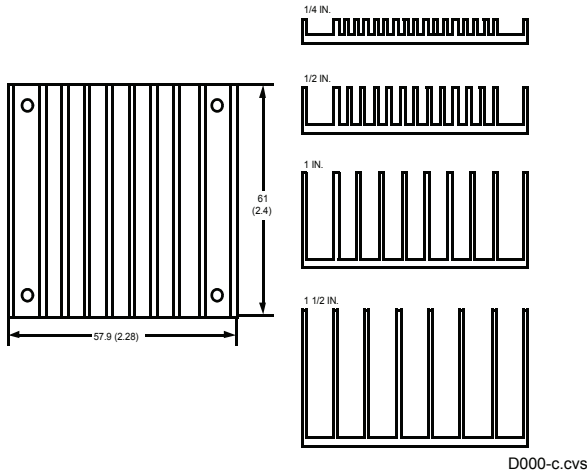


Figure 34. Longitudinal Heat Sink

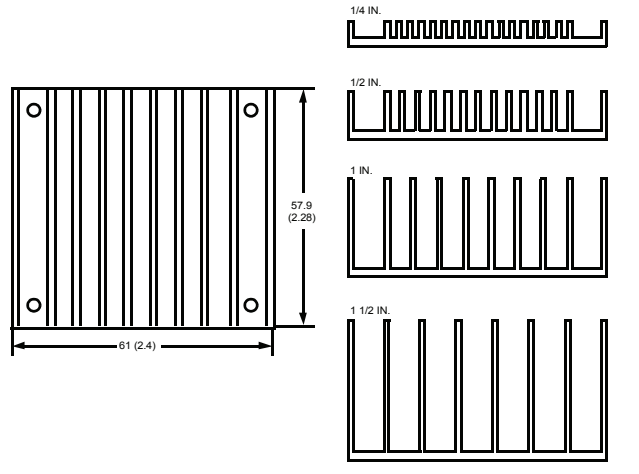


Figure 35. Transverse Heat Sink



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DS99-289EPS (Replaces DS99-288EPS)