

HEXFET® POWER MOSFET

IRFN150
JANTX2N7224U
JANTXV2N7224U
[REF:MIL-PRF-19500/592]
N - CHANNEL

100Volt, 0.070Ω MOSFET

HEXFET® power MOSFET technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry achieves very low on-state resistance combined with high transconductance.

HEXFET® power MOSFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and electrical parameter temperature stability. They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high-energy pulse circuits.

The Surface Mount Device (SMD-1) package represents another step in the continual evolution of surface mount technology. The SMD-1 will give designers the extra flexibility they need to increase circuit board density. International Rectifier has engineered the SMD-1 package to meet the specific needs of the power market by increasing the size of the bottom source pads, thereby enhancing the thermal and electrical performance.

Absolute Maximum Ratings

	Parameter		Units
I_D @ $V_{GS} = 10V, T_C = 25^\circ C$	Continuous Drain Current	34	A
I_D @ $V_{GS} = 10V, T_C = 100^\circ C$	Continuous Drain Current	21	
I_{DM}	Pulsed Drain Current ①	136	
P_D @ $T_C = 25^\circ C$	Max. Power Dissipation	150	W
	Linear Derating Factor	1.2	W/°C
V_{GS}	Gate-to-Source Voltage	±20	V
EAS	Single Pulse Avalanche Energy ②	150	mJ
IAR	Avalanche Current ①	34	A
EAR	Repetitive Avalanche Energy ①	15	mJ
dv/dt	Peak Diode Recovery dv/dt ③	5.5	V/ns
T_J	Operating Junction	-55 to 150	°C
T_{STG}	Storage Temperature Range		
	Package Mounting Surface Temperature	300 (for 5 seconds)	
	Weight	2.6 (typical)	g

Product Summary

Part Number	BV _{DSS}	R _{DS(on)}	I _D
IRFN150	100V	0.07Ω	34A

Features:

- Avalanche Energy Ratings
- Dynamic dv/dt Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Surface Mount
- Light Weight

Electrical Characteristics @ T_j = 25°C (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
BV _{DSS}	Drain-to-Source Breakdown Voltage	100	—	—	V	V _{GS} = 0V, I _D = 1.0mA
ΔBV _{DSS} /ΔT _J	Temperature Coefficient of Breakdown Voltage	—	0.13	—	V/°C	Reference to 25°C, I _D = 1.0mA
R _{DS(on)}	Static Drain-to-Source On-State Resistance	—	—	0.081	Ω	V _{GS} = 10V, I _D = 34A ④
		—	—	0.07		V _{GS} = 10V, I _D = 21A
V _{GS(th)}	Gate Threshold Voltage	2.0	—	4.0	V	V _{DS} = V _{GS} , I _D = 250μA
g _{fs}	Forward Transconductance	9.0	—	—	S (r)	V _{DS} > 15V, I _{DS} = 21A ④
I _{DSS}	Zero Gate Voltage Drain Current	—	—	25	μA	V _{DS} = 0.8 x Max Rating, V _{GS} = 0V
		—	—	250		V _{DS} = 0.8 x Max Rating V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	V _{GS} = 20V
I _{GSS}	Gate-to-Source Leakage Reverse	—	—	-100		V _{GS} = -20V
Q _g	Total Gate Charge	—	—	125	nC	V _{GS} = 10V, I _D = 34A
Q _{gs}	Gate-to-Source Charge	—	—	22		V _{DS} = Max Rating x 0.5
Q _{gd}	Gate-to-Drain ('Miller') Charge	—	—	65		
t _{d(on)}	Turn-On Delay Time	—	—	35	ns	V _{DD} = 50V, I _D = 34A, R _G = 2.35Ω, V _{GS} = 10V
t _r	Rise Time	—	—	190		
t _{d(off)}	Turn-Off Delay Time	—	—	170		
t _f	Fall Time	—	—	130		
L _S + L _D	Total Inductance	—	4.0	—	nH	Measured from the center of drain pad to center of source pad
C _{iss}	Input Capacitance	—	3700	—	pF	V _{GS} = 0V, V _{DS} = 25V f = 1.0MHz
C _{oss}	Output Capacitance	—	1100	—		
C _{rss}	Reverse Transfer Capacitance	—	200	—		

Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Typ	Max	Units	Test Conditions
I _S	Continuous Source Current (Body Diode)	—	—	34	A	
I _{SM}	Pulse Source Current (Body Diode) ①	—	—	136		
V _{SD}	Diode Forward Voltage	—	—	1.8	V	T _j = 25°C, I _S = 34A, V _{GS} = 0V ④
t _{rr}	Reverse Recovery Time	—	—	500	ns	T _j = 25°C, I _F = 34A, di/dt ≥ 100A/μs
Q _{RR}	Reverse Recovery Charge	—	—	2.9	μC	V _{DD} ≤ 30V ④
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L _S + L _D .				

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R _{thJC}	Junction-to-Case	—	—	0.83	°C/W	Soldered to a copper-clad PC board
R _{thJ-PCB}	Junction-to-PC board	—	3.0	—		

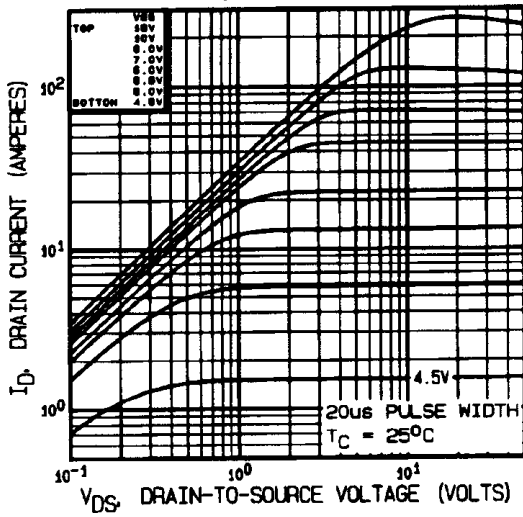


Fig 1. Typical Output Characteristics

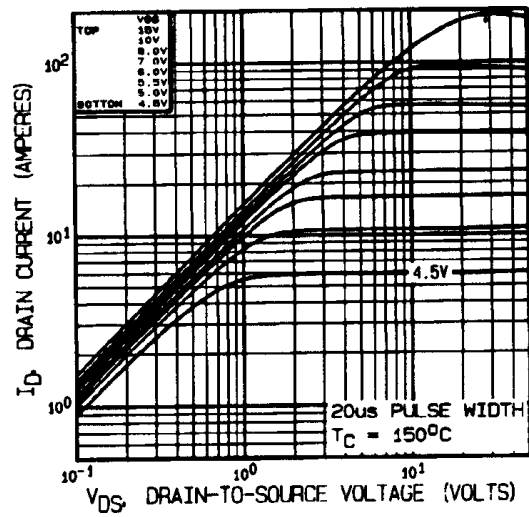


Fig 2. Typical Output Characteristics

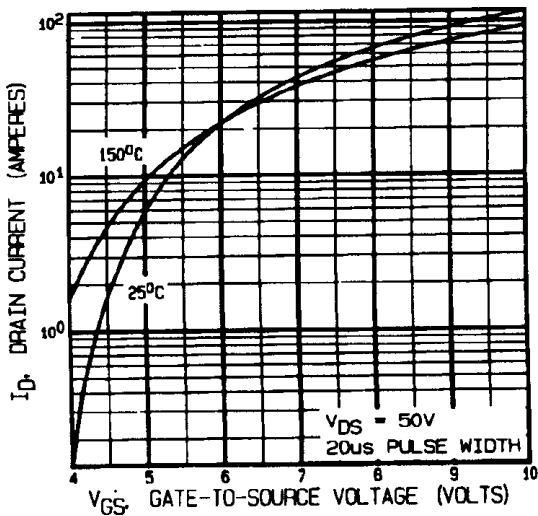


Fig 3. Typical Transfer Characteristics

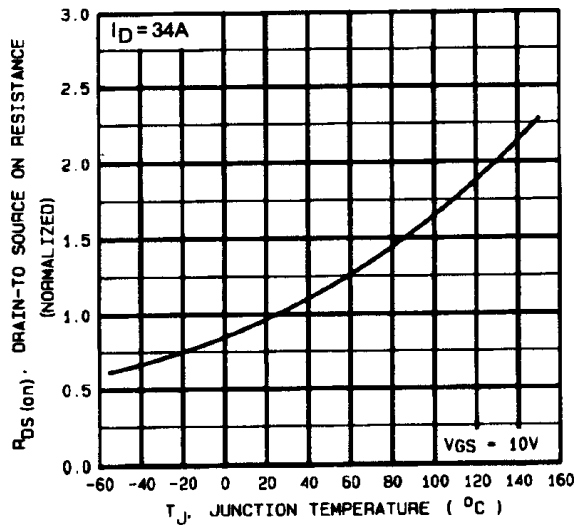


Fig 4. Normalized On-Resistance Vs. Temperature

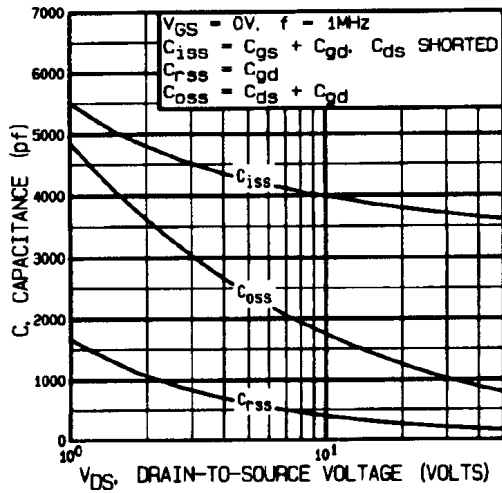


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

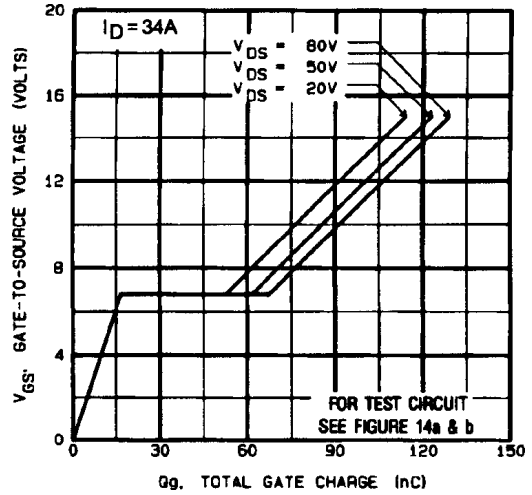


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

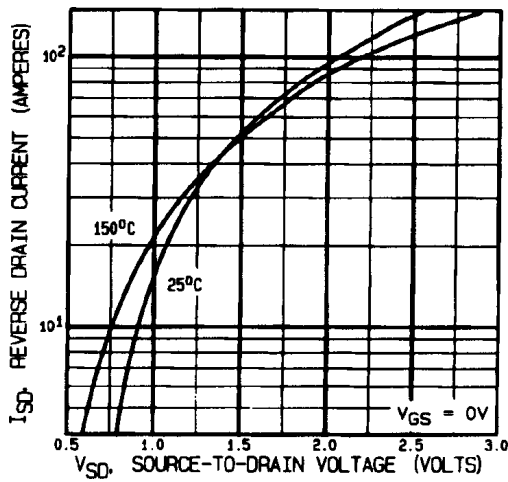


Fig 7. Typical Source-Drain Diode Forward Voltage

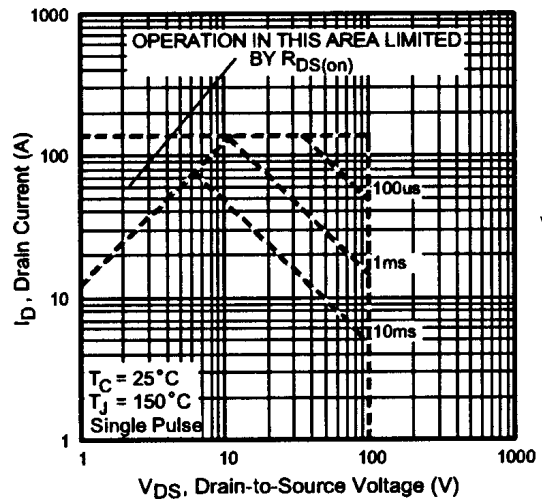


Fig 8. Maximum Safe Operating Area

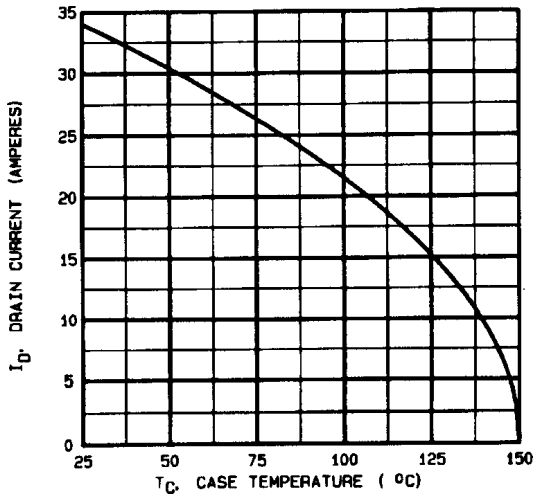


Fig 9. Maximum Drain Current Vs. Case Temperature

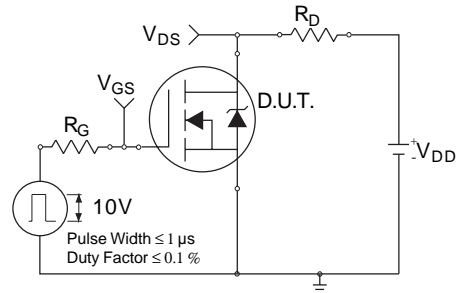


Fig 10a. Switching Time Test Circuit

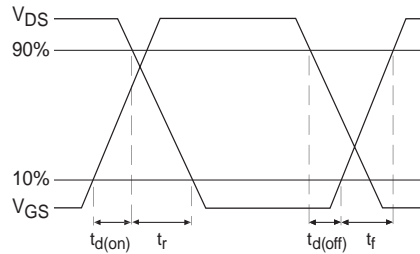


Fig 10b. Switching Time Waveforms

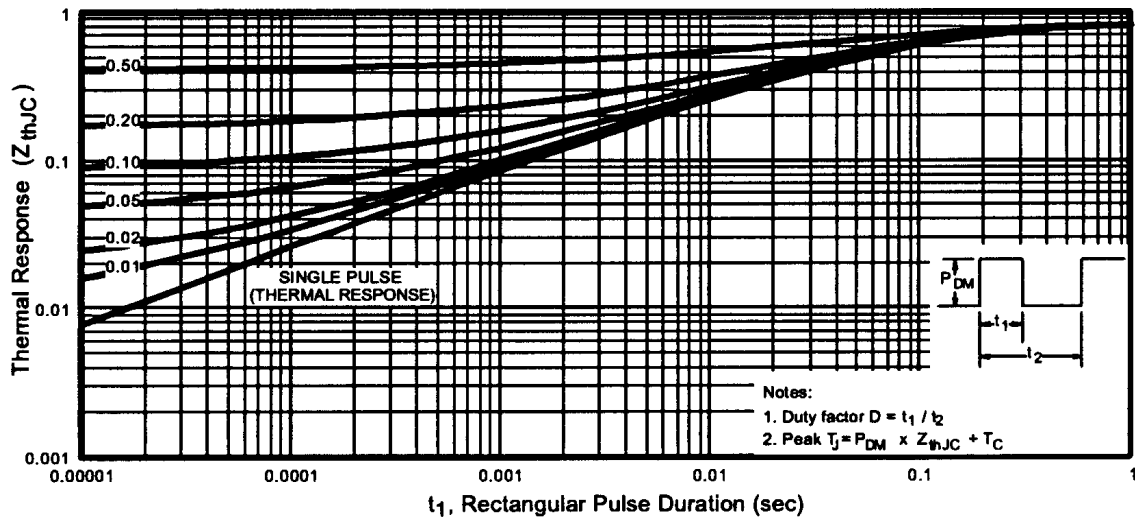


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

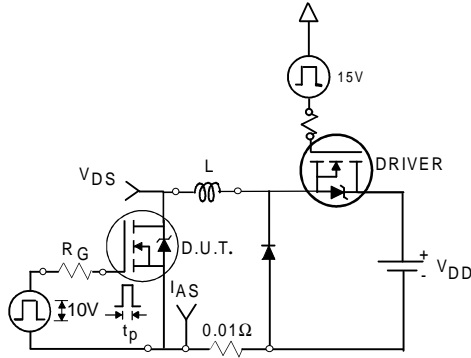


Fig 12a. Unclamped Inductive Test Circuit

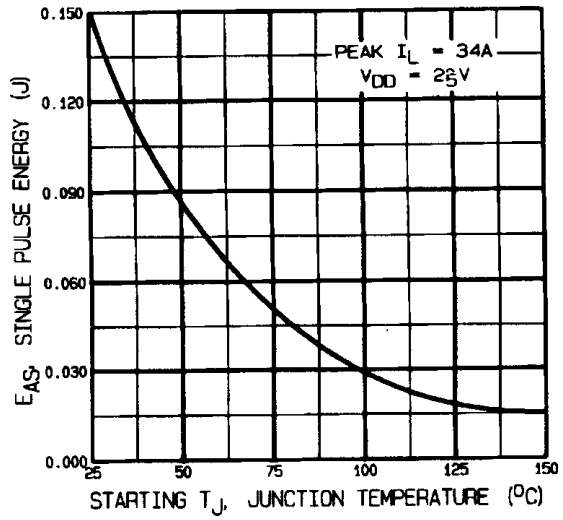


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

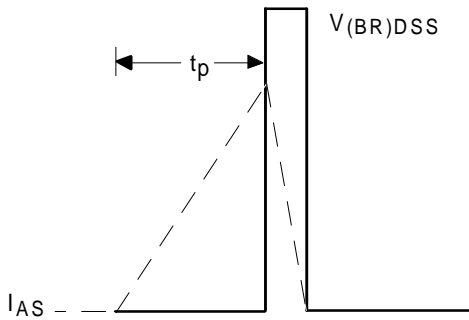


Fig 12b. Unclamped Inductive Waveforms

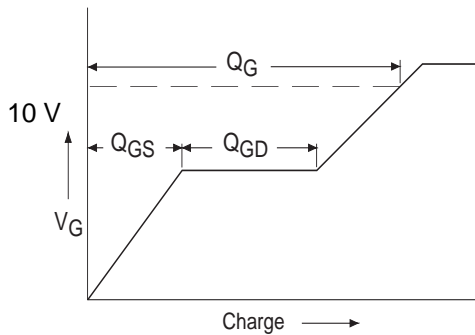


Fig 13a. Basic Gate Charge Waveform

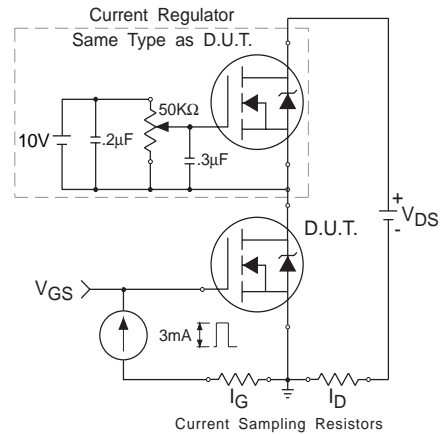
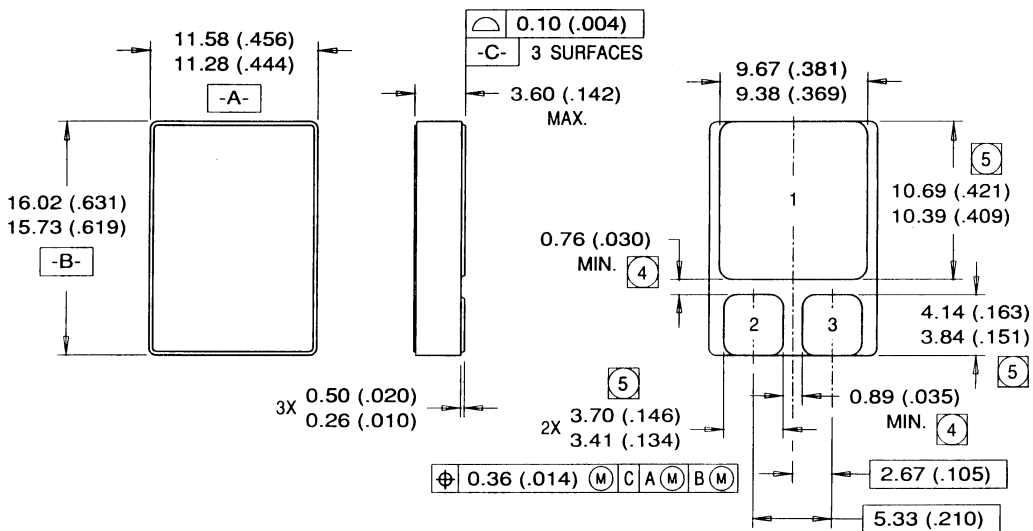


Fig 13b. Gate Charge Test Circuit

Notes:

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② $V_{DD} = 25\text{ V}$, Starting $T_J = 25^\circ\text{C}$,
Peak $I_L = 34\text{ A}$, $V_{GS} = 10\text{ V}$, $25 \leq R_G \leq 200\Omega$
- ③ $I_{SD} \leq 34\text{ A}$, $di/dt \leq 200\text{ A}/\mu\text{s}$,
 $V_{DD} \leq 100\text{ V}$, $T_J \leq 150^\circ\text{C}$
- ④ Pulse width $\leq 300\ \mu\text{s}$; Duty Cycle $\leq 2\%$

Case Outline and Dimensions — SMD-1



NOTES:

- 1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1982
- 2. CONTROLLING DIMENSION: INCH.
- 3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
- ④ DIMENSION INCLUDES METALLIZATION FLASH.
- ⑤ DIMENSION DOES NOT INCLUDE METALLIZATION FLASH.

PAD ASSIGNMENTS

- 1 = DRAIN
- 2 = GATE
- 3 = SOURCE