

Selection Guide

SAMPLE AND HOLD AMPLIFIERS: Typical Values at 25°C, Unless Otherwise Specified

TYPE	SAMPLE/HOLD TYPE	TEMP. RANGE (°C)	(NOTE 1) LEAD COUNT AND PACKAGE TYPE	ACQUISITION TIME (TO 0.01%)	HOLD STEP ERROR	APERTURE TIME	GAIN BANDWIDTH PRODUCT
HA1-2420-2	External Hold Cap, Low Cost	-55 to 125	14 Cerdip	3.2µs	10mV	30ns	2.5MHz (C _H = 1000pF)
HA1-2425-5		0 to 75	14 Cerdip				
HA3-2425-5		0 to 75	14 PDIP				
HA4P2425-5		0 to 75	20 PLCC				
HA9P2425-5		0 to 75	14 SOIC				
HA1-5320-2	High Speed, Low Charge, Transfer, Precision, Includes Hold Capacitor	-55 to 125	14 Cerdip	1µs (C _H = Internal)	1mV	25ns	2.0MHz (C _H = 100pF)
HA1-5320-5		0 to 75	14 Cerdip				
HA1-5320/883		-55 to 125	14 Cerdip				
HA3-5320-5		0 to 75	14 PDIP				
HA4-5320/883		-55 to 125	20 CLCC				
HA9P5320-5		0 to 75	16 SOIC (300 mil)				
HA9P5320-9		-40 to 85	16 SOIC (300 mil)				
HA1-5330-5	Very High Speed, Precision, Monolithic, Includes Hold Capacitor	0 to 75	14 Cerdip	650ns	0.5mV	20ns	4.5MHz
HA1-5330-4		-25 to 85	14 Cerdip				
HA1-5330-2		-55 to 125	14 Cerdip				
HA1-5330/883		-55 to 125	14 Cerdip				
HA3-5330-5		0 to 75	14 PDIP				
HA4-5330/883		-55 to 125	20 CLCC				
HA1-5340-5	High Speed, Low Distortion, Includes Hold Capacitor	0 to 75	14 Cerdip	700ns	15mV	15ns	10MHz
HA1-5340-9		-40 to 85	14 Cerdip				
HA1-5340/883		-55 to 125	14 Cerdip				
HA3-5340-5		0 to 75	14 PDIP				
HA3-5340-9		-40 to 85	14 PDIP				
HA4-5340/883		-55 to 125	20 CLCC				
HA9P5340-5		0 to 75	16 SOIC (300 mil)				
HA5351IP	Ultra High Speed and Low Power, Includes Hold Capacitor, Low Pin Count	-40 to 85	8 PDIP	64ns	10mV	10ns	40MHz
HA5351IB		-40 to 85	8 SOIC				

NOTE:

1. See Linear Package Selection Guide in Section 11.