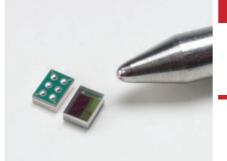


# **Color sensor**



S11059-01WT

# I<sup>2</sup>C interface-compatible color sensor

The S11059-01WT is a color sensor that supports the  $I^2C$  (inter-integrated circuit) interface. It is sensitive to red ( $\lambda$ =615 nm), green ( $\lambda$ =530 nm), blue ( $\lambda$ =460 nm), and infrared ( $\lambda$ =855 nm) light, and outputs detected results as 16-bit digital data for each color. The photodiode for each color is automatically switched sequentially to perform measurements. The sensitivity and integration time can be adjusted so that light measurements can be performed over a wide range.

#### Features

- **■** I<sup>2</sup>C interface compatible
- Sequential measurements of red, green, blue, and infrared light
- 2-step sensitivity switching (sensitivity ratio 1 : 10)
- Sensitivity adjustment by setting the integration time
- Low voltage (2.5 V or 3.3 V) operation
- Low current consumption: 75 μA typ.
- Small package (WL-CSP: wafer level-chip size package)
- Internal infrared-cut filter
- ightharpoonup Wide dynamic range (Low gain: 1 to 10 k/x)

### Applications

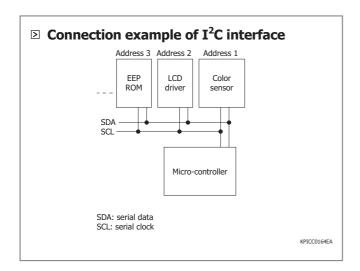
- LCD backlight adjustment for cell phones, notebook PC, etc.
- Energy-saving sensor for large-size TV, etc.
- Various types of light detection or color adjustment

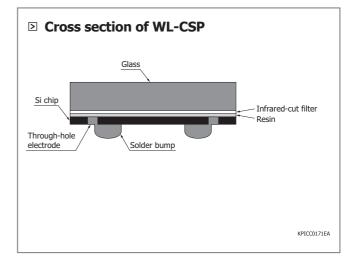
# I<sup>2</sup>C interface-compatible to allow direct connection to micro-controller

The sensor supports the I<sup>2</sup>C interface and so can exchange data with the micro-controller using two signal lines jointly usable with other devices. The digital output makes it easy to install into electronic devices such as cell phones and flat-panel TVs whose microcontroller is compatible with the I2C interface. The sensor supports  $I^2C$  Fast mode (400 kHz) and operates on 2.25 to 3.63 V.

# WL-CSP makes the device even smaller and highly reliable

The WL-CSP measures only 1.18  $\times$  1.68  $\times$  0.58 mm and lead-free reflow solder (260 °C) can be used.





#### **→** Absolute maximum ratings (Ta=25 °C)

Parameter	Symbol	Value	Unit
Supply voltage	Vdd	-0.3 to +6	V
Load current	Io	±10	mA
Power dissipation	Р	100	mW
Operating temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-40 to +100	°C
Reflow soldering conditions*1	Tsol	Peak temperature 260 °C, 3 times	-

<sup>\*1:</sup> Moisture absorption and reflow conditions: JEDEC J-STD-020D LEVEL2a

# Recommended operating conditions

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit			
Supply voltage	Vdd		2.25	-	3.63	V			
I <sup>2</sup> C bus pull-up voltage	Vbus	Rp*2=2.2 kΩ	1.65	-	Vdd + 0.5	V			
High level input voltage	Vih	Vbus≥2.25 V, Vdd>2.75 V	0.7Vbus	-	Vdd + 0.5	\/			
(SDA, SCL)	VIII	Vbus<2.25 V, Vdd≤2.75 V	0.8Vbus	-	Vdd + 0.5	V			
Low level input voltage	Vil	Vbus≥2.25 V, Vdd>2.75 V	-0.5	-	0.2Vbus	V			
(SDA, SCL)	VII	Vbus<2.25 V, Vdd≤2.75 V	V						
Bus capacitance (SDA, SCL)	Cbus		-	-	400	pF			

<sup>\*2:</sup> Pull-up resistor value is determined by Cbus and Vbus.

# **=** Electrical and optical characteristics

■ Sensor section [Ta=25 °C, Vdd=3.3 V, A light source, unless otherwise noted (initial setting: low gain, integration time: 546 ms/ch)] Symbol Condition Min. Max Unit Blue 400 to 540 Green 455 to 630 Spectral response range\*3 λ nm Red 575 to 660 Infrared, more than 700 nm 785 to 885 Blue 460 Green 530 Peak sensitivity wavelength nm λp Red 615 Infrared, more than 700 nm 855 Operating mode Idd 30 75 150 E=0 lx (dark state), Current consumption μΑ Standby mode Idds excluding output current 0.1 1.0 3.0 Dark count Sd E=0 lx (dark state) 5 counts Gain ratio rg High gain/Low gain 10 2.01 3.35 4.69 Sbl Blue Sgl Green 4.57 10.66 7.61 Initial setting Srl Red 5.69 9.48 13.28 Sirl Infrared 1.66 Photo sensitivity Low gain counts/lx 2.51 Sbl 4.19 Blue 3.35 Sgl 5.71 7.61 9.52 Green Initial setting\*4 Srl Red 7.11 9.48 11.85 Sirl Infrared 1.66 3.54 Red/Blue sensi. ratio Srl/Sbl 2.12 2.83 Initial setting Red/Green sensi. ratio Low gain Srl/Sgl 0.93 1.56 1.25 Same chip Blue/Green sensi. ratio 0.33 0.44 0.55 Sbl/Sgl Sbh Blue 19.0 31.7 44.4 45.7 106.7 Sgh Green 76.2 Integration time Srh Red 546 ms/ch 56.7 94.5 132.4 Sirh Infrared 15.3 Photo sensitivity High gain counts/lx Sbh Blue 23.8 31.7 39.7 Sgh Green Integration time 57.2 76.2 95.3 Red 546 ms/ch\*4 70.9 94.5 118.2 Srh Sirh Infrared 15.3 2.24 Red/Blue sensi. ratio Srh/Sbh 2.98 3.73 Integration time 546 ms/ch Srh/Sgh 0.93 1.55 Red/Green sensi. ratio High gain 1.24 Same chip 0.31 Blue/Green sensi. ratio Sbh/Sgh 0.42 0.52 Integration time setting (Tint) 105 Integration time 175 245 μs

<sup>\*4:</sup> Integration time is measured and corrected. See "Compensation method for sensitivity variation". Integration time measurement accuracy is 0.36%.



<sup>\*3:</sup> Relative sensitivity=more than 10%

■ I<sup>2</sup>C section (Ta=25 °C, Vdd=3.3 V, unless otherwise noted)

Parameter	·	Symbol	Condition	Min.	Тур.	Unit			
I <sup>2</sup> C address		ADDR	DDR 7 bits 0x2A (0101010)						
I <sup>2</sup> C clock frequency		fclk	1 - 400						
SDA, SCL output	High level	Voh	Rp=2.2 kΩ	0.7Vbus	-	-	V		
voltage	Low level	Vol	Rp=2.2 kΩ	0	-	0.4	V		
Input/output terminal of	capacitance	Ci		-	-	20 pF			
SDA/SCL output fall ti	me*5	tf	Rp=2.2 kΩ, Cp=400 pF	-	-	250	ns		

<sup>\*5:</sup> SCL/SDA output rise time is determined by a time constant of Cbus × Rp.

Note: The I<sup>2</sup>C interface (SDA, SCL) timings conform to the "I<sup>2</sup>C bus specification version 2.1".

#### Register map

Adrs	Function					bit							
Aurs	Function	7 6		5	4	3	2	1	0				
00	Control	ADC reset 1: Reset 0: Operation	Standby function 1: Standby mode 0: Operating mode	Standby function monitor	1	Gain selection 1: High gain 0: Low gain	Integration mode 1: Manual setting mode 0: Fixed period mode	(00) 87.5 µs,	time setting , (01) 1.4 ms (11) 179.2 ms				
01	  Manual timing register		Integration time manual setting register (MSB)										
02	manual uning register	Integration time manual setting register (LSB)											
03	Sensor data register				Οu	tput data (re	d, MSB)						
04	(red)	Output data (red, LSB)											
05	Sensor data register				Out	put data (gre	en, MSB)						
06	(green)		Output data (green, LSB)										
07	Sensor data register				Ou	tput data (blu	ie, MSB)						
08	(blue)				Ou	tput data (bl	ue, LSB)						
09	Sensor data register				Outp	ut data (infra	red, MSB)						
0A	(infrared)				Outp	out data (infra	ared, LSB)	-					

- Adrs 00 bit 7: Asserting this bit to "1", the ADC block is reset. The register data is not reset. To start the operation, set this bit to "0". Adrs 00 bit 6: Asserting this bit to "1" the device goes into standby mode. The ADC block stops its operation. The register data is not reset. To start the operation, set this bit to "0".
- Adrs 00 bit 5: This monitors auto standby function. "1" means standby mode. This is read only.
- Adrs 00 bit 3: Gain selection bit. "1" is high gain mode and "0" is low gain mode. This bit is selecting the photodiode area. The size ratio of high gain photodiode area and low gain photodiode area is 10:1. Therefore the gain ratio is 10 times from low to
- Adrs 00 bit 2: Asserting this bit to "1", the device goes into manual setting mode. Deasserting this bit to 0, goes into fixed period mode. In manual setting mode, the S11059-01WT automatically goes to standby mode after a measurement is made. In fixed period mode, measurements are continuously repeated.
- Adrs 00 bit 1,0: These bits select the period of internal basis clock. The period is equal to integration time per color in fixed period mode. "00" is 87.5 us, "01" is 1.4 ms, "10" is 22.4 ms, "11" is 179.2 ms. In manual setting mode, "00" is 175 µs, "01" is 2.8 ms, "10" is 44.8 ms, "11" is 368 ms. The integration time per color is set to multiple value (Adrs 01 & 02) with the period.
- Adrs 01 & 02: This is a multiple value setting in manual setting mode, and can be set to a minimum of 0x0000 and a maximum of 0xFFFF (65535). This is used to set how far to expand the integration time per color which specified by "Integration time setting" (Tint). For example, if you want to set the integration time per color to 546 ms, set 175 µs by Tint="00" and then set this register to N=3120 (0xC30).

Mode	Manual timing register	Integration time setting (Tint)									
Mode	(Adrs 01 & 02)	00	01	10	11						
Fixed period mode	Disabled	87.5 μs	1.4 ms	22.4 ms	179.2 ms						
Manual setting mode	N	175 × N μs	2.8 × N ms	44.8 × N ms	358.4 × N ms						

Adrs 03 to 0A: These bytes are register for sensor data. S11059-01WT measurement result is stored in these registers when the  $I^2C$ command is changed to read mode. The values are kept until next read cycle.

#### 🖶 Initial setting [Low gain, manual setting mode, Tint=00 (175 μs), integartion time 546 ms/ch]

Adrs	Function		bit											
Aurs	Function	7	6	5	4	3	2	1	0	Hex				
00	Control	1	1	1	-	0	1	0	0	0xE4				
01	Manual timing register	0	0	0	0	1	1	0	0	0x0C				
02	Manual timing register	0	0	1	1	0	0	0	0	0x30				



### Program example

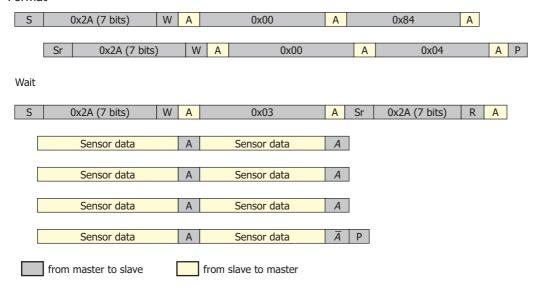
Condition 1: Initial setting [manual setting mode, low gain, Tint=00 (175 µs), integration time 546 ms/ch (0x0C30 is set in manual timing register)]

#### Command

Action					Data	body				Ack	Remark		
Address call (0x2A)	S	0	1	0	1	0	1	0	W	Α	7-bit address		
Register call (0x00)		0	0	0	0	0	0	0	0	Α	Calls control byte		
Register write (0x84)		1	0	0	0	0	1	0	0	Α	ADC reset, standby disabled		
Address call (0x2A)	Sr	0	1	0	1	0	1	0	W	Α	Restart, address		
Register call (0x00)		0	0	0	0	0	0	0	0	Α	Calls control byte		
Register write (0x04)		0	0	0	0	0	1	0	0	Α	P ADC reset disabled, bus release		
				V	Vait lo	nger	than	integr	ation	time (	>2184 ms)		
Address call (0x2A)	S	0	1	0	1	0	1	0	W	Α	7-bit address		
Register call (0x03)		0	0	0	0	0	0	1	1	Α	Calls output data byte		
Address call (0x2A)	Sr	0	1	0	1	0	1	0	R	Α	Changes to read mode		
Data read out (R: MSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Red data output		
Data read out (R: LSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Red data output		
Data read out (G: MSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Green data output		
Data read out (G: LSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Green data output		
Data read out (B: MSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Pluo data output		
Data read out (B: LSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Blue data output		
Data read out (Infrared: M:	SB)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Infrared data output		
Data read out (Infrared: LS	SB)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Ā	P		

S=Start condition, Sr=Restart condition, A=Acknowledge, A=Acknowledge by host, P=Stop condition, R=Read mode (1), W=Write mode (0),  $\overline{A}$ =not acknowledge

#### ■ Format





#### S11059-01WT

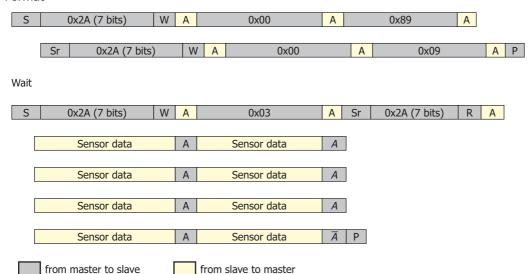
Condition 2 [fixed period mode, high gain, Tint=01 (1.4 ms), integration time 1.4 ms/ch]

#### Command

Action					Data	body				Ack	Remark
Address call (0x2A)	S	0	1	0	1	0	1	0	W	Α	7-bit address
Register call (0x00)		0	0	0	0	0	0	0	0	Α	Calls control byte
Register write (0x89)		1	0	0	0	1	0	0	1	Α	ADC reset, standby disabled
Address call (0x2A)	Sr	0	1	0	1	0	1	0	W	Α	7-bit address
Register call (0x00)		0	0	0	0	0	0	0	0	Α	Calls control byte
Resistor write (0x09)		0	0	0	0	1	0	0	1	Α	P ADC reset disabled, bus release
Wait longer than integration time (> 5.6 ms). Within this period, repeat mea								riod, repeat measurement is continued.			
Address call (0x2A)	S	0	1	0	1	0	1	0	W	Α	7-bit address
Register call (0x03)		0	0	0	0	0	0	1	1	Α	Calls output data byte
Address call (0x2A)	Sr	0	1	0	1	0	1	0	R	Α	Changes to read mode
Data read out (R: MSB	3)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Red data output
Data read out (R: LSB)	)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Red data output
Data read out (G: MSE	3)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Green data output
Data read out (G: LSB)	)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Green data output
Data read out (B: MSB	3)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Blue data output
Data read out (B: LSB)	)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Tolue data output
Data read out (Infrared: I	MSB)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Infrared data output
Data read out (Infrared: I	LSB)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Ā	P

S=Start condition, Sr=Restart condition, A=Acknowledge, A=Acknowledge by host, P=Stop condition, R=Read mode(1), W=Write mode(0),  $\overline{A}$ =not acknowledge

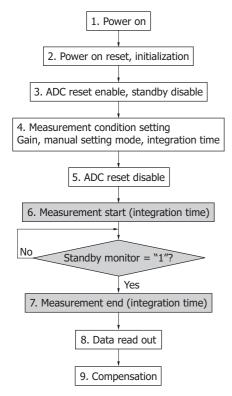
#### ■ Format





#### S11059-01WT

# Compensation method for sensitivity variation



Sensitivity variation can be decreased using the compensation coefficient which is calculated from the integration time measurement result. Explanation of compensation method is shown as follows.

#### Integration time measurement method

In case of integration time measurement, it is necessary to set manual setting mode. The integration time measurement starts after "ADC reset" disabled. To measure the finishing integration time (measurement) Tmeas, check "Standby monitor" bit until it becomes to "1".

### Compensation method

The sensitivity compensation that used integration time is as follows:

$$K = \frac{Tset}{Tmeas}$$
$$S' = S \cdot K$$

K : compensation coefficient
Tset : integration time (setting)
Tmeas: integration time (measurement)
S : photo sensitivity (measurement)
S' : photo sensitivity (compensation)

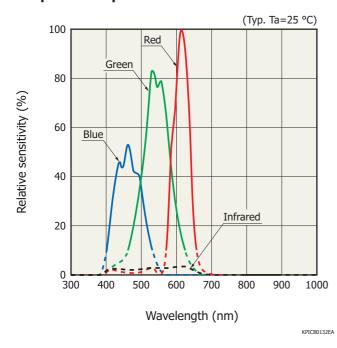
### Measurement accuracy of integration time

The measurement minimum resolution of Tmeas is defined by the looping duration (Tunit). In case of default setting, the Tset is 2184 ms and assuming the Tunit to 7.8 ms, the accuracy of integration time is calulated by following formula.

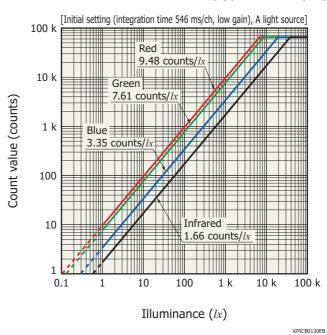
$$\frac{\text{Tunit}}{\text{Tset}} \times 100 = \frac{7.8}{2184} \times 100 = 0.36\%$$

The specification of compensated sensitivity is defined as 0.36% accuracy.

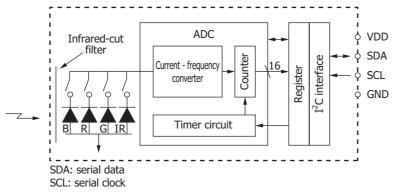
#### Spectral response



### - Count value vs. illuminance (typical example)

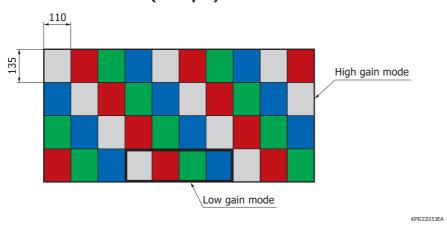


# **Block diagram**

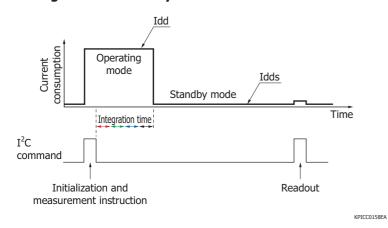


KPICC0152EA

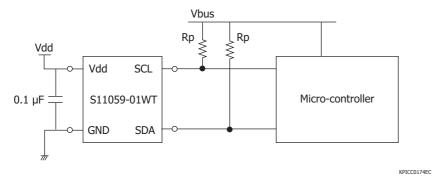
# - Details of active area (unit: μm)



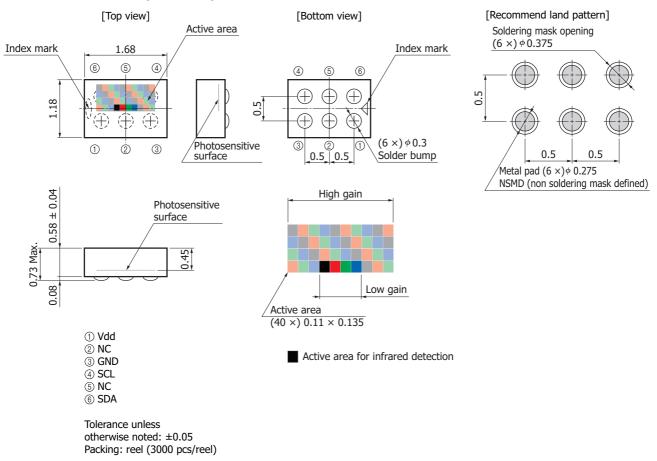
- Timing chart of standby function



### - Connection example



# Dimensional outline (unit: mm)



KPICA0081EB

Note: When using this product, please request our technical information (S11059 series) and ensure appropriate design according to the information.

#### Line-up of RGB color sensors

Type No.	Туре	Active area size	Package	ser wav	Peak elength			Photo				
		(mm)	(mm) (nm)									
			$4 \times 4.8 \times 1.8^{t}$	В	460	В		0.18 (A/W	) [λ	=46	50 nm]	- Total
S9032-02	Photodiode	ф2.0	6-pin	G	540	G	0.23 (A/W		) [λ	=54	10 nm]	
			(filter 0.75 <sup>t</sup> )	R	620	R		0.16 (A/W	) [λ=620 nm]			
			$3 \times 4 \times 1.3^{t}$	В	460	В		0.18 (A/W	<u>) [</u> λ	=46	50 nm]	odina.
S9702	Photodiode	1.0 × 1.0	4-pin	G	540	G		0.23 (A/W	<u>) [</u> λ	=54	10 nm]	( Internal
			(filter 0.75t)	R	620	R		0.16 (A/W	<u>) [</u> λ	=62	20 nm]	
			$3 \times 1.6 \times 1.0^{t}$	В	460	В		0.2 (A/W)	[λ:	=46	0 nm]	-
S10917-35GT	Photodiode	1.0 × 1.0	COB	G	540	G		0.23 (A/W	<u>) [</u> λ	=54	10 nm]	
			(on-chip filter)	R	620	R		0.17 (A/W	<u>) [</u> λ			
			$3 \times 1.6 \times 1.0^{t}$			В	0.21 (A/W) [λ=460 nm]					_
S10942-01CT	Photodiode	1.0 × 1.0	COB		*6	G	0.25 (A/W) [λ=540 nm]			10 nm]		
			(on-chip filter)			R		0.45 (A/W) [λ=640 nm]				The state of the s
	D:-:t-1		4 × 4.8 × 1.8 <sup>t</sup>	В	465		В	0.21 (LSB/lx)	_	В	1.9 (LSB/lx)	2000
S9706	Digital Photo IC	1.2 × 1.2	6-pin	G	540	NO-	G	0.45 (LSB/lx)	High	G	4.1 (LSB/lx)	
	PHOTO IC		(filter 0.75t)	R	615	_	R	0.64 (LSB/lx)	_	R	5.8 (LSB/lx)	
	730		1 60 1 10 0 50	В	460		В	3.35 (counts/lx)		В	31.7 (counts/lx)	
C110F0 01WT	I <sup>2</sup> C interface-		$1.68 \times 1.18 \times 0.58^{t}$	G	530	Low	G	7.61 (counts/lx)	<u>ا</u>	G	76.2 (counts/lx)	
S11059-01WT	compatible	1.22 × 0.56	VVL CSI	R	615		R	9.48 (counts/lx)	Ξ΄	R	94.5 (counts/lx)	
	color sensor		(on-chip filter)	IR	855			1.66 (counts/lx)		IR	15.3 (counts/lx)	

<sup>\*6:</sup> Refer to "Spectral response" of "Si photodiode S10942-01CT" datasheet.

Information described in this material is current as of July, 2011. Product specifications are subject to change without prior notice due to improvements or other reasons. Before assembly into final products, please contact us for the delivery specification sheet to check the latest information.

Type numbers of products listed in the delivery specification sheets or supplied as samples may have a suffix "(X)" which means preliminary specifications or a suffix "(Z)" which means developmental specifications.

The product warranty is valid for one year after delivery and is limited to product repair or replacement for defects discovered and reported to us within that one year period. However, even if within the warranty period we accept absolutely no liability for any loss caused by natural disasters or improper product use.

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