NOT RECOMMENDED FOR NEW DESIGN USE 1N4148W / 1N4448W

LL4148 / LL4448

FAST SWITCHING SURFACE MOUNT DIODE

Features and Benefits

- Fast Switching Speed
- Surface Mount Package Ideally Suited for Automatic Insertion
- General Purpose Rectification
- Silicon Epitaxial Planar Construction
- Lead Free Finish, RoHS Compliant (Note 1)

Mechanical Data

- Case: MiniMELF
- Case Material: Glass: UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Sn97.5Ag2.5. Solderable per MIL-STD-202, Method 208
- · Polarity: Cathode Band
- Marking: Cathode Band Only
- Weight: 0.05 grams (approximate)

Ordering Information (Note 2)

Part Number	Case		Packaging
LL4148-13	MiniMELF		10K/Tape & Reel, 13-inch
LL4448-7	MiniMELF	4 17 3	2.5K/Tape & Reel, 7-inch

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2). All applicable RoHS exemptions applied.
- 2. For Packaging Details, go to our website at http://www.diodes.com.

Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	LL4148	LL4448	Unit
Non-Repetitive Peak Reverse Voltage	V_{RM}	100		V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	75		V
RMS Reverse Voltage	V _{R(RMS)}	53		V
Forward Continuous Current (Note 3)	I _{FM}	300	500	mA
Average Rectified Output Current (Note 3)	lo	150		mA
Non-Repetitive Peak Forward Surge Current @ t = 1.0s @ t = 1.0µs	IÈSM	1.0		А
Power Dissipation (Note 3) Derate above 25°C	P _D	500 1.68		mW mW/°C
Thermal Resistance, Junction to Ambient Air (Note 3)	$R_{ heta JA}$	300		K/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 TO +1	75	°C

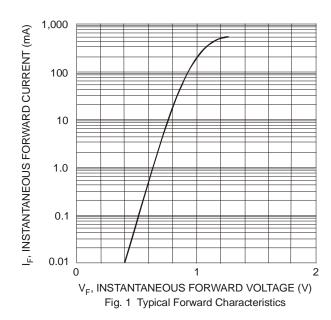
Electrical Characteristics @TA = 25°C unless otherwise specified

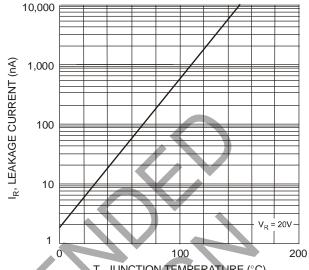
	Characteristic		Symbol	Min	Max	Unit	Test Condition
		LL4148		-	1.0		$I_F = 10mA$
Forward Voltage		LL4448	V_{F}	0.62	0.72	V	$I_F = 5.0 \text{mA}$
	LL4448	1448	-	1.0		$I_F = 100 \text{mA}$	
Maximum Peak Reverse Current (Note 4)		I _{RM}	-	5.0	μΑ	$V_R = 75V$	
			-	50	μΑ	$V_R = 75V, T_J = 150$ °C	
			-	30	μΑ	$V_R = 75V, T_J = 150$ °C	
			-	25	nA	$V_R = 75V$	
Capacitance			CJ	-	4.0	pF	$V_R = 0$, $f = 1.0MHz$
Reverse Recovery Time		t _{rr}	-	4.0	ns	$I_F = 10 \text{mA}$, to $I_R = 1.0 \text{mA}$,	
						$V_R = 6.0V, R_L = 100\Omega$	

Notes:

- 3. Valid provided that device terminals are kept at ambient Temperature.
- 4. Short duration pulse test used to minimize self-heating effect.

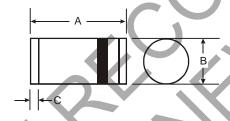






T_j, JUNCTION TEMPERATURE (°C)
Fig. 2 Typical Leakage Current vs. Junction Temperature

Package Outline Dimensions



MiniMELF			
Dim	Min	Max	
Α	3.30	3.70	
В	1.30	1.60	
С	0.28	0.50	
All Dimensions in mm			

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LL4148 / LL4448

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