

2.0A HIGH VOLTAGE SCHOTTKY BARRIER RECTIFIER POWERDI®

Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: POWERDI123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (B)
- Weight: 0.01 grams (approximate)

POWERDI123



Top View

Ordering Information (Note 4)

Part Number	Case	Packaging		
DFLS2100-7	POWERDI123	3000/Tape & Reel		
DFLS2100Q-7	POWERDI123	3000/Tape & Reel		

Notes:

- $1. \ EU \ Directive \ 2002/95/EC \ (RoHS) \ \& \ 2011/65/EU \ (RoHS \ 2) \ compliant. \ All \ applicable \ RoHS \ exemptions \ applied.$
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



F09A = Product Type Marking Code YM = Date Code Marking Y = Year (ex: V = 2008)

M = Month (ex: 9 = September)

Date Code Key

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Year	2008	2009	2010	2011	201	2 20	013	2014	- 2	2015	2016	2017	2018
Code	V	W	X	Υ	Z		Α	В		С	D	Е	F
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Αι	ıg	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	3	9	0	N	D



Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage	V _{RRM} V _{RWM}	100	V
DC Blocking Voltage	V RWM V _R	100	V
RMS Reverse Voltage	V _{R(RMS)}	71	V
Average Forward Current	I _{F(AV)}	2.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	40	А

Thermal Characteristics

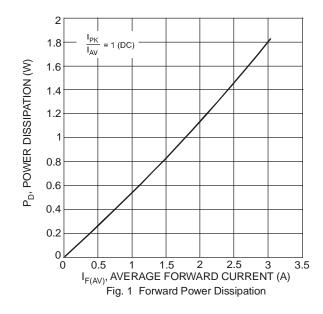
Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering (Note 5)	$R_{ heta JS}$	_	7	°C/W
Thermal Resistance Junction to Ambient (Note 6) (T _A = +25°C)	$R_{\theta JA}$	125	_	°C/W
Operating and Storage Temperature Range	T_J , T_{STG}	-55 to	+175	°C

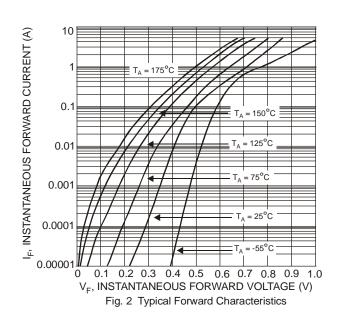
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	$V_{(BR)R}$	100	_	_	V	$I_R = 1\mu A$
Forward Voltage	V _F	_	_	0.77 0.86	· · · /	I _F = 1.0A I _F = 2.0A
Leakage Current (Note 7)	I _R	_	_	1	μА	$V_R = 100V, T_A = +25^{\circ}C$
Total Capacitance	Ст	_	36	_	pF	$V_R = 5VDC$, $f = 1MHz$

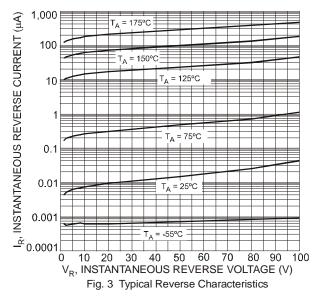
Notes:

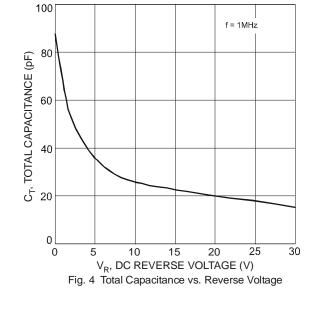
- 5. Theoretical $R_{\theta JS}$ calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
- 6. Part mounted on FR-4 board with 2 oz., minimum recommended copper pad layout, which can be found on our website at http://www.diodes.com.
- 7. Short duration pulse test used to minimize self-heating effect.

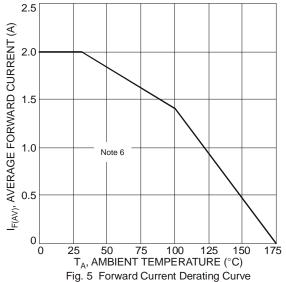






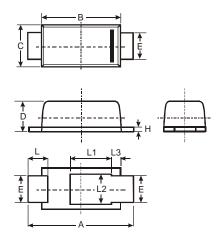






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



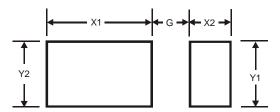
POWERDI123						
Dim	Min	Max	Тур			
Α	3.50	3.90	3.70			
В	2.60	3.00	2.80			
O	1.63	1.93	1.78			
ם	0.93	1.00	0.98			
Е	0.85	1.25	1.00			
Н	0.15	0.25	0.20			
L	0.40	0.50	0.45			
L1	-	-	1.35			
L2	-	-	1.10			
L3 0.20						
All Dimensions in mm						

DOWEDDIAGO



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4

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