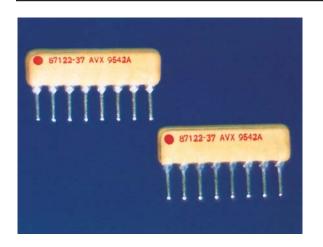
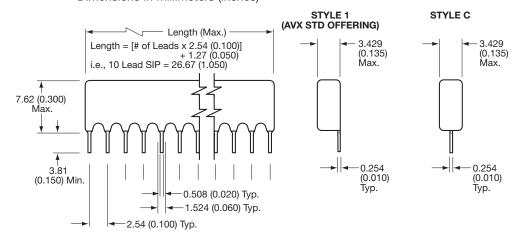
Single-In-Line Packages (SIP)



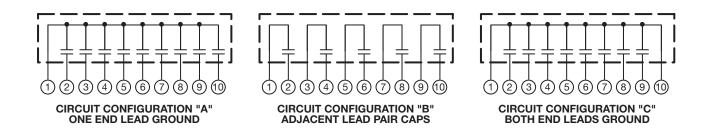
Capacitor Arrays



SIP-style, MLC ceramic capacitor arrays are Single-In-Line, conformally coated packages. These capacitor networks incorporate multiple capacitors into a single substrate and, therefore, offer excellent TC tracking. The utilization of SIP capacitor arrays minimizes board real estate and reduces component count in the assembly. Various circuit configurations and capacitance/voltage values are available.



Dimensions in millimeters (inches)

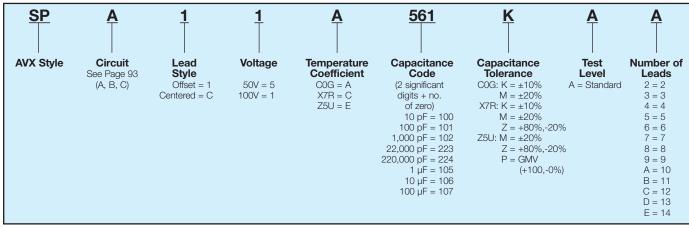


Single-In-Line Packages (SIP)



Capacitor Arrays

HOW TO ORDER



*For dimensions, voltages, or capacitance values not specified, please contact factory.

Maximum Capacitance*				
	50V	100V		
COG	2200 pF	1500 pF		
X7R	0.10 µF	0.033 µF		
Z5U	0.39 µF	0.10 µF		

AVX IS QUALIFIED TO THE FOLLOWING DSCC DRAWINGS

SPECIFICATION #	DESCRIPTION	CIRCUIT	LEADS	CAPACITANCE RANGE
87112	BX-100 VDC	А	8	1000 pF - 0.1 μF
87116	COG-100 VDC	A	8	10 pF - 820 pF
87119	BX-100 VDC	С	10	1000 pF - 0.1 μF
87120	COG-100 VDC	С	10	10 pF - 1000 pF
87122	BX-100 VDC	В	8	1000 pF - 0.1 μF
88019	BX-100 VDC	А	10	1000 pF - 0.1 μF
89086	COG-100 VDC	В	8	10 pF - 820 pF