

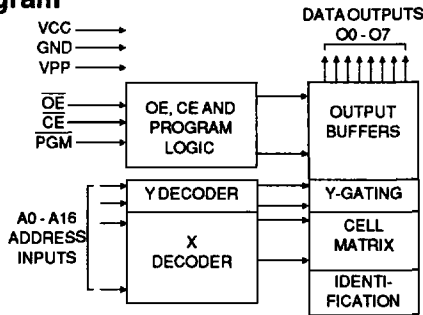
Features

- **Low Power CMOS Operation**
 100 μ A max. Standby
 25 mA max. Active at 5 MHz (AT27C010L)
 40 mA max. Active at 5 MHz (AT27C010)
- **Fast Read Access Time - 120ns**
- **Wide Selection of JEDEC Standard Packages Including OTP**
 32-Lead 600 mil CerDip and OTP Plastic DIP
 32-Pad LCC
 32-Lead JLCC and OTP PLCC
- **5V \pm 10% Supply**
- **High Reliability CMOS Technology**
 2000V ESD Protection
 200mA Latchup Immunity
- **Rapid Programming - 100 μ s/byte (typical)**
- **Two-Line Control**
- **CMOS and TTL Compatible Inputs and Outputs**
- **Integrated Product Identification Code**
- **Full Military, Commercial and Industrial Temperature Ranges**

**1 Megabit
(128K x 8)
UV
Erasable
CMOS
EPROM**

4

Block Diagram



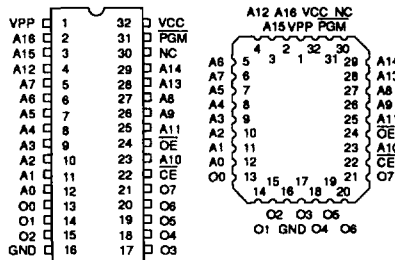
Description

The AT27C010/L chip family is a low-power, high performance 1,048,576 bit Ultraviolet Erasable and Electrically Programmable Read Only Memory (EPROM) organized as 128K x 8 bits. They require only one 5V power supply in normal read mode operation. Any byte can be accessed in less than 120ns, eliminating the need for speed reducing WAIT states on high performance microprocessor systems.

Two power versions are offered. In read mode, the AT27C010 typically consumes 25mA while the AT27C010L takes only 8mA. Standby mode supply current for both parts is typically less than 20 μ A.

Pin Configurations

Pin Name	Function
A0-A16	Addresses
O0-O7	Outputs
CE	Chip Enable
OE	Output Enable
PGM	Program Strobe
NC	No Connect





Description (Continued)

The AT27C010/L come in a choice of industry standard JEDEC-approved packages including: 32-pin DIP in ceramic or one time programmable (OTP) plastic, 32-pad ceramic leadless chip carrier (LCC), and 32-lead ceramic (JLCC) or OTP plastic (PLCC) J-leaded chip carrier. All devices feature two line control (CE, OE) to give designers the flexibility to prevent bus contention.

With high density 128K byte storage capability, the AT27C010/L allow firmware to be stored reliably and to be accessed by the system without the delays of mass storage media. Atmel's 27C010/L have additional features to ensure high quality and efficient production use. The Rapid Programming Algorithm reduces the time required to program the part and guarantees reliable programming. Programming time is typically only 100µs/byte. The Integrated Product Identification Code electronically identifies the device and manufacturer. This feature is used by industry standard programming equipment to select the proper programming algorithms and voltages.

Erasure Characteristics

The entire memory array of the AT27C010/L is erased (all outputs read as V_{OH}) after exposure to ultraviolet light at a wavelength of 2537Å. Complete erasure is assured after a minimum of 20 minutes exposure using 12,000 µW/cm² intensity lamps spaced one inch away from the chip. Minimum erase time for lamps at other intensity ratings can be calculated from the minimum integrated erasure dose of 15W·sec/cm². To prevent unintentional erasure, an opaque label is recommended to cover the clear window on any UV erasable EPROM which will be subjected to continuous fluorescent indoor lighting or sunlight.

Absolute Maximum Ratings*

Temperature Under Bias	-55°C to +125°C
Storage Temperature.....	-65°C to +150°C
Voltage on Any Pin with Respect to Ground.....	-2.0V to +7.0V ⁽¹⁾
Voltage on A9 with Respect to Ground	-2.0V to +14.0V ⁽¹⁾
V _{PP} Supply Voltage with Respect to Ground.....	-2.0V to +14.0V ⁽¹⁾
Integrated UV Erase Dose.....	7258 W·sec/cm ²

*NOTICE: Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Notes:

1. Minimum voltage is -0.6V dc which may undershoot to -2.0V for pulses of less than 20ns. Maximum output pin voltage is $V_{CC}+0.75V$ dc which may overshoot to +7.0V for pulses of less than 20ns.

Operating Modes

MODE \ PIN	\overline{CE}	\overline{OE}	\overline{PGM}	Ai	V _{PP}	V _{CC}	Outputs
Read	V _{IL}	V _{IL}	X ⁽¹⁾	Ai	X	V _{CC}	DOUT
Output Disable	X	V _{IH}	X	X	X	V _{CC}	High Z
Standby	V _{IH}	X	X	X	X	V _{CC}	High Z
Fast Program ⁽²⁾	V _{IL}	V _{IH}	V _{IL}	Ai	V _{PP}	V _{CC}	DIN
PGM Verify	V _{IL}	V _{IL}	V _{IH}	Ai	V _{PP}	V _{CC}	DOUT
PGM Inhibit	V _{IH}	X	X	X	V _{PP}	V _{CC}	High Z
Product Identification ⁽⁴⁾	V _{IL}	V _{IL}	X	A9=V _H ⁽³⁾ A0=V _{IH} or V _{IL} A1-A16=V _{IL}	X	V _{CC}	Identification Code

- Notes: 1. X can be V_{IL} or V_{IH}.
2. Refer to Programming characteristics.
3. V_H = 12.0 ± 0.5V.

4. Two identifier bytes may be selected. All Ai inputs are held low (V_{IL}), except A9 which is set to V_H and A0 which is toggled low (V_{IL}) to select the Manufacturer's Identification byte and high (V_{IH}) to select the Device Code byte.

D.C. and A.C. Operating Conditions for Read Operation

AT27C010 / AT27C010L						
		-12	-15	-17	-20	-25
Operating Temperature (Case)	Com.	0°C - 70°C	0°C - 70°C	0°C - 70°C	0°C - 70°C	0°C - 70°C
	Ind.	-40°C - 85°C	-40°C - 85°C	-40°C - 85°C	-40°C - 85°C	-40°C - 85°C
	Mil.	-55°C - 125°C	-55°C - 125°C	-55°C - 125°C	-55°C - 125°C	-55°C - 125°C
V _{CC} Power Supply		5V ± 10%	5V ± 10%	5V ± 10%	5V ± 10%	5V ± 10%

D.C. and Operating Characteristics for Read Operation

4

Symbol	Parameter	Condition	Min	Max	Units	
I _{LI}	Input Load Current	V _{IN} =-0.1V to V _{CC} +1V		5	μA	
I _{LO}	Output Leakage Current	V _{OUT} =-0.1V to V _{CC} +0.1V		10	μA	
I _{PP1} (2)	V _{PP} (1) Read/Standby Current	V _{PP} =3.8 to V _{CC} +0.3V		10	μA	
I _{SB}	V _{CC} (1) Standby Current	I _{SB1} (CMOS), CĒ=V _{CC} -0.3 to V _{CC} +1.0V		100	μA	
		I _{SB2} (TTL), CĒ=2.0 to V _{CC} +1.0V		1	mA	
I _{CC}	V _{CC} Active Current	f=5MHz, I _{OUT} =0mA, CĒ=V _{IL}	AT27C010L	Com.	25	mA
				Ind.,Mil.	30	mA
			AT27C010	Com.	40	mA
				Ind.,Mil.	50	mA
V _{IL}	Input Low Voltage		-0.6	0.8	V	
V _{IH}	Input High Voltage		2.0	V _{CC} +1	V	
V _{OL}	Output Low Voltage	I _{OL} =2.1mA		.45	V	
V _{OH}	Output High Voltage	I _{OH} =-100μA		V _{CC} -0.3	V	
		I _{OH} =-2.5mA		3.5	V	
		I _{OH} =-400μA		2.4	V	

Notes: 1. V_{CC} must be applied simultaneously or before V_{PP}, and removed simultaneously or after V_{PP}.

2. V_{PP} may be connected directly to V_{CC}, except during programming. The supply current would then be the sum of I_{CC} and I_{PP}.

A.C. Characteristics for Read Operation

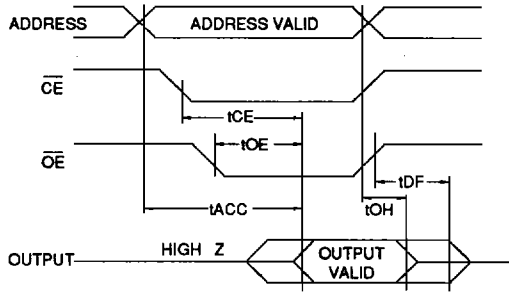
			AT27C010 / AT27C010L										
			-12		-15		-17		-20		-25		Units
Symbol	Parameter	Condition	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	
t _{ACC} (3)	Address to Output Delay	CĒ=OĒ =V _{IL}	Com.	120	150	170	200	250	ns				
			Ind.,Mil.	120	150	170	200	250	ns				
t _{CE} (2)	CĒ to Output Delay	OĒ=V _{IL}	120	150	170	200	250	ns					
t _{OE} (2,3)	OĒ to Output Delay	CĒ=V _{IL}	35	40	65	70	100	ns					
t _{DF} (4,5)	OĒ High to Output Float	CĒ=V _{IL}	50	60	50	55	60	ns					
t _{OH}	Output Hold from Address, CĒ or OĒ, whichever occurred first	CĒ=OĒ =V _{IL}	0	0	0	0	0	ns					

Notes: 2, 3, 4, 5. - see AC Waveforms for Read Operation.





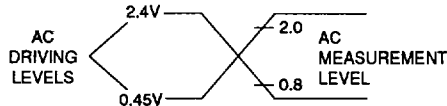
A.C. Waveforms for Read Operation ⁽¹⁾



Notes:

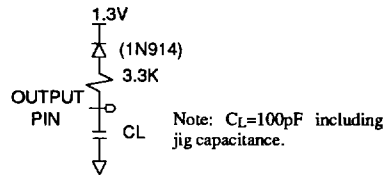
1. Timing measurement references are 0.8V and 2.0V. Input AC driving levels are 0.45V and 2.4V, unless otherwise specified.
2. \overline{OE} may be delayed up to t_{CE-tOE} after the falling edge of \overline{CE} without impact on t_{CE} .
3. \overline{OE} may be delayed up to $t_{ACC-tOE}$ after the address is valid without impact on t_{ACC} .
4. This parameter is only sampled and is not 100% tested.
5. Output float is defined as the point when data is no longer driven.

Input Test Waveforms and Measurement Levels



$t_R, t_F < 20ns$ (10% to 90%)

Output Test Load

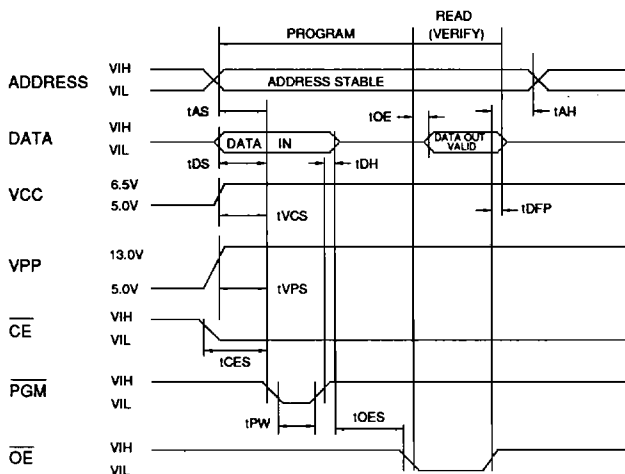


Pin Capacitance ($f=1MHz$ $T=25^\circ C$) ⁽¹⁾

	Typ	Max	Units	Conditions
C_{IN}	4	8	pF	$V_{IN} = 0V$
C_{OUT}	8	12	pF	$V_{OUT} = 0V$

Notes: 1. Typical values for nominal supply voltage. This parameter is only sampled and is not 100% tested.

Programming Waveforms ⁽¹⁾



Notes:

1. The Input Timing Reference is 0.8V for V_{IL} and 2.0V for V_{IH} .
2. t_{OE} and t_{DFP} are characteristics of the device but must be accommodated by the programmer.
3. When programming the AT27C010/L a 0.1 μF capacitor is required across V_{PP} and ground to suppress spurious voltage transients.

D.C. Programming Characteristics

$T_A=25\pm 5^\circ\text{C}$, $V_{CC}=6.5\pm 0.25\text{V}$, $V_{PP}=13.0\pm 0.25\text{V}$

Symbol	Parameter	Test Conditions	Limits		Units
			Min	Max	
I_{LI}	Input Load Current	$V_{IN}=V_{IL}, V_{IH}$	10		μA
V_{IL}	Input Low Level	(All Inputs)	-0.6	0.8	V
V_{IH}	Input High Level		2.0	$V_{CC}+1$	V
V_{OL}	Output Low Volt.	$I_{OL}=2.1\text{mA}$.45		V
V_{OH}	Output High Volt.	$I_{OH}=400\mu\text{A}$	2.4		V
I_{CC2}	V_{CC} Supply Current (Program and Verify)		40		mA
I_{PP2}	V_{PP} Supply Current	$\overline{CE}=\overline{PGM}=V_{IL}$	20		mA
V_{ID}	A9 Product Identification Voltage		11.5	12.5	V

A.C. Programming Characteristics

$T_A=25\pm 5^\circ\text{C}$, $V_{CC}=6.5\pm 0.25\text{V}$, $V_{PP}=13.0\pm 0.25\text{V}$

Symbol	Parameter	Test Conditions* (see Note 1)	Limits		Units
			Min	Max	
t_{AS}	Address Setup Time		2		μs
t_{CES}	\overline{CE} Setup Time		2		μs
t_{OES}	\overline{OE} Setup Time		2		μs
t_{DS}	Data Setup Time		2		μs
t_{AH}	Address Hold Time		0		μs
t_{DH}	Data Hold Time		2		μs
t_{DFP}	\overline{OE} High to Output Float Delay (Note 2)		0	130	ns
t_{VPS}	V_{PP} Setup Time		2		μs
t_{VCS}	V_{CC} Setup Time		2		μs
t_{PW}	PGM Program Pulse Width (Note 3)		95	105	μs
t_{OE}	Data Valid from \overline{OE}			150	ns

***A.C. Conditions of Test:**

- Input Rise and Fall Times (10% to 90%) 20ns
- Input Pulse Levels 0.45V to 2.4V
- Input Timing Reference Level 0.8V to 2.0V
- Output Timing Reference Level 0.8V to 2.0V

Notes:

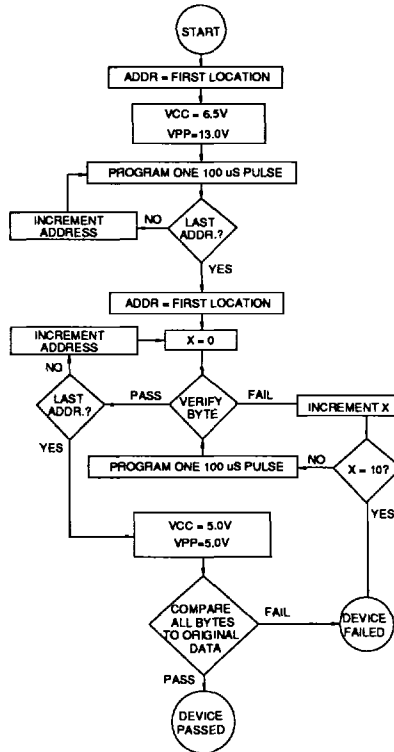
- V_{CC} must be applied simultaneously or before V_{PP} and removed simultaneously or after V_{PP} .
- This parameter is only sampled and is not 100% tested. Output Float is defined as the point where data is no longer driven — see timing diagram.
- Program Pulse width tolerance is $100\mu\text{sec}\pm 5\%$.

Atmel's 27C010/L Integrated Product Identification Code:

Codes	Pins									Hex Data
	A0	O7	O6	O5	O4	O3	O2	O1	O0	
Manufacturer	0	0	0	0	1	1	1	1	0	1E
Device Type	1	0	0	0	0	0	1	0	1	05

Rapid Programming Algorithm

A $100\mu\text{s}$ PGM pulse width is used to program. The address is set to the first location. V_{CC} is raised to 6.5V and V_{PP} is raised to 13.0V. Each address is first programmed with one $100\mu\text{s}$ PGM pulse without verification. Then a verification/reprogramming loop is executed for each address. In the event a byte fails to pass verification, up to 10 successive $100\mu\text{s}$ pulses are applied with a verification after each pulse. If the byte fails to verify after 10 pulses have been applied, the part is considered failed. After the byte verifies properly, the next address is selected until all have been checked. V_{PP} is then lowered to 5.0V and V_{CC} to 5.0V. All bytes are read again and compared with the original data to determine if the device passes or fails.





Ordering Information

t _{acc} (ns)	I _{cc} (mA)		Ordering Code	Package	Operation Range
	Active	Standby			
120	40	0.1	AT27C010-12DC AT27C010-12JC AT27C010-12KC AT27C010-12LC AT27C010-12PC	32DW6 32J 32KW 32LW 32P6	Commercial (0°C to 70°C)
120	50	0.1	AT27C010-12DI AT27C010-12JI AT27C010-12KI AT27C010-12LI AT27C010-12PI	32DW6 32J 32KW 32LW 32P6	Industrial (-40°C to 85°C)
			AT27C010-12DM AT27C010-12KM AT27C010-12LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010-12DM/883 AT27C010-12KM/883 AT27C010-12LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
150	40	0.1	AT27C010-15DC AT27C010-15JC AT27C010-15KC AT27C010-15LC AT27C010-15PC	32DW6 32J 32KW 32LW 32P6	Commercial (0°C to 70°C)
150	50	0.1	AT27C010-15DI AT27C010-15JI AT27C010-15KI AT27C010-15LI AT27C010-15PI	32DW6 32J 32KW 32LW 32P6	Industrial (-40°C to 85°C)
			AT27C010-15DM AT27C010-15KM AT27C010-15LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010-15DM/883 AT27C010-15KM/883 AT27C010-15LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
170	40	0.1	AT27C010-17DC AT27C010-17JC AT27C010-17KC AT27C010-17LC AT27C010-17PC	32DW6 32J 32KW 32LW 32P6	Commercial (0°C to 70°C)
170	50	0.1	AT27C010-17DI AT27C010-17JI AT27C010-17KI AT27C010-17LI AT27C010-17PI	32DW6 32J 32KW 32LW 32P6	Industrial (-40°C to 85°C)
			AT27C010-17DM AT27C010-17KM AT27C010-17LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010-17DM/883 AT27C010-17KM/883 AT27C010-17LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)

Ordering Information

tACC (ns)	Icc (mA)		Ordering Code	Package	Operation Range
	Active	Standby			
200	40	0.1	AT27C010-20DC AT27C010-20JC AT27C010-20KC AT27C010-20LC AT27C010-20PC	32DW6 32J 32KW 32LW 32P6	Commercial (0°C to 70°C)
200	50	0.1	AT27C010-20DI AT27C010-20JI AT27C010-20KI AT27C010-20LI AT27C010-20PI	32DW6 32J 32KW 32LW 32P6	Industrial (-40°C to 85°C)
			AT27C010-20DM AT27C010-20KM AT27C010-20LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010-20DM/883 AT27C010-20KM/883 AT27C010-20LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
250	40	0.1	AT27C010-25DC AT27C010-25JC AT27C010-25KC AT27C010-25LC AT27C010-25PC	32DW6 32J 32KW 32LW 32P6	Commercial (0°C to 70°C)
250	50	0.1	AT27C010-25DI AT27C010-25JI AT27C010-25KI AT27C010-25LI AT27C010-25PI	32DW6 32J 32KW 32LW 32P6	Industrial (-40°C to 85°C)
			AT27C010-25DM AT27C010-25KM AT27C010-25LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010-25DM/883 AT27C010-25KM/883 AT27C010-25LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
170	50	0.1	5962-89614 04 M XX 5962-89614 04 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
200	50	0.1	5962-89614 03 M XX 5962-89614 03 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
250	50	0.1	5962-89614 02 M XX 5962-89614 02 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)
300	50	0.1	5962-89614 01 M XX 5962-89614 01 M YX	32DW6 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)

4





Ordering Information

Package Type	
32DW6	32 Lead, 0.600" Wide, Windowed, Ceramic Dual Inline Package (Cerdip)
32J	32 Lead, Plastic J-Leaded Chip Carrier OTP (PLCC)
32KW	32 Lead, Windowed, Ceramic J-Leaded Chip Carrier (JLCC)
32LW	32 Pad, Windowed, Ceramic Leadless Chip Carrier (LCC)
32P6	32 Lead, 0.600" Wide, Plastic Dual Inline Package OTP (PDIP)

Ordering Information

t _{ACC} (ns)	I _{CC} (mA)		Ordering Code	Package	Operation Range
	Active	Standby			
120	25	0.1	AT27C010L-12DC AT27C010L-12JC AT27C010L-12KC AT27C010L-12LC AT27C010L-12PC	32DW6 32J 32KW 32LW 32P6	Commercial (0°C to 70°C)
120	30	0.1	AT27C010L-12DI AT27C010L-12JI AT27C010L-12KI AT27C010L-12LI AT27C010L-12PI	32DW6 32J 32KW 32LW 32P6	Industrial (-40°C to 85°C)
			AT27C010L-12DM AT27C010L-12KM AT27C010L-12LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010L-12DM/883 AT27C010L-12KM/883 AT27C010L-12LM/883	32DW6 32KW 32LW	Military Class B, Fully Compliant (-55°C to 125°C)
150	25	0.1	AT27C010L-15DC AT27C010L-15JC AT27C010L-15LC AT27C010L-15KC AT27C010L-15PC	32DW6 32J 32LW 32KW 32P6	Commercial (0°C to 70°C)
150	30	0.1	AT27C010L-15DI AT27C010L-15JI AT27C010L-15KI AT27C010L-15LI AT27C010L-15PI	32DW6 32J 32KW 32LW 32P6	Industrial (-40°C to 85°C)
			AT27C010L-15DM AT27C010L-15KM AT27C010L-15LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010L-15DM/883 AT27C010L-15KM/883 AT27C010L-15LM/883	32DW6 32KW 32LW	Military Class B, Fully Compliant (-55°C to 125)
170	25	0.1	AT27C010L-17DC AT27C010L-17JC AT27C010L-17KC AT27C010L-17LC AT27C010L-17PC	32DW6 32J 32KW 32LW 32P6	Commercial (0°C to 70°C)
170	30	0.1	AT27C010L-17DI AT27C010L-17JI AT27C010L-17KI AT27C010L-17LI AT27C010L-17PI	32DW6 32J 32KW 32LW 32P6	Industrial (-40°C to 85°C)
			AT27C010L-17DM AT27C010L-17KM AT27C010L-17LM	32DW6 32KW 32LW	Military (-55°C to 125°C)
			AT27C010L-17DM/883 AT27C010L-17KM/883 AT27C010L-17LM/883	32DW6 32KW 32LW	Military/883C Class B, Fully Compliant (-55°C to 125°C)

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Ordering Information

tacc (ns)	Icc (mA)		Ordering Code	Package	Operation Range	
	Active	Standby				
200	25	0.1	AT27C010L-20DC	32DW6	Commercial (0°C to 70°C)	
			AT27C010L-20JC	32J		
			AT27C010L-20KC	32KW		
			AT27C010L-20LC	32LW		
			AT27C010L-20PC	32P6		
200	30	0.1	AT27C010L-20DI	32DW6	Industrial (-40°C to 85°C)	
			AT27C010L-20JI	32J		
			AT27C010L-20KI	32KW		
		AT27C010L-20LI	32LW	Military (-55°C to 125°C)		
		AT27C010L-20PI	32P6			
		AT27C010L-20DM	32DW6			
		AT27C010L-20KM	32KW	Military/883C Class B, Fully Compliant (-55°C to 125°C)		
		AT27C010L-20LM	32LW			
		AT27C010L-20DM/883	32DW6			
AT27C010L-20KM/883	32KW	Commercial (0°C to 70°C)				
AT27C010L-20LM/883	32LW					
AT27C010L-25DC	32DW6					
250	25	0.1	AT27C010L-25JC	32J	Commercial (0°C to 70°C)	
			AT27C010L-25KC	32KW		
			AT27C010L-25LC	32LW		
			AT27C010L-25PC	32P6		
			AT27C010L-25DI	32DW6		Industrial (-40°C to 85°C)
			AT27C010L-25JI	32J		
			AT27C010L-25KI	32KW		
			AT27C010L-25LI	32LW		Military (-55°C to 125°C)
			AT27C010L-25PI	32P6		
			AT27C010L-25DM	32DW6		
			AT27C010L-25KM	32KW		Military/883C Class B, Fully Compliant (-55°C to 125°C)
			AT27C010L-25LM	32LW		
			AT27C010L-25DM/883	32DW6		
			AT27C010L-25KM/883	32KW		Commercial (0°C to 70°C)
			AT27C010L-25LM/883	32LW		
AT27C010L-25PC	32P6					

Package Type	
32DW6	32 Lead, 0.600" Wide, Windowed, Ceramic Dual Inline Package (Cerdip)
32J	32 Lead, Plastic J-Leaded Chip Carrier OTP (PLCC)
32KW	32 Lead, Windowed, Ceramic J-Leaded Chip Carrier (JLCC)
32LW	32 Pad, Windowed, Ceramic Leadless Chip Carrier (LCC)
32P6	32 Lead, 0.600" Wide, Plastic Dual Inline Package OTP (PDIP)