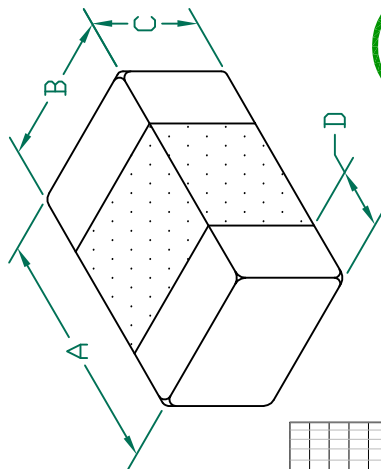


# HI08050121R-10

UNCONTROLLED DOCUMENT

## PHYSICAL DIMENSIONS:

- A 2.00 [.079] + 0.20 [.008]
- B 1.25 [.049] + 0.20 [.008]
- C 0.90 [.035] + 0.20 [.008]
- D 0.51 [.020] + 0.25 [.010]



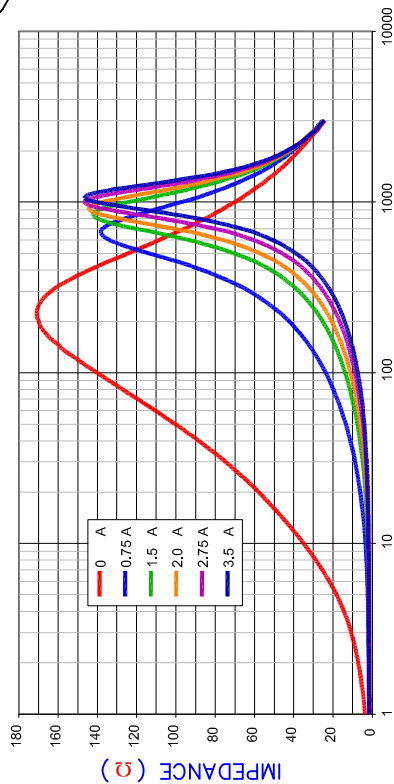
ELECTRICAL CHARACTERISTICS:			
Z @ 100MHz ( $\Omega$ )	DCR ( $\Omega$ )	Rated Current	
Nominal	120		
Minimum	90		
Maximum	150	0.020	3500 mA

NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPE.
2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
3. TERMINATION FINISH IS 100% TIN.
4. OPERATING TEMPERATURE TEMP:  $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$  (INCLUDING SELF-HEATING)

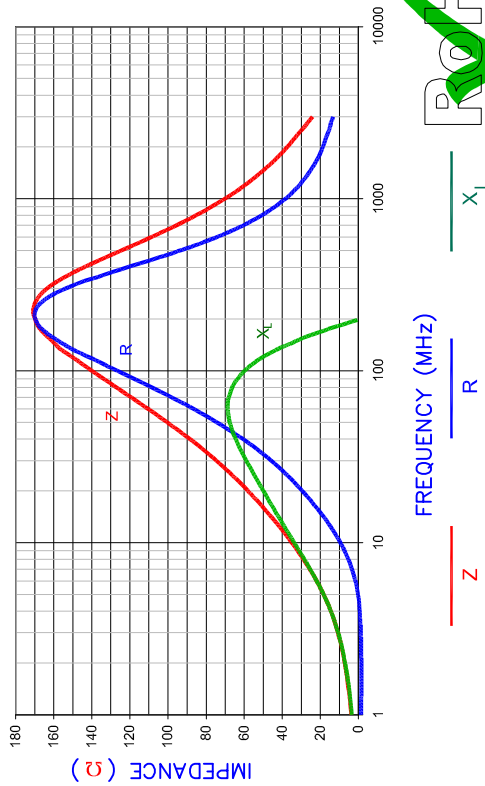


Z vs FREQUENCY  
IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)

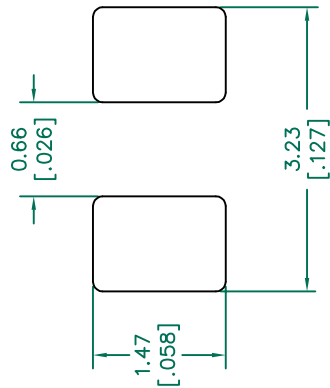
|Z|, R, AND X vs. FREQUENCY



FREQUENCY (MHz)

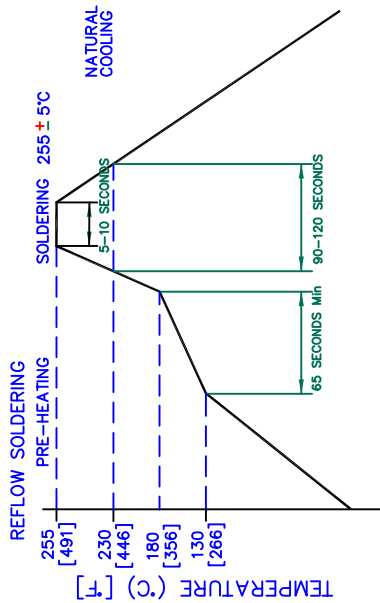
AGILENT E4991A RF Impedance/Material Analyzer  
HP 16194A Test Fixture REF. 5522

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].

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PROJECT/PART NUMBER:		HI08050121R-10	SCALE:	NTS	TOOL #	SHEET: 2 of 2	
DATE:		08/23/04	DATE:	08/23/04	CAD #	HI08050121R-10-F	
DESCRIPTION:		ORIGINAL DRAFT	DATE:	08/23/04	JRK	INT	
OPERATING TEMPERATURE		08/05/13	QU	03/04/10	JUN	DATE	
CHANGE TO PAPER TAPE		UPDATE LAIRD LOGO AND REFLOW CURVE		06/01/09	JRK	REV	
UPDATE COMPANY LOGO & ROHS SYMBOL				01/16/08	JRK	DESCRIPTION	
UPDATE COMPANY LOGO				03/29/06	JRK	ORIGINAL DRAFT	
UPDATE CURVES				08/23/04	JRK	DATE	

