QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS QUICKSWITCH 16-BIT LOW RESISTANCE MULTIWIDTH™ BUS SWITCH

IDTQS32XR245

FEATURES:

- Enhanced N channel FET with no inherent diode to Vcc
- 2.5Ω bidirectional switches connect inputs to outputs
- Zero propagation delay, zero ground bounce
- QS32XR245 is a 16-bit version of QS3R245
- Flow-through pinout for easy layout
- Undershoot clamp diodes on all switch and control inputs
- TTL-compatible control inputs
- Available in 40-pin QVSOP Package

APPLICATIONS

- Low resistance applications
- Hot-swapping and hot-docking (low resistance for PCI and Compact PCI applications)
- Bus switching and isolation
- Voltage translation (5V to 3.3V)
- Capacitance reduction and isolation
- Power conservation
- Logic replacement (data processing)
- Clock gating

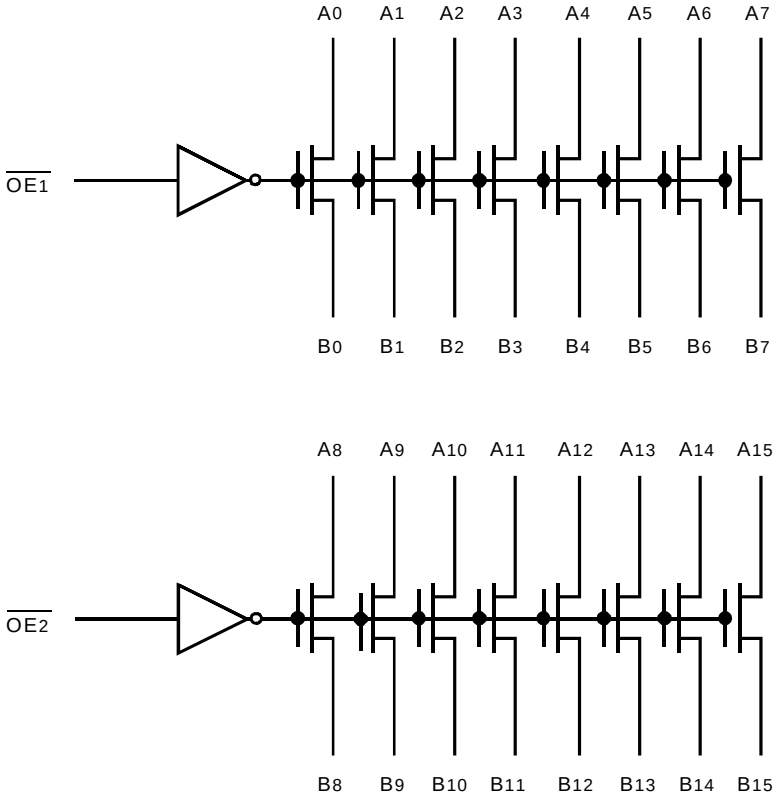
DESCRIPTION:

The QS32XR245 is a member of the MultiWidth™ family of QuickSwitch devices and provides a set of 32 high-speed low resistance CMOS compatible bus switches in a flow-thru pinout. This device is available in the MillipaQ package, the world's first small outline 16-bit solution. The low on-resistance of the QS32XR245 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise. When Output Enable ($\overline{OEn}$) is low, the switches are turned on, connecting bus A to bus B. When $\overline{OEn}$ is high, the switches are turned off. This device is ideally suited for 16/32 bit applications where board space is at a premium. The low resistance of QS32XR245 makes it ideal for PCI hot docking application.

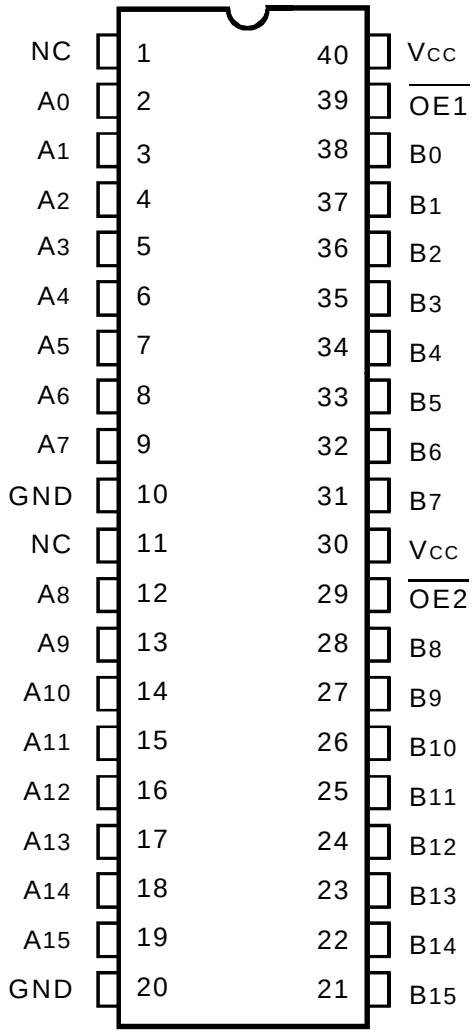
QuickSwitch devices provide an order of magnitude faster speed than conventional logic devices.

The QS32XR245 is characterized for operation at -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



QVSOP
TOP VIEW

ABSOLUTE MAXIMUM RATINGS (1)

Symbol	Description	Max.	Unit
V_{TERM}(2)	Supply Voltage to Ground	– 0.5 to +7	V
V_{TERM}(3)	DC Switch Voltage V_S	– 0.5 to +7	V
V_{TERM}(3)	DC Input Voltage V_{IN}	– 0.5 to +7	V
V_{AC}	AC Input Voltage (pulse width ≤20ns)	-3	V
I_{OUT}	DC Output Current	120	mA
P_{MAX}	Maximum Power Dissipation	.92	W
T_{STG}	Storage Temperature	– 65 to +150	°C

NOTES:
1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. Vcc Terminals.
3. All terminals except Vcc.

CAPACITANCE

(T_A = +25°C, f = 1.0MHz, V_{IN} = 0V, V_{OUT} = 0V)

Pins	Typ.	Max. (1)	Unit
Control Inputs	3	4	pF
Quickswitch Channels (Switch OFF)	5	6	pF

NOTE:
1. This parameter is guaranteed but not production tested.

PIN DESCRIPTION

Pin Names	I/O	Description
$\overline{OE_1}$, $\overline{OE_2}$	I	Output Enable
An	I/O	Bus A
Bn	I/O	Bus B

FUNCTION TABLE(1)

$\overline{OE_1}$	$\overline{OE_2}$	A_0 - A_7	A_8 - A_{15}	Function
H	H	Hi-Z	Hi-Z	Disconnect
L	H	B_0 - B_7	Hi-Z	Connect
H	L	Hi-Z	B_8 - B_{15}	Connect
L	L	B_0 - B_7	B_8 - B_{15}	Connect

NOTE:
1. H = HIGH Voltage Level
   L = LOW Voltage Level
   Z = High-Impedence

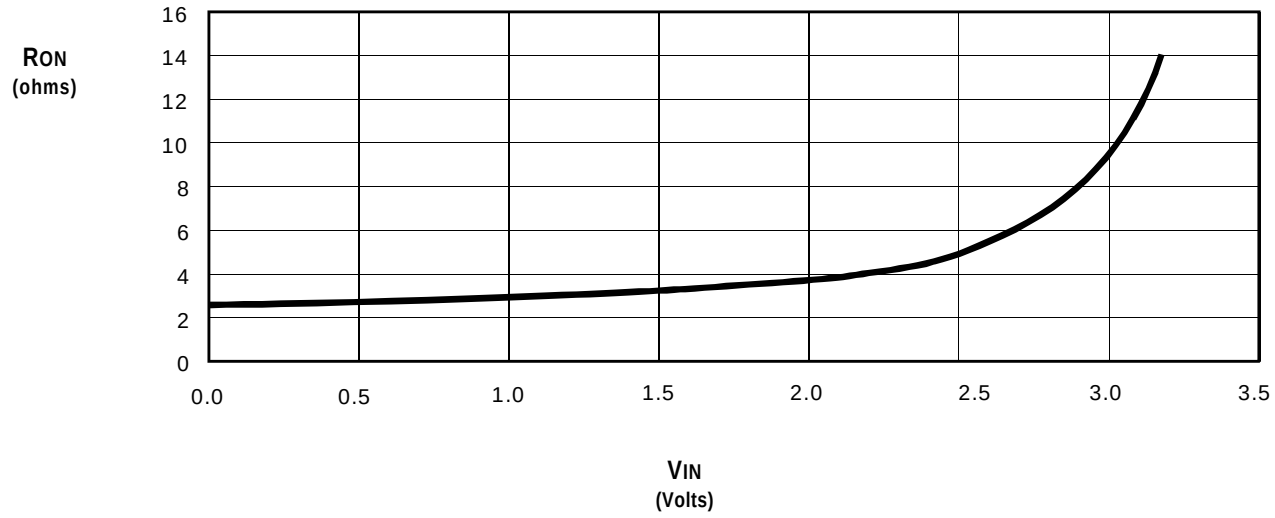
DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:
Industrial: T_A = -40°C to +85°C, V_{CC} = 5.0V ± 10%

Symbol	Parameter	Test Conditions	Min.	Typ.(1)	Max.	Unit
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH for Control Pins	2	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW for Control Pins	—	—	0.8	V
I_{IN}	Input Leakage Current (Control Inputs)	$0V \le V_{IN} \le Vcc$	—	—	±1	µA
I_{OZ}	Off-State Current (Hi-Z)	$0V \le V_{OUT} \le Vcc$, Switches OFF	—	—	±1	µA
R_{ON}	Switch ON Resistance	Vcc = Min., V_{IN} = 0V, I_{ON} = 30mA	—	2.5	5	Ω
R_{ON}	Switch ON Resistance	Vcc = Min., V_{IN} = 2.4V, I_{ON} = 15mA	—	4	8.5	Ω
V_P	Pass Voltage (2)	V_{IN} = Vcc = 5V, I_{OUT} = -5µA	3.7	4	4.3	V

NOTES:
1. Typical values are at V_{CC} = 5.0V, T_A = 25°C.
2. Pass voltage is guaranteed but not production tested.

TYPICAL ON RESISTANCE vs V_{IN} AT V_{CC} = 5V



POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions[1]	Max.	Unit
I_{CCQ}	Quiescent Power Supply Current	V_{CC} = Max., V_{IN} = GND or Vcc, f = 0	6	µA
ΔI_{CC}	Power Supply Current per Control Input HIGH [2]	V_{CC} = Max., V_{IN} = 3.4V, f = 0	2.5	mA
I_{CCD}	Dynamic Power Supply Current per MHz [3]	V_{CC} = Max., A and B pins open Control Input Toggling at 50% Duty Cycle	0.25	mA/MHz

NOTES:
1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
2. Per TLL driven input (V_{IN} = 3.4V, control inputs only). A and B pins do not contribute to ΔI_{CC}.
3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

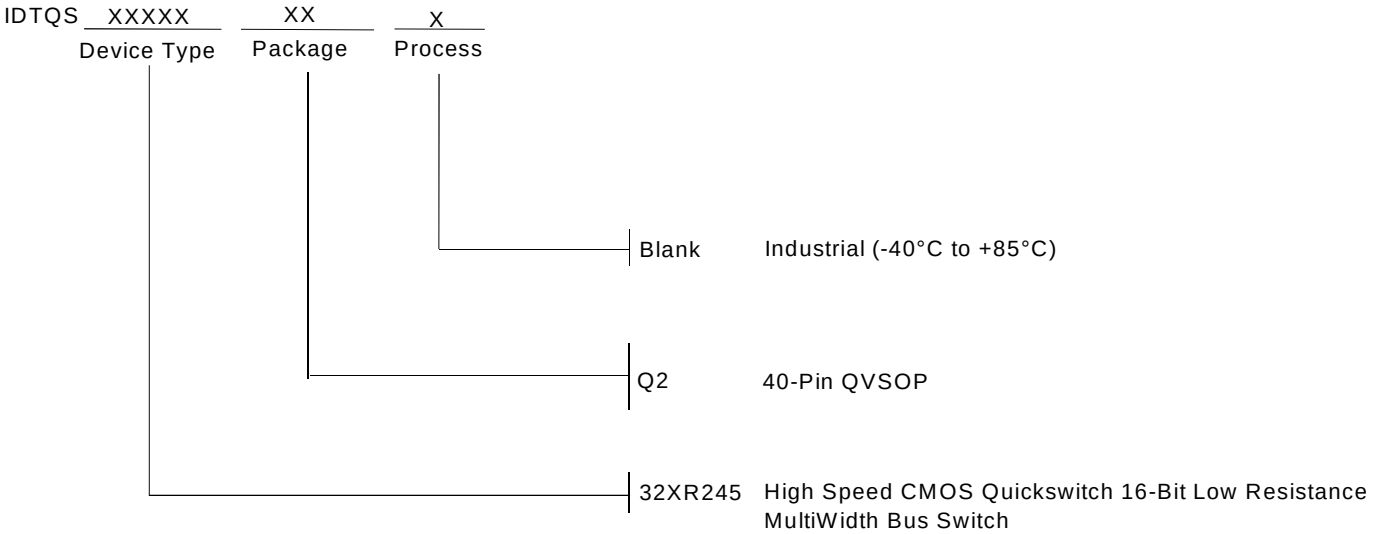
SWITCHING CHARACTERISTICS OVER OPERATING RANGE

T_A = -40°C to +85°C, V_{CC} = 5.0V ± 10%
C_{LOAD} = 50pF, R_{LOAD} = 500Ω unless otherwise noted.

Symbol	Parameter	Min. [1]	Typ.	Max.	Unit
t_{PLH} t_{PHL}	Data Propagation Delay [2,3] An to Bn, Bn to An	—	—	0.12	ns
t_{PZH} t_{PZL}	Switch Turn-on Delay $\overline{OE}$ to An/Bn	0.5	—	5.6	ns
t_{PHZ} t_{PLZ}	Switch Turn-off Delay [2] $\overline{OE}$ to An/Bn	0.5	—	4.5	ns

NOTES:
1. Minimums are guaranteed but not production tested.
2. This parameter is guaranteed but not production tested.
3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.12ns for C_L = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION



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