

Complete Data Sheet available via web, Harris' home page: <http://www.semi.harris.com> or via Harris AnswerFAX, see Section 17

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Dual SPDT CMOS Analog Switch

Features

- See IH504X and IH514X for Other Functions
- Dual SPDT
- Switches Greater than 20V_{p-p} Signals with ±15V Supplies
- Quiescent Current Less than 1mA
- Break-Before-Make Switching t_{OFF} 200ns, t_{ON} 300ns (Typ)
- TTL, DTL, CMOS, PMOS Compatible

Ordering Information

PART NUMBER	TEMP. RANGE (°C)	PACKAGE	PKG. NO.
IH5043MJE	-55 to 125	16 Ld CERDIP	F16.3
IH5043CJE	0 to 70	16 Ld CERDIP	F16.3
IH5043CPE	0 to 70	16 Ld PDIP	E16.3
IH5043CY	0 to 70	16 Ld SOIC	M16.15
IH5043MJE/883B	-55 to 125	16 Ld CERDIP	F16.3

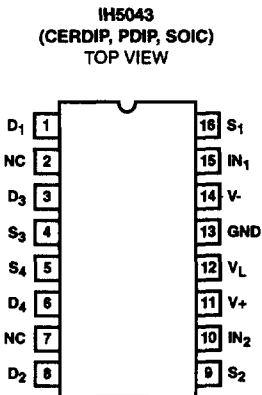
Description

The IH5043 analog switch uses an improved, high voltage CMOS monolithic technology. These devices provide ease of use and performance advantages not previously available from solid state switches.

Key performance advantage is TTL compatibility and ultra low power operation. The quiescent current requirement is less than 1mA. Also, the IH5043 guarantees Break-Before-Make switching, accomplished by extending the t_{ON} time (300ns Typ), so that it exceeds t_{OFF} time (200ns Typ). This insures that an ON channel will be turned OFF before an OFF channel can turn ON. The need for external logic required to avoid channel to channel shorting during switching is eliminated.

The IH5043 is a pin-for-pin, improved performance replacement for other analog switches.

Pinout



Functional Block Diagram

FUNCTIONAL DRIVER, TYPICAL DRIVER, GATE ($1/2$ AS SHOWN)

